**Supermicro**

SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

**CPU2017 License:** 001176

**Test Sponsor:** Supermicro

**Tested by:** Supermicro

**Test Date:** Nov-2018

**Hardware Availability:** Nov-2018

**Software Availability:** Mar-2018

### SPECrate2017_fp_base = 37.5

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>12</td>
<td>38.1</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>12</td>
<td>34.7</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>12</td>
<td>19.5</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>12</td>
<td>52.8</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>12</td>
<td>61.6</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>12</td>
<td>49.7</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>12</td>
<td>45.8</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>12</td>
<td>32.8</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>12</td>
<td>49.7</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>12</td>
<td>115</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>12</td>
<td>80.7</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>12</td>
<td>21.4</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>12</td>
<td>12.3</td>
</tr>
</tbody>
</table>

---

**Hardware**

- **CPU Name:** Intel Xeon E-2176G
- **Max MHz.:** 4700
- **Nominal:** 3700
- **Enabled:** 6 cores, 1 chip, 2 threads/core
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 256 KB I+D on chip per core
- **L3:** 12 MB I+D on chip per chip
- **Other:** None
- **Memory:** 32 GB (2 x 16 GB 2Rx8 PC4-2666V-E)
- **Storage:** 1 x 512 GB NVMe SSD
- **Other:** None

---

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)
- **Kernel:** 4.4.114-94.11-default
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
- **Parallel:** No
- **Firmware:** Supermicro BIOS version 1.0 released Sep-2018
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
**Supermicro**  
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

---

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Nov-2018  
**Hardware Availability:** Nov-2018  
**Software Availability:** Mar-2018

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>12</td>
<td>1645</td>
<td>73.2</td>
<td>1686</td>
<td>71.4</td>
<td>1690</td>
<td>71.2</td>
<td>1686</td>
<td>71.4</td>
<td>1690</td>
<td>71.2</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>12</td>
<td>387</td>
<td>39.3</td>
<td>393</td>
<td>38.7</td>
<td>395</td>
<td>38.5</td>
<td>393</td>
<td>38.7</td>
<td>395</td>
<td>38.5</td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>12</td>
<td>327</td>
<td>34.9</td>
<td>331</td>
<td>34.4</td>
<td>329</td>
<td>34.7</td>
<td>329</td>
<td>34.7</td>
<td>329</td>
<td>34.7</td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>12</td>
<td>1612</td>
<td>19.5</td>
<td>1627</td>
<td>19.3</td>
<td><strong>1613</strong></td>
<td><strong>19.5</strong></td>
<td>1627</td>
<td>19.3</td>
<td><strong>1613</strong></td>
<td><strong>19.5</strong></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>12</td>
<td><strong>530</strong></td>
<td><strong>52.8</strong></td>
<td>532</td>
<td>52.7</td>
<td>529</td>
<td>53.0</td>
<td>529</td>
<td>53.0</td>
<td>529</td>
<td>53.0</td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>12</td>
<td>761</td>
<td>16.6</td>
<td>766</td>
<td>16.5</td>
<td>768</td>
<td>16.5</td>
<td>765</td>
<td>16.5</td>
<td>764</td>
<td>16.6</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>12</td>
<td>823</td>
<td>32.7</td>
<td>824</td>
<td>32.6</td>
<td>824</td>
<td>32.6</td>
<td>819</td>
<td>32.8</td>
<td>819</td>
<td>32.8</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>12</td>
<td>368</td>
<td>49.7</td>
<td>368</td>
<td>49.7</td>
<td>369</td>
<td>49.6</td>
<td>368</td>
<td>49.7</td>
<td>369</td>
<td>49.6</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>12</td>
<td>456</td>
<td>46.0</td>
<td>458</td>
<td>45.8</td>
<td>459</td>
<td>45.7</td>
<td>458</td>
<td>45.8</td>
<td>459</td>
<td>45.7</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>12</td>
<td>260</td>
<td>115</td>
<td>260</td>
<td><strong>115</strong></td>
<td>260</td>
<td>115</td>
<td>260</td>
<td><strong>115</strong></td>
<td>260</td>
<td>115</td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>12</td>
<td>250</td>
<td>80.7</td>
<td>250</td>
<td>80.8</td>
<td><strong>250</strong></td>
<td><strong>80.7</strong></td>
<td>250</td>
<td>80.8</td>
<td><strong>250</strong></td>
<td><strong>80.7</strong></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>12</td>
<td><strong>2184</strong></td>
<td><strong>21.4</strong></td>
<td>2184</td>
<td>21.4</td>
<td>2188</td>
<td>21.4</td>
<td>2184</td>
<td>21.4</td>
<td>2188</td>
<td>21.4</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>12</td>
<td>1555</td>
<td>12.3</td>
<td>1550</td>
<td>12.3</td>
<td><strong>1554</strong></td>
<td><strong>12.3</strong></td>
<td>1516</td>
<td>12.6</td>
<td>1516</td>
<td>12.6</td>
<td><strong>1516</strong></td>
</tr>
</tbody>
</table>

---

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH="/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default

Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
```
sync; echo 3>/proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

SPECrate2017_fp_base = 37.5
SPECrate2017_fp_peak = 38.1

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2018
Hardware Availability: Nov-2018
Software Availability: Mar-2018

General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-01g1 Thu Nov 1 08:20:57 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) E-2176G CPU @ 3.70GHz
  1 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
  siblings : 12
  physical 0: cores 0 1 2 3 4 5

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 2
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2176G CPU @ 3.70GHz
Stepping: 10
CPU MHz: 4288.675
CPU max MHz: 4500.0000
CPU min MHz: 800.0000
BogoMIPS: 7391.96
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K

(Continued on next page)
Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

SPEC CPU2017 Floating Point Rate Result

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| SPECrate2017_fp_base | 37.5 |
| SPECrate2017_fp_peak | 38.1 |

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2018
Hardware Availability: Nov-2018
Software Availability: Mar-2018

Platform Notes (Continued)

| L2 cache: 256K |
| L3 cache: 12288K |
| NUMA node0 CPU(s): 0-11 |
| Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xptr pdcm pccid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm hwp hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retprobe kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1 |

/proc/cpuinfo cache data
    cache size: 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
    available: 1 nodes (0)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
    node 0 size: 32087 MB
    node 0 free: 20642 MB
    node distances:
    node 0
    0: 10

From /proc/meminfo
    MemTotal: 32857712 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 3
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
        os-release:
            NAME="SLES"
            VERSION="12-SP3"
            VERSION_ID="12.3"
            PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
            ID="sles"
            ANSI_COLOR="0;32"
            CPE_NAME="cpe:/o:suse:sles:12:sp3"

(Continued on next page)
Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

SPECrate2017_fp_base = 37.5
SPECrate2017_fp_peak = 38.1

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Nov-2018
Tested by: Supermicro
Hardware Availability: Nov-2018
Software Availability: Mar-2018

Platform Notes (Continued)

uname -a:
    Linux linux-0lg1 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Nov 1 00:39

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p4 xfs 435G 32G 404G 8% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0 09/14/2018
Memory:
    2x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
==============================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base, peak)
==============================================================================
icpc (ICC) 18.0.2 20180210
(Continued on next page)
Supercruption
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

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CPU2017 License: 001176
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Compiler Version Notes (Continued)

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(peak)
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(base) 526.blender_r(base, peak)
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(base, peak)
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
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(Continued on next page)
Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

**SPEC CPU2017 Floating Point Rate Result**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_peak</th>
<th>SPECrate2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>38.1</td>
<td>37.5</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176

**Test Sponsor:** Supermicro

**Test Date:** Nov-2018

**Hardware Availability:** Nov-2018

**Tested by:** Supermicro

**Software Availability:** Mar-2018

---

**Compiler Version Notes (Continued)**

```plaintext
FC  554.roms_r(peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  521.wrf_r(peak) 527.cam4_r(peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

---

**Base Compiler Invocation**

C benchmarks:
```plaintext
icc -m64 -std=c11
```

C++ benchmarks:
```plaintext
icpc -m64
```

Fortran benchmarks:
```plaintext
ifort -m64
```

Benchmarks using both Fortran and C:
```plaintext
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```plaintext
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```plaintext
icpc -m64 icc -m64 -std=c11 ifort -m64
```
Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

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CPU2017 License: 001176
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Test Date: Nov-2018
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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 37.5
SPECrate2017_fp_peak = 38.1

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Supermicro
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)

SPECrate2017_fp_base = 37.5
SPECrate2017_fp_peak = 38.1

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Nov-2018
Hardware Availability: Nov-2018

Tested by: Supermicro
Software Availability: Mar-2018

Peak Optimization Flags (Continued)

510.parest_r: basepeak = yes

Fortran benchmarks:
503.bwaves_r: basepeak = yes
549.fotonik3d_r: basepeak = yes
554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
527.cam4_r: basepeak = yes

Benchmarks using both C and C++:
511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml
<table>
<thead>
<tr>
<th>SPEC CPU2017 Floating Point Rate Result</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Supermicro</strong></td>
</tr>
<tr>
<td>SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2176G)</td>
</tr>
<tr>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 001176</th>
<th>Test Date: Nov-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Supermicro</td>
<td>Hardware Availability: Nov-2018</td>
</tr>
<tr>
<td>Tested by: Supermicro</td>
<td>Software Availability: Mar-2018</td>
</tr>
</tbody>
</table>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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