**SPEC® CPU2017 Integer Rate Result**

**Huawei**

**Huawei XH628 V5 (Intel Xeon Gold 6136)**

**SPECrate2017_int_base = 150**

**SPECrate2017_int_peak = 158**

<table>
<thead>
<tr>
<th></th>
<th>Huawei XH628 V5 (Intel Xeon Gold 6136)</th>
<th></th>
</tr>
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<tbody>
<tr>
<td><strong>CPU2017 License:</strong></td>
<td>3175</td>
<td></td>
</tr>
<tr>
<td><strong>Test Sponsor:</strong></td>
<td>Huawei</td>
<td></td>
</tr>
<tr>
<td><strong>Tested by:</strong></td>
<td>Huawei</td>
<td></td>
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<tr>
<td><strong>Test Date:</strong></td>
<td>Sep-2018</td>
<td></td>
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<tr>
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<td>Aug-2018</td>
<td></td>
</tr>
<tr>
<td><strong>Software Availability:</strong></td>
<td>Mar-2018</td>
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**Copies**

<table>
<thead>
<tr>
<th>Test</th>
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<tr>
<td>500.perlbench_r</td>
<td>48</td>
</tr>
<tr>
<td>502.gcc_r</td>
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</tr>
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<td>520.omnetpp_r</td>
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<tr>
<td>523.xalancbmk_r</td>
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<td>541.leela_r</td>
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<tr>
<td>548.exchange2_r</td>
<td>48</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>48</td>
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</table>

**Hardware**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Details</th>
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<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon Gold 6136</td>
</tr>
<tr>
<td><strong>Max MHz.:</strong></td>
<td>3700</td>
</tr>
<tr>
<td><strong>Nominal:</strong></td>
<td>3000</td>
</tr>
<tr>
<td><strong>Enabled:</strong></td>
<td>24 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td><strong>Orderable:</strong></td>
<td>1.2 chips</td>
</tr>
<tr>
<td><strong>Cache L1:</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>L2:</strong></td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td><strong>L3:</strong></td>
<td>24.75 MB I+D on chip per chip</td>
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<tr>
<td><strong>Other:</strong></td>
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<tr>
<td><strong>Memory:</strong></td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)</td>
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<tr>
<td><strong>Storage:</strong></td>
<td>1 x 1800 GB SAS, 10000 RPM</td>
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<tr>
<td><strong>Other:</strong></td>
<td>None</td>
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**Software**

<table>
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<th>Details</th>
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</thead>
<tbody>
<tr>
<td><strong>OS:</strong></td>
<td>Red Hat Enterprise Linux Server release 7.4 (Maipo) 3.10.0-693.11.6.el7.x86_64</td>
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<tr>
<td><strong>Compiler:</strong></td>
<td>C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux</td>
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<tr>
<td><strong>Parallel:</strong></td>
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<tr>
<td><strong>Firmware:</strong></td>
<td>Version 0.86 Released Aug-2018</td>
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<td><strong>File System:</strong></td>
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<td><strong>System State:</strong></td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td><strong>Base Pointers:</strong></td>
<td>64-bit</td>
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<tr>
<td><strong>Peak Pointers:</strong></td>
<td>32/64-bit</td>
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<tr>
<td><strong>Other:</strong></td>
<td>jemalloc memory allocator V5.0.1</td>
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</table>
### SPEC CPU2017 Integer Rate Result

**Huawei**

**Huawei XH628 V5 (Intel Xeon Gold 6136)**

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<td>Software Availability: Mar-2018</td>
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**Specrate2017_int_base = 150**

**Specrate2017_int_peak = 158**

---

#### Results Table

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<th>Seconds</th>
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<th>Seconds</th>
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<td>548.exchange2_r</td>
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<td>480</td>
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<td>515</td>
<td>101</td>
</tr>
</tbody>
</table>

**Specrate2017_int_base = 150**

**Specrate2017_int_peak = 158**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:

```
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3>|/proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

**Yes:** The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

**Yes:** The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

**Yes:** The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
Huawei

Huawei XH628 V5 (Intel Xeon Gold 6136)

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CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei  
Hardware Availability: Aug-2018  
Software Availability: Mar-2018  
Test Date: Sep-2018

General Notes (Continued)

jemalloc, a general purpose malloc implementation  
built with the REDHat Enterprise 7.5, and the system compiler gcc 4.8.5  

Platform Notes

BIOS configuration:
Power Policy Set to Performance  
SNC Set to Enabled  
IMC Interleaving Set to 1-way Interleave  
XPT Prefetch Set to Enabled  
Sysinfo program /spec2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bce091c0f  
running on localhost.localdomain Tue Sep 4 13:24:17 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name: Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz  
  2 "physical id"s (chips)  
  48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores: 12  
siblings: 24  
physical 0: cores 0 3 4 5 6 7 16 18 19 20 21 22  
physical 1: cores 0 1 2 3 8 9 10 11 18 19 24 27

From lscpu:  
Architecture: x86_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 48  
On-line CPU(s) list: 0-47  
Thread(s) per core: 2  
Core(s) per socket: 12  
Socket(s): 2  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz  
Stepping: 4  
CPU MHz: 3000.000

(Continued on next page)
Huawei

Huawei XH628 V5 (Intel Xeon Gold 6136)

SPECrate2017_int_base = 150
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CPU2017 License: 3175
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Platform Notes (Continued)

BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-2, 6-8, 24-26, 30-32
NUMA node1 CPU(s): 3-5, 9-11, 27-29, 33-35
NUMA node2 CPU(s): 12-14, 16, 17, 22, 36-38, 40, 41, 46
NUMA node3 CPU(s): 15, 18-21, 23, 39, 42-45, 47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pcd pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ervisor invpcid rtm cqm mpx rdt_a avx512if rdseed adx smap clflushopt clwb avx512fd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pin pts

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 6 7 8 24 25 26 30 31 32
node 0 size: 96437 MB
node 0 free: 93837 MB
node 1 cpus: 3 4 5 9 10 11 27 28 29 33 34 35
node 1 size: 98304 MB
node 1 free: 96015 MB
node 2 cpus: 12 13 14 16 17 22 36 37 38 40 41 46
node 2 size: 98304 MB
node 2 free: 96040 MB
node 3 cpus: 15 18 19 20 21 23 39 42 43 44 45 47
node 3 size: 98304 MB
node 3 free: 95596 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo

(Continued on next page)
Huawei
Huawei XH628 V5 (Intel Xeon Gold 6136)

SPECrate2017_int_base = 150
SPECrate2017_int_peak = 158

Platform Notes (Continued)

MemTotal: 394174376 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
  Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Sep 4 13:22

SPEC is set to: /spec2017

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   553G  8.2G  545G  2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS INSYDE Corp. 0.86 08/06/2018
  Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
      557.xz_r(base)
==============================================================================
icc (ICC) 18.0.2 20180210

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

**Huawei**

Huawei XH628 V5 (Intel Xeon Gold 6136)

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<td>Huawei</td>
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**SPECrate2017_int_base = 150**

**SPECrate2017_int_peak = 158**

### Compiler Version Notes (Continued)

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
============================================
CC  500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
    557.xz_r(peak)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
    541.leela_r(base)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
    541.leela_r(peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
FC  548.exchange2_r(base)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
FC  548.exchange2_r(peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

(Continued on next page)
Huawei
Huawei XH628 V5 (Intel Xeon Gold 6136)

**SPEC CPU2017 Integer Rate Result**

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---

### Base Compiler Invocation (Continued)

**C++ benchmarks:**

```bash
icpc -m64
```

**Fortran benchmarks:**

```bash
ifort -m64
```

---

### Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -DSPEC_LP64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

---

### Base Optimization Flags

**C benchmarks:**

```bash
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**C++ benchmarks:**

```bash
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**Fortran benchmarks:**

```bash
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

---

### Peak Compiler Invocation

**C benchmarks (except as noted below):**

```bash
icc -m64 -std=c11
```

---

(Continued on next page)
Peak Compiler Invocation (Continued)

502.gcc_r.icc -m32 -std=c11 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r.icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalanchmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: basepeak = yes
525.x264_r: basepeak = yes
557.xz_r: basepeak = yes

(Continued on next page)
Huawei

Huawei XH628 V5 (Intel Xeon Gold 6136)

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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

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Peer Optimization Flags (Continued)

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: basepeak = yes

541.leela_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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Tested with SPEC CPU2017 v1.0.2 on 2018-09-04 09:24:16-0400.