Huawei

Huawei CH225 V5 (Intel Xeon Gold 6146)  

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
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<tbody>
<tr>
<td>9.80</td>
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</tr>
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</table>

**CPU2017 License:** 3175  
**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2018

<table>
<thead>
<tr>
<th>Thread</th>
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<td>x264_s</td>
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<tr>
<td>deepsjeng_s</td>
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<td>leela_s</td>
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**Threads**

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</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 6146  
- **Max MHz.:** 4200  
- **Nominal:** 3200  
- **Enabled:** 24 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None  

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.3 (Maipo)  
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++  
- **Compiler for Linux:** Fortran: Version 18.0.2.199 of Intel Fortran  
- **Compiler for Linux:**  
- **Parallel:** Yes  
- **Firmware:** Version 0.80 Released Jun-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1
**SPEC CPU2017 Integer Speed Result**

**Huawei**

Huawei CH225 V5 (Intel Xeon Gold 6146)

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**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Test Date:** Oct-2018  
**Hardware Availability:** Jul-2017  
**Tested by:** Huawei  
**Software Availability:** Mar-2018

### Results Table

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<td>23.4</td>
<td>262</td>
<td>23.6</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Results appear in the order in which they were run. Bold underlined text indicates a median measurement.**

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

- sync; echo 3> /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Oct-2018
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Mar-2018

Platform Notes

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Wed Oct 17 07:41:14 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 3 4 5 6 7 16 18 19 20 21 22
physical 1: cores 0 2 3 4 9 10 11 16 17 18 24 26

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 24
On-line CPU(s) list: 0-23
Thread(s) per core: 1
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
Stepping: 4
CPU MHz: 3201.000
BogoMIPS: 6406.09
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23

(Continued on next page)
Huawei CH225 V5 (Intel Xeon Gold 6146)

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CPU2017 License: 3175  
Test Date: Oct-2018  
Test Sponsor: Huawei  
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Platform Notes (Continued)

/proc/cpuinfo cache data
   cache size : 25344 KB

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
   node 0 size: 194737 MB
   node 0 free: 189629 MB
   node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
   node 1 size: 196608 MB
   node 1 free: 192097 MB
   node distances:
     node 0   1
     0:  10  21
     1:  21  10

From /proc/meminfo
   MemTotal: 394169164 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
   os-release:
     NAME="Red Hat Enterprise Linux Server"
     VERSION="7.3 (Maipo)"
     ID="rhel"
     ID_LIKE="fedora"
     VERSION_ID="7.3"
     PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
     ANSI_COLOR="0;31"
     CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
   Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 17 07:40

SPEC is set to: /spec2017
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/sda4 xfs 400G 11G 390G 3% /

(Continued on next page)
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Huawei CH225 V5 (Intel Xeon Gold 6146)

\[
\text{SPECspeed2017\_int\_base} = 9.80 \\
\text{SPECspeed2017\_int\_peak} = 10.2
\]

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Oct-2018

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Software Availability: Mar-2018

Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.80 06/27/2018
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
\text{CC} \ 600.\text{perlbench\_s(base)} \ 602.\text{gcc\_s(base)} \ 605.\text{mcf\_s(base)} \ 625.\text{x264\_s(base, peak)} \ 657.\text{xz\_s(base)}
==============================================================================
\text{icc (ICC) 18.0.2 20180210}
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
\text{CC} \ 600.\text{perlbench\_s(peak)} \ 602.\text{gcc\_s(peak)} \ 605.\text{mcf\_s(peak)} \ 657.\text{xz\_s(peak)}
==============================================================================
\text{icc (ICC) 18.0.2 20180210}
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
\text{CXXC} \ 620.\text{omnetpp\_s(base)} \ 623.\text{xalancbmk\_s(base)} \ 631.\text{deepsjeng\_s(base)} \ 641.\text{leela\_s(base)}
==============================================================================
\text{icpc (ICC) 18.0.2 20180210}
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
\text{CXXC} \ 620.\text{omnetpp\_s(peak)} \ 623.\text{xalancbmk\_s(peak)} \ 631.\text{deepsjeng\_s(peak)} \ 641.\text{leela\_s(peak)}
==============================================================================
\text{icpc (ICC) 18.0.2 20180210}
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
\text{FC} \ 648.\text{exchange2\_s(base, peak)}
==============================================================================

(Continued on next page)
Huawei CH225 V5 (Intel Xeon Gold 6146)

Huawei

SPEC CPU2017 Integer Speed Result

SPECspeed2017_int_peak = 10.2
SPECspeed2017_int_base = 9.80

CPU2017 License: 3175
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Test Date: Oct-2018
Tested by: Huawei
Software Availability: Mar-2018

Compiler Version Notes (Continued)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
  icc -m64 -std=c11

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
  -W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
  -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
  -W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
  -W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

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Huawei CH225 V5 (Intel Xeon Gold 6146)

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SPECspeed2017_int_peak = 10.2

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Tested by: Huawei

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks (except as noted below):
icpc -m64

623.xalancbmk_s: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -O3
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -openmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

(Continued on next page)
Peak Optimization Flags (Continued)

602.gcc_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -O3
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-\L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

-Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-\L/usr/local/je5.0.1-64/lib -ljemalloc

The flags files that were used to format this result can be browsed at

<table>
<thead>
<tr>
<th>Huawei CH225 V5 (Intel Xeon Gold 6146)</th>
<th>SPECspeed2017_int_base = 9.80</th>
<th>SPECspeed2017_int_peak = 10.2</th>
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<td>Test Date: Oct-2018</td>
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<td>Tested by: Huawei</td>
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You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-10-17 07:41:13-0400.