# SPEC® CPU2017 Integer Speed Result

## Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6134, 3.20GHz)

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s 16</td>
<td>7.20</td>
<td>9.00</td>
</tr>
<tr>
<td>602.gcc_s 16</td>
<td>9.29</td>
<td>9.48</td>
</tr>
<tr>
<td>605.mcf_s 16</td>
<td>10.9</td>
<td>11.1</td>
</tr>
<tr>
<td>620.omnetpp_s 16</td>
<td>6.13</td>
<td>6.45</td>
</tr>
<tr>
<td>623.xalancbmk_s 16</td>
<td>9.51</td>
<td>10.1</td>
</tr>
<tr>
<td>625.x264_s 16</td>
<td>11.8</td>
<td>11.8</td>
</tr>
<tr>
<td>631.deepsjeng_s 16</td>
<td>5.04</td>
<td>5.01</td>
</tr>
<tr>
<td>641.leela_s 16</td>
<td>4.33</td>
<td>4.35</td>
</tr>
<tr>
<td>648.exchange2_s 16</td>
<td>13.4</td>
<td>13.4</td>
</tr>
<tr>
<td>657.xz_s 16</td>
<td>19.9</td>
<td>20.0</td>
</tr>
</tbody>
</table>

### Software

- OS: SUSE Linux Enterprise Server 12 SP3 4.4.114-94.11-default
- Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- Parallel: Yes
- Firmware: Version 0.3.12 released Feb-2018
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: 32/64-bit
- Other: jemalloc memory allocator library V5.0.1

### Hardware

- CPU Name: Intel Xeon Gold 6134
- Max MHz.: 3700
- Nominal: 3200
- Enabled: 16 cores, 2 chips
- Orderable: 1,2 chips
- Cache L1: 32 KB I + 32 KB D on chip per core
- L2: 1 MB I+D on chip per core
- L3: 24.75 MB I+D on chip per chip
- Other: None
- Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)
- Storage: 960 GB SAS SSD
- Other: None
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**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**CPU2017 License:** 55  
**Test Date:** Mar-2018  
**Hardware Availability:** Sep-2018  
**Software Availability:** Feb-2018

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>285</td>
<td>5.04</td>
<td>284</td>
<td>5.04</td>
<td>16</td>
<td>286</td>
<td>5.01</td>
<td>285</td>
<td>5.02</td>
<td>286</td>
<td>5.01</td>
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<td>16</td>
<td>393</td>
<td>4.34</td>
<td>395</td>
<td>4.32</td>
<td>394</td>
<td>4.33</td>
<td>16</td>
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<td>222</td>
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<td>220</td>
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<td>16</td>
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<td>306</td>
<td>20.2</td>
<td>308</td>
<td>20.0</td>
<td>309</td>
<td>20.0</td>
</tr>
</tbody>
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**SPECspeed2017_int_base = 8.70**  
**SPECspeed2017_int_peak = 9.00**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
- sync; echo 3> /proc/sys/vm/drop_caches

---

Page 2  
Standard Performance Evaluation Corporation (info@spec.org)  
https://www.spec.org/
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Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6134, 3.20GHz)  SPECspeed2017_int_base = 8.70

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Mar-2018
Tested by: Dell Inc.
Hardware Availability: Sep-2018
Software Availability: Feb-2018

Platform Notes

BIOS settings:
Sub NUMA Cluster Disabled
Virtualization Technology Disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1EE Disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub Disabled
Logical Processor Disabled
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-kuth Wed Mar 14 08:42:20 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 2 3 9 16 19 26 27
physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
Stepping: 4

(Continued on next page)
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Test Date: Mar-2018

Platform Notes (Continued)

CPU MHz: 3192.472
BogoMIPS: 6384.94
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15
Flags: fpu vme de pse tsc mtrr ps xsave cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref perf nofpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpre pdcm pclid dcasse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_cxsw spec_ctrl retpoline kaiser tpr_shadow vmmi flexpriority
ep tpd fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx
avx512f avx512dq rdseed adx clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsaves cxfertb1 cqm_llc cqm_occup_llc pku ospke

/proc/cpuinfo cache data
cache size : 25344 KB

WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14
node 0 size: 95354 MB
node 0 free: 94621 MB
node 1 cpus: 1 3 5 7 9 11 13 15
node 1 size: 96749 MB
node 1 free: 96317 MB

From /proc/meminfo
MemTotal: 196715324 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

SUSE Linux Enterprise Server 12 SP3

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Platform Notes (Continued)

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
 NAME="SLES"
 VERSION="12-SP3"
 VERSION_ID="12.3"
 PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
 ID="sles"
 ANSI_COLOR="0;32"
 CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
 Linux linux-kuth 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 22 00:32

SPEC is set to: /root/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>xfs</td>
<td>890G</td>
<td>16G</td>
<td>874G</td>
<td>2%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.12 02/06/2018
Memory:
12x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
12x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

================================================================================
 CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
================================================================================
 icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

================================================================================

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### Compiler Version Notes (Continued)

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<th>CC</th>
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<tr>
<th>CXXC</th>
<th>620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)</th>
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<th>FC</th>
<th>648.exchange2_s(base, peak)</th>
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<tbody>
<tr>
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### Base Compiler Invocation

- **C benchmarks:**
  - icc

- **C++ benchmarks:**
  - icpc

- **Fortran benchmarks:**
  - ifort

### Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

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| SPECspeed2017_int_base | 8.70 |
| SPECspeed2017_int_peak | 9.00 |

### Base Portability Flags (Continued)

- `602.gcc_s`: `-DSPEC_LP64`
- `605.mcf_s`: `-DSPEC_LP64`
- `620.omnetpp_s`: `-DSPEC_LP64`
- `623.xalancbmk_s`: `-DSPEC_LP64 -DSPEC_LINUX`
- `625.x264_s`: `-DSPEC_LP64`
- `631.deepsjeng_s`: `-DSPEC_LP64`
- `641.leela_s`: `-DSPEC_LP64`
- `648.exchange2_s`: `-DSPEC_LP64`
- `657.xz_s`: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Base Other Flags

**C benchmarks:**

- `-m64 -std=c11`

**C++ benchmarks:**

- `-m64`

**Fortran benchmarks:**

- `-m64`
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Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

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### Peak Optimization Flags (Continued)

- 625.x264_s: 
  - `-Wl,-z,muldefs` 
  - `-xCORE-AVX512` 
  - `-ipo` 
  - `-O3` 
  - `-no-prec-div` 
  - `-qopt-mem-layout-trans=3` 
  - `-gopenmp` 
  - `-DSPEC_OPENMP` 
  - `-L/usr/local/je5.0.1-64/lib` 
  - `-ljemalloc`

- 657.xz_s: Same as 602.gcc_s

#### C++ benchmarks:

- 620.omnetpp_s: 
  - `-Wl,-z,muldefs` 
  - `-prof-gen(pass 1)` 
  - `-prof-use(pass 2)` 
  - `-ipo` 
  - `-xCORE-AVX512` 
  - `-O3` 
  - `-no-prec-div` 
  - `-qopt-mem-layout-trans=3` 
  - `-DSPEC_SUPPRESS_OPENMP` 
  - `-gopenmp` 
  - `-DSPEC_OPENMP` 
  - `-L/usr/local/je5.0.1-64/lib` 
  - `-ljemalloc`

- 623.xalancbmk_s: 
  - `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32` 
  - `-Wl,-z,muldefs` 
  - `-prof-gen(pass 1)` 
  - `-prof-use(pass 2)` 
  - `-ipo` 
  - `-xCORE-AVX512` 
  - `-O3` 
  - `-no-prec-div` 
  - `-qopt-mem-layout-trans=3` 
  - `-DSPEC_SUPPRESS_OPENMP` 
  - `-gopenmp` 
  - `-DSPEC_OPENMP` 
  - `-L/usr/local/je5.0.1-32/lib` 
  - `-ljemalloc`

- 631.deepsjeng_s: Same as 620.omnetpp_s

- 641.leela_s: Same as 620.omnetpp_s

#### Fortran benchmarks:

- `-Wl,-z,muldefs` 
  - `-xCORE-AVX512` 
  - `-ipo` 
  - `-no-prec-div` 
  - `-qopt-mem-layout-trans=3` 
  - `-nostandard-realloc-lhs` 
  - `-align array32byte` 
  - `-L/usr/local/je5.0.1-64/lib` 
  - `-ljemalloc`

---

### Peak Other Flags

#### C benchmarks:

- `-m64` 
  - `-std=c11`

#### C++ benchmarks (except as noted below):

- `-m64`

- 623.xalancbmk_s: `-m32`

#### Fortran benchmarks:

- `-m64`
Dell Inc.
PowerEdge MX740c (Intel Xeon Gold 6134, 3.20GHz)

SPECspeak2017_int_base = 8.70
SPECspeak2017_int_peak = 9.00

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2018
Hardware Availability: Sep-2018
Software Availability: Feb-2018

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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