### SPEC® CPU2017 Floating Point Rate Result

**Huawei**

**Huawei 5288 V5 (Intel Xeon Silver 4109T)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_peak</th>
<th>SPECrate2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>85.2</td>
<td>82.9</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175  
**Test Date:** Sep-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018  

**Test Sponsor:** Huawei  
**Tested by:** Huawei

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Silver 4109T</td>
<td>OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)</td>
</tr>
<tr>
<td>Max MHz.: 3000</td>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Nominal: 2000</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Enabled: 16 cores, 2 chips, 2 threads/core</td>
<td>Firmware: Version 0.81 Released Jul-2018</td>
</tr>
<tr>
<td>Orderable: 1.2 chips</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>L3: 11 MB I+D on chip per chip</td>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
<td></td>
</tr>
<tr>
<td>Storage: 1 x 1200 GB SAS, 10000 RPM</td>
<td></td>
</tr>
<tr>
<td>Other: None</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
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<tbody>
<tr>
<td>SPECrate2017_fp_peak (85.2)</td>
<td>SPECrate2017_fp_base (82.9)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp</th>
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<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>68.8</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>53.2</td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>54.5</td>
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<tr>
<td>510.parest_r</td>
<td>32</td>
<td>57.8</td>
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</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>85.6</td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>68.1</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>94.1</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>73.9</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>74.6</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
<td>70.7</td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
<td>105</td>
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</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>105</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>52.6</td>
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SPEC CPU2017 Floating Point Rate Result

Huawei
Huawei 5288 V5 (Intel Xeon Silver 4109T)

SPECrate2017_fp_base = 82.9
SPECrate2017_fp_peak = 85.2

Results Table

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<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
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<th>Peak Seconds</th>
<th>Peak Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
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<tbody>
<tr>
<td>503.bwaves_r</td>
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<td>988</td>
<td>325</td>
<td>990</td>
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<tr>
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<td>549.fotonik3d_r</td>
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<td>32</td>
<td>968</td>
<td>52.6</td>
<td>965</td>
<td>52.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
SPEC CPU2017 Floating Point Rate Result

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Huawei 5288 V5 (Intel Xeon Silver 4109T)

SPECrates2017_fp_base = 82.9
SPECrates2017_fp_peak = 85.2

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Sep-2018
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Jan-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Thu Sep 6 20:31:34 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
        2 "physical id"s (chips)
        32 "processors"
        cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
        cpu cores : 8
        siblings : 16
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
    Architecture:          x86_64
    CPU op-mode(s):        32-bit, 64-bit
    Byte Order:            Little Endian
    CPU(s):                32
    On-line CPU(s) list:   0-31
    Thread(s) per core:    2
    Core(s) per socket:    8
    Socket(s):             2
    NUMA node(s):          2
    Vendor ID:             GenuineIntel
    CPU family:            6
    Model:                 85
    Model name:            Intel(R) Xeon(R) Silver 4109T CPU @ 2.00GHz
    Stepping:              4
    CPU MHz:               2000.000
    BogoMIPS:              4000.00
(Continued on next page)
Huawei

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Platform Notes (Continued)

Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpre
pcdmc pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single intel_pt
spec_ctrl ibpb_support tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust
bm1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc
cqm_occuppy_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

/proc/cpuinfo cache data
cache size: 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 391349 MB
node 0 free: 367140 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 393216 MB
node 1 free: 370951 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 790512260 kB
HugePages_Total: 0
Hugepagesize: 2048 KB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"

(Continued on next page)
Huawei 5288 V5 (Intel Xeon Silver 4109T) SPECrate2017_fp_base = 82.9
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Platform Notes (Continued)

VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
    Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 2 16:50

SPEC is set to: /spec2017
    Filesystem  Type Size  Used Avail Use% Mounted on
    /dev/sda2      xfs  781G  132G  650G  17% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS INSYDE Corp. 0.81 07/02/2018
    Memory:
        24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
CC  519.lbm_r(peak) 544.nab_r(peak)
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icc (ICC) 18.0.0 20170811
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CXXC 508.namd_r(base) 510.parest_r(base)

(Continued on next page)
Huawei
Huawei 5288 V5 (Intel Xeon Silver 4109T)

Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 508.namd_r(peak) 510.parest_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 511.povray_r(base) 526.blender_r(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 511.povray_r(peak) 526.blender_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 507.cactuBSSN_r(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 507.cactuBSSN_r(peak)
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(Continued on next page)
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CPU2017 License: 3175
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Compiler Version Notes (Continued)

ifort (IFORT) 18.0.0 20170811
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==============================================================================
FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)

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==============================================================================
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icc (ICC) 18.0.0 20170811
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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

(Continued on next page)
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Base CompilerInvocation (Continued)

Benchmarks using both Fortran and C:
ifort icc
Benchmarks using both C and C++:
icpc icc
Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Huawei

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nolist-standard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nolist-standard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

(Continued on next page)
### Huawei

**Huawei 5288 V5 (Intel Xeon Silver 4109T)**

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<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
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<td>82.9</td>
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<td>Huawei</td>
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<td>Tested by:</td>
<td>Huawei</td>
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<tr>
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<td>Software Availability:</td>
<td>Jan-2018</td>
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### Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using both C and C++:
- `icpc icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

**C benchmarks:**

519.lbm_r: `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3`

538.imagick_r: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3`

544.nab_r: Same as 519.lbm_r

**C++ benchmarks:**

 `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3`

**Fortran benchmarks:**

503.bwaves_r: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

549.fotonik3d_r: Same as 503.bwaves_r
Huawei

Huawei 5288 V5 (Intel Xeon Silver 4109T)

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**Peak Optimization Flags (Continued)**

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
507.cactuBSSN_r: basepeak = yes

**Peak Other Flags**

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
## SPEC CPU2017 Floating Point Rate Result

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### Huawei

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
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You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-09-06 20:31:32-0400.  
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