Dell Inc.

PowerEdge MX840c (Intel Xeon Gold 6130 CPU, 2.10GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

---

**Threads**

<table>
<thead>
<tr>
<th>Benchmark(s)</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>183</td>
<td>187</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>79.1</td>
<td>79.0</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>64</td>
<td>79.0</td>
<td>71.8</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>64</td>
<td>74.5</td>
<td>74.5</td>
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<tr>
<td>627.cam4_s</td>
<td>64</td>
<td>112</td>
<td>112</td>
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<tr>
<td>628.pop2_s</td>
<td>64</td>
<td>38.7</td>
<td>36.8</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>64</td>
<td>151</td>
<td>151</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>64</td>
<td>282</td>
<td>282</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>64</td>
<td>111</td>
<td>111</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>64</td>
<td>236</td>
<td>248</td>
</tr>
</tbody>
</table>

---

**Hardware**

CPU Name: Intel Xeon Gold 6130
Max MHz.: 3700
Nominal: 2100
Enabled: 64 cores, 4 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 22 MB I+D on chip per chip
Other: None
Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Storage: 1 TB SATA SSD
Other: None

**Software**

OS: SUSE Linux Enterprise Server 12 SP3
4.4.126-94.22-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
Compiler for Linux:
Fortran: Version 18.0.0.128 of Intel Fortran
Compiler for Linux
Parallel: Yes
Firmware: Version 0.4.4 released May-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Dell Inc.
PowerEdge MX840c (Intel Xeon Gold 6130 CPU, 2.10GHz)

SPECspeed2017_fp_base = 146
SPECspeed2017_fp_peak = 147

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>66.6</td>
<td>886</td>
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<td>875</td>
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<td>90.5</td>
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<td>188</td>
<td>89.4</td>
<td>186</td>
<td>89.0</td>
<td>187</td>
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<tr>
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<td>64</td>
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<td>79.1</td>
<td>66.5</td>
<td>78.8</td>
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<td>66.5</td>
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<tr>
<td>621.wrf_s</td>
<td>64</td>
<td>184</td>
<td>71.8</td>
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<td>73.3</td>
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<td>111</td>
<td>79.0</td>
<td>112</td>
<td>79.3</td>
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<tr>
<td>628.pop2_s</td>
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<td>152</td>
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<td>644.nab_s</td>
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<td>62.0</td>
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<td>62.0</td>
<td>282</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>82.9</td>
<td>110</td>
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<tr>
<td>654.roms_s</td>
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<td>66.7</td>
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<td>67.2</td>
<td>234</td>
<td>65.8</td>
<td>239</td>
<td>64</td>
<td>63.6</td>
<td>247</td>
<td>63.4</td>
<td>248</td>
<td>62.9</td>
<td>250</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 146
SPECspeed2017_fp_peak = 147

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity= fine, compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Platform Notes

BIOS settings:
Sub NUMA Cluster Disabled
Virtualization Technology Disabled
SPEC CPU2017 Floating Point Speed Result

Dell Inc.
PowerEdge MX840c (Intel Xeon Gold 6130 CPU, 2.10GHz)

SPECspeed2017_fp_base = 146
SPECspeed2017_fp_peak = 147

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Jun-2018
Tested by: Dell Inc.
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E Disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub Disabled
Logical Processor Disabled
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on linux-0x7z Tue Jun 5 23:17:01 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
4 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.092

(Continued on next page)
Platform Notes (Continued)

BogoMIPS: 4190.18
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63
Flags: fpu vme de pse ts cmov tsc mca cmov pat pse36 ccf flush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm intel_pt rsb_ctxtsw spec_ctrl stibp retpoline kaiser tpr_shadow vnum
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap cfiflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsave xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

/pro/cpuid info cache data
  cache size : 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60
  node 0 size: 192124 MB
  node 0 free: 189440 MB
  node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61
  node 1 size: 193525 MB
  node 1 free: 192090 MB
  node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62
  node 2 size: 193525 MB
  node 2 free: 192300 MB
  node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63
  node 3 size: 193522 MB
  node 3 free: 190809 MB
  node distances:
    node 0 1 3
    0: 10 21 21 21
    1: 21 10 21 21
    2: 21 21 10 21
    3: 21 21 21 10

From /proc/meminfo

(Continued on next page)
Platform Notes (Continued)

MemTotal: 791243144 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-0x7z 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 5 18:07

SPEC is set to: /root/cpu2017
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda3 xfs  927G 33G 895G  4% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Dell Inc. 0.4.4 05/22/2018
  Memory:
    26x 002C00B3002C 18ASF2G72PD2-2G6D1 16 GB 2 rank 2666
    2x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
    20x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
  CC   619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
  
(Continued on next page)
Dell Inc.  
PowerEdge MX840c (Intel Xeon Gold 6130 CPU, 2.10GHz)  

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>146</th>
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</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>147</td>
</tr>
</tbody>
</table>

**Compiler Version Notes (Continued)**

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 619.libm_s(peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```
------------------------------------------------------------------------------
FC 607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 607.cactuBSSN_s(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

(Continued on next page)
**SPEC CPU2017 Floating Point Speed Result**

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6130 CPU, 2.10GHz)

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<tr>
<td>SPECspeed2017_fp_peak = 147</td>
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jun-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

---

**Compiler Version Notes (Continued)**

```
=================================================================================================
 CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
=================================================================================================
 CC  621.wrf_s(peak) 628.pop2_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

---

**Base Compiler Invocation**

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

---

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
638.imagick_s: -DSPEC_LP64

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

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Base Portability Flags (Continued)

644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11
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Test Date: Jun-2018  
Hardware Availability: Sep-2017  
Software Availability: Sep-2017

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp

(Continued on next page)
### Peak Optimization Flags (Continued)

621.wrf_s (continued):
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

### Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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