## SPEC® CPU2017 Floating Point Speed Result

### Huawei

**Huawei 1288H V5 (Intel Xeon Gold 6146)**

<table>
<thead>
<tr>
<th>Software</th>
<th>SPECspeed2017_fp_base = 106</th>
</tr>
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<tbody>
<tr>
<td>CPU2017 License:</td>
<td>3175</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jun-2018</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
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<td>Tested by:</td>
<td>Huawei</td>
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<td>Software Availability:</td>
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<table>
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<tr>
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<tr>
<td>Threads</td>
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<tr>
<td>603.bwaves_s</td>
<td>24</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>24</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>24</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>24</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>24</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>24</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>24</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>24</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>24</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>24</td>
</tr>
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### Hardware

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
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<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Gold 6146</td>
</tr>
<tr>
<td>Max MHz.:</td>
<td>4200</td>
</tr>
<tr>
<td>Nominal:</td>
<td>3200</td>
</tr>
<tr>
<td>Enabled:</td>
<td>24 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
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<tr>
<td>L3:</td>
<td>24.75 MB I+D on chip per chip</td>
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<tr>
<td>Other:</td>
<td>None</td>
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<tr>
<td>Memory:</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)</td>
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<tr>
<td>Storage:</td>
<td>1 x 1200 GB SAS, 10000 RPM</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
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### Software

<table>
<thead>
<tr>
<th>Spec</th>
<th>Value</th>
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<tbody>
<tr>
<td>OS:</td>
<td>Red Hat Enterprise Linux Server release 7.4 (Maipo) 3.10.0-693.11.6.el7.x86_64</td>
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<tr>
<td>Compiler:</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
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<tr>
<td>Parallel:</td>
<td>Yes</td>
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<tr>
<td>Firmware:</td>
<td>Version 0.62 Released Mar-2018</td>
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<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
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<td>Other:</td>
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Results Table

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<tr>
<th>Benchmark</th>
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<td>122</td>
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<tr>
<td>607.cactusBSSN_s</td>
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<td>125</td>
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<td>123</td>
<td>129</td>
<td>123</td>
<td>129</td>
<td>122</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable

(Continued on next page)
Huawei

Huawei 1288H V5 (Intel Xeon Gold 6146)

SPEC CPU2017 Floating Point Speed Result

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Huawei 1288H V5 (Intel Xeon Gold 6146)

SPECspeed2017_fp_base = 106

SPECspeed2017_fp_peak = 108

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b6c091c0f
running on localhost.localdomain Tue Jun 26 05:29:58 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 12
physical 0: cores 0 1 2 3 4 9 10 16 18 19 25 26
physical 1: cores 0 1 2 3 8 9 10 11 18 19 24 27

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                24
On-line CPU(s) list:   0-23
Thread(s) per core:    1
Core(s) per socket:    12
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
Stepping:              4
CPU MHz:               3201.000
CPU max MHz:           3201.0000
CPU min MHz:           1200.0000
BogoMIPS:              6400.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              25344K
NUMA node0 CPU(s):     0-11
NUMA node1 CPU(s):     12-23
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
(Continued on next page)
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Platform Notes (Continued)

pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperf perf eagerfpfu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdg_l3 invpgd_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid_single
depths opt clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_1lc cqm_occup_l1c cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
node 0 size: 194741 MB
node 0 free: 189364 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
node 1 size: 196608 MB
node 1 free: 191549 MB
node distances:
node 0  0 1
0:  10 21
1:  21 10

From /proc/meminfo
MemTotal: 394174720 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

uname -a:

(Continued on next page)
Huawei

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Platform Notes (Continued)

Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 25 11:27

SPEC is set to: /spec2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs 1.8T 37G 1.7T 3% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.62 03/26/2018
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

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<table>
<thead>
<tr>
<th>CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)</th>
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<tbody>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
</tr>
<tr>
<td>Copyright (C) 1985-2017 Intel Corporation. All rights reserved.</td>
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<table>
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<tr>
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<tr>
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CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Compiler Version Notes (Continued)

FC 607.cactuBSSN_s(peak)

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Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
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FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

ifort (IFORT) 18.0.0 20170811
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FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)

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CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)

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icc (ICC) 18.0.0 20170811
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CC 621.wrf_s(peak) 628.pop2_s(peak)

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Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
   -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
   -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
   -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
   -nostandard-realloc-lhs -align array32byte

(Continued on next page)
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---

### Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
- `-xCORE-AVX2`
- `-ipo`
- `-O3`
- `-no-prec-div`
- `-qopt-prefetch`
- `-ffinite-math-only`
- `-qopt-mem-layout-trans=3`
- `-qopenmp`
- `-DSPEC_OPENMP`
- `-nostandard-realloc-lhs`
- `-align array32byte`

---

### Base Other Flags

**C benchmarks:**
- `-m64`
- `-std=c11`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64`
- `-std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64`
- `-std=c11`

---

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**Fortran benchmarks:**
- `ifort`

**Benchmarks using both Fortran and C:**
- `ifort icc`

**Benchmarks using Fortran, C, and C++:**
- `icpc icc ifort`

---

### Peak Portability Flags

*Same as Base Portability Flags*
## SPEC CPU2017 Floating Point Speed Result

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### Peak Optimization Flags

**C benchmarks:**

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP

638.imagick_s: basepeak = yes

644.nab_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

### Fortran benchmarks:

603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs -align array32byte

649.fotonik3d_s: basepeak = yes

654.roms_s: Same as 603.bwaves_s

### Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

### Benchmarks using Fortran, C, and C++:

-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
Huawei

Huawei 1288H V5 (Intel Xeon Gold 6146)

SPECspeed2017_fp_peak = 108
SPECspeed2017_fp_base = 106

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Peak Other Flags

C benchmarks:
- m64 -std=c11

Fortran benchmarks:
- m64

Benchmarks using both Fortran and C:
- m64 -std=c11

Benchmarks using Fortran, C, and C++:
- m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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