## Huawei

**Huawei XH321 V5 (Intel Xeon Silver 4110)**

### SPEC® CPU2017 Floating Point Speed Result

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Jun-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Jan-2018</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base (59.8)</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s 16</td>
<td>72.2</td>
</tr>
<tr>
<td>607.cactuBSSN_s 16</td>
<td>73.6</td>
</tr>
<tr>
<td>619.lbm_s 16</td>
<td>31.7</td>
</tr>
<tr>
<td>621.wrf_s 16</td>
<td>43.5</td>
</tr>
<tr>
<td>627.cam4_s 16</td>
<td>34.2</td>
</tr>
<tr>
<td>628.pop2_s 16</td>
<td>45.5</td>
</tr>
<tr>
<td>638.imagick_s 16</td>
<td>41.9</td>
</tr>
<tr>
<td>644.nab_s 16</td>
<td>73.1</td>
</tr>
<tr>
<td>649.fotonik3d_s 16</td>
<td>61.3</td>
</tr>
<tr>
<td>654.roms_s 16</td>
<td>64.0</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Silver 4110</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz.:</td>
<td>3000</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled:</td>
<td>16 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 1200 GB SAS, 10000 RPM</td>
</tr>
</tbody>
</table>

| OS: | Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-693.11.6.el7.x86_64 |
| Compiler: | C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux |
| Parallel: | Yes |
| Firmware: | Version 0.59 Released Feb-2018 |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 64-bit |
| Other: | None |
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>189</td>
<td>313</td>
<td>188</td>
<td>314</td>
<td><strong>188</strong></td>
<td><strong>313</strong></td>
<td>16</td>
<td><strong>188</strong></td>
<td><strong>314</strong></td>
<td>187</td>
<td>315</td>
<td>188</td>
<td>313</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>230</td>
<td>72.4</td>
<td>232</td>
<td>71.9</td>
<td><strong>231</strong></td>
<td><strong>72.2</strong></td>
<td>16</td>
<td><strong>226</strong></td>
<td>73.6</td>
<td>228</td>
<td>73.3</td>
<td><strong>227</strong></td>
<td><strong>73.6</strong></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>16</td>
<td><strong>165</strong></td>
<td><strong>31.7</strong></td>
<td>166</td>
<td>31.6</td>
<td>165</td>
<td>31.7</td>
<td>16</td>
<td><strong>165</strong></td>
<td><strong>31.7</strong></td>
<td>165</td>
<td>31.7</td>
<td>165</td>
<td>31.7</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>304</td>
<td><strong>43.5</strong></td>
<td>306</td>
<td>43.2</td>
<td>298</td>
<td>44.4</td>
<td>16</td>
<td><strong>279</strong></td>
<td>47.3</td>
<td><strong>279</strong></td>
<td><strong>47.4</strong></td>
<td>276</td>
<td><strong>47.6</strong></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>16</td>
<td>259</td>
<td>34.2</td>
<td><strong>259</strong></td>
<td><strong>34.2</strong></td>
<td>259</td>
<td>34.2</td>
<td>16</td>
<td><strong>260</strong></td>
<td>34.1</td>
<td>259</td>
<td>34.2</td>
<td><strong>259</strong></td>
<td><strong>34.2</strong></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>16</td>
<td>261</td>
<td>45.6</td>
<td><strong>261</strong></td>
<td><strong>45.5</strong></td>
<td>262</td>
<td>45.4</td>
<td>16</td>
<td><strong>247</strong></td>
<td><strong>48.0</strong></td>
<td>247</td>
<td>48.1</td>
<td>248</td>
<td>47.9</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>16</td>
<td><strong>345</strong></td>
<td><strong>41.9</strong></td>
<td>344</td>
<td>41.9</td>
<td>346</td>
<td>41.7</td>
<td>16</td>
<td><strong>345</strong></td>
<td><strong>41.9</strong></td>
<td>344</td>
<td>41.9</td>
<td>346</td>
<td>41.7</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>16</td>
<td><strong>239</strong></td>
<td><strong>73.1</strong></td>
<td>239</td>
<td>73.2</td>
<td>239</td>
<td>73.1</td>
<td>16</td>
<td><strong>239</strong></td>
<td>73.1</td>
<td><strong>239</strong></td>
<td><strong>73.1</strong></td>
<td>239</td>
<td>73.1</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>16</td>
<td>149</td>
<td>61.1</td>
<td><strong>149</strong></td>
<td><strong>61.3</strong></td>
<td>148</td>
<td>61.5</td>
<td>16</td>
<td><strong>149</strong></td>
<td>61.1</td>
<td><strong>149</strong></td>
<td><strong>61.3</strong></td>
<td>148</td>
<td>61.5</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>16</td>
<td>246</td>
<td>64.1</td>
<td><strong>246</strong></td>
<td><strong>64.0</strong></td>
<td>246</td>
<td>63.9</td>
<td>16</td>
<td><strong>229</strong></td>
<td>68.7</td>
<td>229</td>
<td>68.6</td>
<td>229</td>
<td>68.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Custom
Hyper-Threading Set to Disable

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 59.8
SPECspeed2017_fp_peak = 61.2

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

ADDDC Sparing Set to Disabled
Sysinfo program /spec/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on localhost.localdomain Sat Jun 23 00:33:59 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
2 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2101.000
BogoMIPS: 4205.34
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15

/proc/cpuinfo cache data
  cache size : 11264 KB

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4110)

SPECspeed2017_fp_peak = 61.2
SPECspeed2017_fp_base = 59.8

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Jan-2018

Platform Notes (Continued)

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 194741 MB
node 0 free: 189524 MB
node 1 cpus: 8 9 10 11 12 13 14 15
node 1 size: 196608 MB
node 1 free: 191524 MB
node distances:
node   0   1
0:  10  21
1:  21  10

From /proc/meminfo
MemTotal:       394174996 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*

```
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
```

uname -a:
```
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 22 18:10

SPEC is set to: /spec
```
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda8    xfs   325G  114G  211G  36% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 59.8
SPECspeed2017_fp_peak = 61.2

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS INSYDE Corp. 0.59 02/24/2018
Memory:
4x NO DIMM NO DIMM
12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
<table>
<thead>
<tr>
<th>CC   619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)</th>
</tr>
</thead>
</table>

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
<table>
<thead>
<tr>
<th>CC   619.lbm_s(peak)</th>
</tr>
</thead>
</table>

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
<table>
<thead>
<tr>
<th>FC   607.cactuBSSN_s(base)</th>
</tr>
</thead>
</table>

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
<table>
<thead>
<tr>
<th>FC   607.cactuBSSN_s(peak)</th>
</tr>
</thead>
</table>

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 59.8
SPECspeed2017_fp_peak = 61.2

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Compiler Version Notes (Continued)

==============================================================================
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  621.wrf_s(peak) 628.pop2_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4110)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>59.8</td>
<td>61.2</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

**Base Portability Flags**

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

**Base Other Flags**

C benchmarks:
- m64 -std=c11

Fortran benchmarks:
- m64
Huawei
Huawei XH321 V5 (Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 59.8
SPECspeed2017_fp_peak = 61.2

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Base Other Flags (Continued)

Benchmarks using both Fortran and C:
- m64 -std=c11

Benchmarks using Fortran, C, and C++:
- m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: basepeak = yes

644.nab_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

(Continued on next page)
**Huawei**

Huawei XH321 V5 (Intel Xeon Silver 4110)  

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>59.8</td>
<td>61.2</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Test Date:** Jun-2018  
**Hardware Availability:** Jul-2017  
**Tested by:** Huawei  
**Software Availability:** Jan-2018

### Peak Optimization Flags (Continued)

**Fortran benchmarks:**

- 603.bwaves_s: `-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp -nostandard-realloc-lhs -align array32byte`  

- 649.fotonik3d_s: `basepeak = yes`  

- 654.roms_s: Same as 603.bwaves_s

**Benchmarks using both Fortran and C:**

- 621.wrf_s: `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`  

- 627.cam4_s: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`  

- 628.pop2_s: Same as 621.wrf_s

**Benchmarks using Fortran, C, and C++:**

- `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`

### Peak Other Flags

**C benchmarks:**

- `-m64 -std=c11`

**Fortran benchmarks:**

- `-m64`

**Benchmarks using both Fortran and C:**

- `-m64 -std=c11`

**Benchmarks using Fortran, C, and C++:**

- `-m64 -std=c11`
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4110)  

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base = 59.8</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak = 61.2</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 3175</th>
<th>Test Date: Jun-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Huawei</td>
<td>Hardware Availability: Jul-2017</td>
</tr>
<tr>
<td>Tested by: Huawei</td>
<td>Software Availability: Jan-2018</td>
</tr>
</tbody>
</table>

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-06-23 00:33:59-0400.
Originally published on 2018-07-10.