SPEC® CPU2017 Integer Rate Result

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Fujitsu

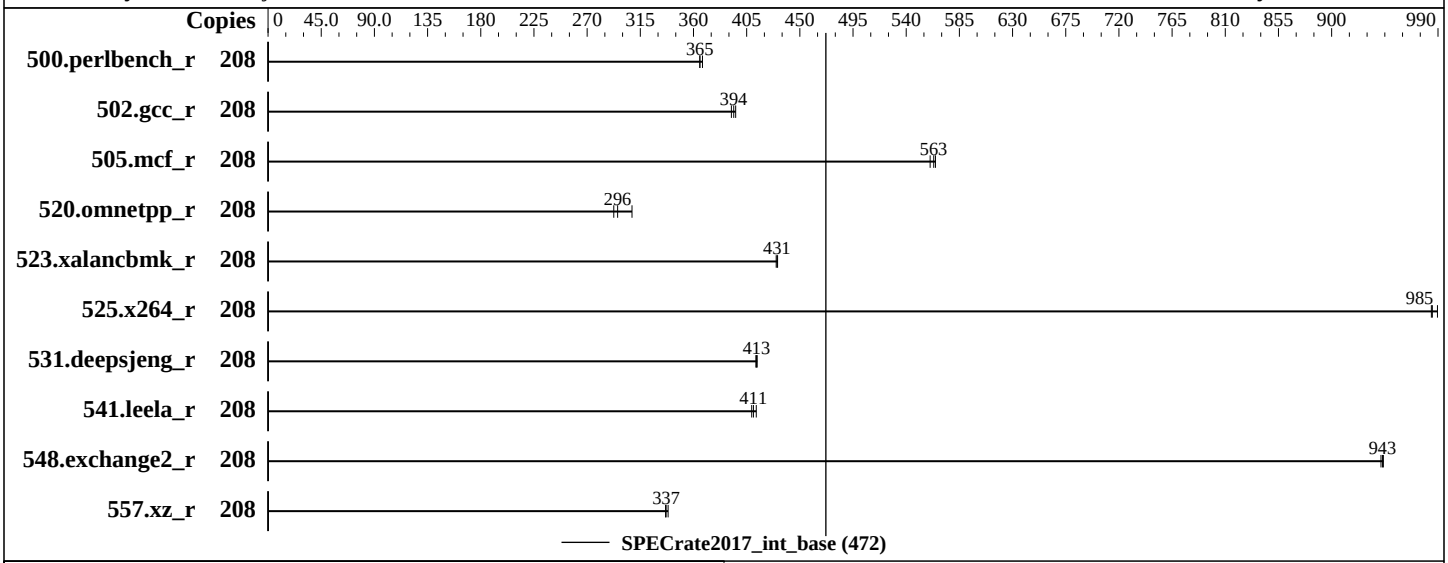
PRIMERGY RX4770 M4, Intel Xeon Platinum 8170, 2.10GHz

SPECrate2017_int_base = 472

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018



Hardware

CPU Name:	Intel Xeon Platinum 8170
Max MHz.:	3700
Nominal:	2100
Enabled:	104 cores, 4 chips, 2 threads/core
Orderable:	2,4 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	35.75 MB I+D on chip per chip
Other:	None
Memory:	768 GB (48 x 16 GB 2Rx4 PC4-2666V-R)
Storage:	384 GB tmpfs
Other:	1 x SAS HDD, 1.2 TB, 10500 RPM, used for swap

Software

OS:	SUSE Linux Enterprise Server 12 SP2 4.4.114-92.64-default
Compiler:	C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Parallel:	No
Firmware:	Fujitsu BIOS Version V5.0.0.12 R1.9.0 for D3753-A1x. Released Mar-2018
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other:	jemalloc memory allocator library V5.0.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	208	906	365	901	368	**906**	**365**							
502.gcc_r	208	744	396	**747**	**394**	751	392							
505.mcf_r	208	595	565	**597**	**563**	600	560							
520.omnetpp_r	208	886	308	**923**	**296**	933	293							
523.xalancbmk_r	208	**510**	**431**	509	431	511	430							
525.x264_r	208	368	990	370	984	**370**	**985**							
531.deepsjeng_r	208	576	414	577	413	**577**	**413**							
541.leela_r	208	**839**	**411**	842	409	834	413							
548.exchange2_r	208	579	942	577	944	**578**	**943**							
557.xz_r	208	664	339	**667**	**337**	668	336							

SPECrate2017_int_base = 472

SPECrate2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter: nohz_full=1-207
Set CPU frequency governor to maximum performance with:
cpupower -c all frequency-set -g performance
Set tmpfs filesystem with:
mkdir /home/memory
mount -t tmpfs -o size=384g,rw tmpfs /home/memory
cpu idle state set with:
cpupower idle-set -d 1
cpupower idle-set -d 2
```

General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/memory/speccpu/lib/ia32:/home/memory/speccpu/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-64"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.4
```

(Continued on next page)

General Notes (Continued)

```
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

 jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
 jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
 jemalloc: sources available via jemalloc.net

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
 BIOS configuration:
 DCU Streamer Prefetcher = Disabled
 Intel Virtualization Technology = Disabled
 HWPM Support = Disabled
 Package C State limit = C0
 LLC Dead Line Alloc = Disabled
 Stale AtoS = Enabled
 Patrol Scrub = Disabled
 IMC Interleaving = 1-way
 Sub NUMA Clustering = Enabled
 Fan Control = Full
 Sysinfo program /home/memory/speccpu/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on RX4770M4 Tue Jun  5 01:39:54 2018

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz
       4  "physical id"s (chips)
       208 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 26
```

(Continued on next page)

Platform Notes (Continued)

```
        siblings  : 52
        physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
        29
        physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
        29
        physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
        29
        physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
        29

  From lscpu:
       Architecture:          x86_64
       CPU op-mode(s):        32-bit, 64-bit
       Byte Order:            Little Endian
       CPU(s):                208
       On-line CPU(s) list:   0-207
       Thread(s) per core:    2
       Core(s) per socket:    26
       Socket(s):             4
       NUMA node(s):          8
       Vendor ID:             GenuineIntel
       CPU family:            6
       Model:                 85
       Model name:            Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz
       Stepping:              4
       CPU MHz:               2101.000
       CPU max MHz:           2101.0000
       CPU min MHz:           1000.0000
       BogoMIPS:              4190.14
       Virtualization:        VT-x
       L1d cache:             32K
       L1i cache:             32K
       L2 cache:              1024K
       L3 cache:              36608K
       NUMA node0 CPU(s):     0-3,7-9,13-15,20-22,104-107,111-113,117-119,124-126
       NUMA node1 CPU(s):     4-6,10-12,16-19,23-25,108-110,114-116,120-123,127-129
       NUMA node2 CPU(s):     26-29,33-35,39-41,46-48,130-133,137-139,143-145,150-152
       NUMA node3 CPU(s):     30-32,36-38,42-45,49-51,134-136,140-142,146-149,153-155
       NUMA node4 CPU(s):     52-55,59-61,65-67,72-74,156-159,163-165,169-171,176-178
       NUMA node5 CPU(s):     56-58,62-64,68-71,75-77,160-162,166-168,172-175,179-181
       NUMA node6 CPU(s):     78-81,85-87,91-93,98-100,182-185,189-191,195-197,202-204
       NUMA node7 CPU(s):     82-84,88-90,94-97,101-103,186-188,192-194,198-201,205-207
       Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
       aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
       fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
```

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Platform Notes (Continued)

```
      xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
      dtherm intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority
      ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx
      avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
      xsavec xgetbv1 cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
    cache size : 36608 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 7 8 9 13 14 15 20 21 22 104 105 106 107 111 112 113 117 118 119
   124 125 126
   node 0 size: 95018 MB
   node 0 free: 85690 MB
   node 1 cpus: 4 5 6 10 11 12 16 17 18 19 23 24 25 108 109 110 114 115 116 120 121 122
   123 127 128 129
   node 1 size: 96760 MB
   node 1 free: 96491 MB
   node 2 cpus: 26 27 28 29 33 34 35 39 40 41 46 47 48 130 131 132 133 137 138 139 143 144
   145 150 151 152
   node 2 size: 96760 MB
   node 2 free: 96505 MB
   node 3 cpus: 30 31 32 36 37 38 42 43 44 45 49 50 51 134 135 136 140 141 142 146 147 148
   149 153 154 155
   node 3 size: 96760 MB
   node 3 free: 96507 MB
   node 4 cpus: 52 53 54 55 59 60 61 65 66 67 72 73 74 156 157 158 159 163 164 165 169 170
   171 176 177 178
   node 4 size: 96760 MB
   node 4 free: 96487 MB
   node 5 cpus: 56 57 58 62 63 64 68 69 70 71 75 76 77 160 161 162 166 167 168 172 173 174
   175 179 180 181
   node 5 size: 96760 MB
   node 5 free: 96174 MB
   node 6 cpus: 78 79 80 81 85 86 87 91 92 93 98 99 100 182 183 184 185 189 190 191 195
   196 197 202 203 204
   node 6 size: 96760 MB
   node 6 free: 96511 MB
   node 7 cpus: 82 83 84 88 89 90 94 95 96 97 101 102 103 186 187 188 192 193 194 198 199
   200 201 205 206 207
   node 7 size: 96609 MB
   node 7 free: 96274 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  11  31  31  21  21  21  21
```

(Continued on next page)

Platform Notes (Continued)

```
     1:  11  10  31  31  21  21  21  21
     2:  31  31  10  11  21  21  21  21
     3:  31  31  11  10  21  21  21  21
     4:  21  21  21  21  10  11  31  31
     5:  21  21  21  21  11  10  31  31
     6:  21  21  21  21  31  31  10  11
     7:  21  21  21  21  31  31  11  10

  From /proc/meminfo
     MemTotal:       790723368 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12 SP2

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"

  uname -a:
     Linux RX4770M4 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db)
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jun 4 22:18

  SPEC is set to: /home/memory/speccpu
     Filesystem     Type   Size  Used Avail Use% Mounted on
     tmpfs          tmpfs  384G  8.8G  376G   3% /home/memory

  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS FUJITSU // American Megatrends Inc. V5.0.0.12 R1.9.0 for D3753-A1x
```

(Continued on next page)

Platform Notes (Continued)

```
      03/29/2018
   Memory:
    48x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
     557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
     541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  548.exchange2_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

C++ benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

Fortran benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

Base Other Flags

C benchmarks:
`-m64 -std=c11`

C++ benchmarks:
`-m64`

Fortran benchmarks:
`-m64`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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