Dell Inc.  

PowerEdge R740xd (Intel Xeon Gold 6144, 3.50GHz)  

<table>
<thead>
<tr>
<th></th>
<th>SPECspeed2017_fp_base = 86.3</th>
<th>SPECspeed2017_fp_peak = 87.9</th>
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<tr>
<td>CPU2017 License:</td>
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<td>Test Sponsor:</td>
<td>Dell Inc.</td>
<td>Mar-2018</td>
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<td>Tested by:</td>
<td>Dell Inc.</td>
<td>Hardware Availability: Sep-2017</td>
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<td></td>
<td>Software Availability: Feb-2018</td>
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<tr>
<td>Hardware</td>
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<tr>
<td>CPU Name:</td>
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<tr>
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<td></td>
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<tr>
<td>Nominal:</td>
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<td></td>
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<tr>
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<td>16 cores, 2 chips</td>
<td></td>
</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
<td></td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
<td></td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
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<tr>
<td>L3:</td>
<td>24.75 MB I+D on chip per chip</td>
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<tr>
<td>Other:</td>
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<tr>
<td>Memory:</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)</td>
<td></td>
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<tr>
<td>Storage:</td>
<td>480 GB SATA SSD</td>
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<tr>
<td>Other:</td>
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<tr>
<td>Software</td>
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<td></td>
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<tr>
<td>OS:</td>
<td>SUSE Linux Enterprise Server 12 SP2</td>
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<td>Compiler:</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++</td>
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<td>File System:</td>
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<td>System State:</td>
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<td>Base Pointers:</td>
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<tr>
<td>Peak Pointers:</td>
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<td>Other:</td>
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## Results Table

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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>140</td>
<td>423</td>
<td>140</td>
<td>422</td>
<td>140</td>
<td>423</td>
<td>16</td>
<td>139</td>
<td>424</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>171</td>
<td>97.5</td>
<td>171</td>
<td>97.2</td>
<td>171</td>
<td>97.3</td>
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<td>127</td>
<td>41.2</td>
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<td>621.wrf_s</td>
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<td>209</td>
<td>63.4</td>
<td>196</td>
<td>67.5</td>
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<td>627.cam4_s</td>
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<td>48.9</td>
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<td>181</td>
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<td>49.0</td>
<td>180</td>
<td>49.1</td>
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<td>628.pop2_s</td>
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<td>190</td>
<td>62.4</td>
<td>189</td>
<td>62.8</td>
<td>190</td>
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<td>16</td>
<td>186</td>
<td>63.9</td>
<td>188</td>
<td>63.1</td>
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<tr>
<td>638.imagick_s</td>
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<td>213</td>
<td>67.9</td>
<td>212</td>
<td>68.1</td>
<td>212</td>
<td>68.0</td>
<td>16</td>
<td>212</td>
<td>68.0</td>
<td>212</td>
<td>68.1</td>
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<tr>
<td>644.nab_s</td>
<td>16</td>
<td>141</td>
<td>124</td>
<td>141</td>
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<td>141</td>
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<td>124</td>
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<td>124</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
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<td>117</td>
<td>77.8</td>
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<td>77.5</td>
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<tr>
<td>654.roms_s</td>
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<td>151</td>
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<td>152</td>
<td>104</td>
<td>16</td>
<td>144</td>
<td>110</td>
<td>145</td>
<td>109</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

## Platform Notes

BIOS settings:
- Sub NUMA Cluster disabled
- Virtualization Technology disabled

(Continued on next page)
### Platform Notes (Continued)

- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor disabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /home/cpu2017rev5/cpu2017/bin/sysinfo
  - Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
  - running on linux-bgfp Tue Mar 20 20:52:27 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
  - 2 "physical id"s (chips)
  - 16 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 8
  - siblings : 8
  - physical 0: cores 0 2 3 9 16 19 26 27
  - physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 16
- On-line CPU(s) list: 0-15
- Thread(s) per core: 1
- Core(s) per socket: 8
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
- Stepping: 4
- CPU MHz: 3491.790
- BogoMIPS: 6983.58
- Virtualization: VT-x

(Continued on next page)
Dell Inc. PowerEdge R740xd (Intel Xeon Gold 6144, 3.50GHz)

SPECspeed2017_fp_base = 86.3
SPECspeed2017_fp_peak = 87.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2018
Hardware Availability: Sep-2017
Software Availability: Feb-2018

Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Cache Levels</th>
<th>Size</th>
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</thead>
<tbody>
<tr>
<td>L1d cache</td>
<td>32K</td>
</tr>
<tr>
<td>L1i cache</td>
<td>32K</td>
</tr>
<tr>
<td>L2 cache</td>
<td>1024K</td>
</tr>
<tr>
<td>L3 cache</td>
<td>25344K</td>
</tr>
</tbody>
</table>

NUMA node0 CPU(s): 0,2,4,6,8,10,12,14
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
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lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp

/platform/cpuinfo/cache.data

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14
node 0 size: 191990 MB
node 0 free: 188347 MB
node 1 cpus: 1 3 5 7 9 11 13 15
node 1 size: 193517 MB
node 1 free: 189003 MB

distances:
node 0 1
0: 10 21
1: 21 10

From `/proc/meminfo`

MemTotal: 394760028 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From `/usr/bin/lsb_release -d`
SUSE Linux Enterprise Server 12 SP2

From `/etc/*release* /etc/*version*`
SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2

(Continued on next page)
Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

```
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-bgfp 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 20 16:14
```

```
SPEC is set to: /home/cpu2017rev5/cpu2017
```

```
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   405G   59G  347G  15% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Dell Inc. 1.3.7 02/08/2018
Memory:
22x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
2x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666
```

(End of data from sysinfo program)
Dell Inc.

PowerEdge R740xd (Intel Xeon Gold 6144, 3.50GHz)

SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECspeed2017_fp_base = 86.3
SPECspeed2017_fp_peak = 87.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2018
Hardware Availability: Sep-2017
Software Availability: Feb-2018

Compiler Version Notes (Continued)

==============================================================================
FC  607.cactuBSSN_s(base)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
FC  607.cactuBSSN_s(peak)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Dell Inc.

PowerEdge R740xd (Intel Xeon Gold 6144, 3.50GHz)

| SPECspeed2017_fp_base = 86.3 |
| SPECspeed2017_fp_peak = 87.9 |

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**Compiler Version Notes (Continued)**

```
CC   621.wrf_s(peak) 628.pop2_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

**Base Compiler Invocation**

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

(Continued on next page)
Dell Inc. PowerEdge R740xd (Intel Xeon Gold 6144, 3.50GHz)

SPECspeed2017_fp_base = 86.3
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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Base Optimization Flags (Continued)

C benchmarks (continued):
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc
Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

-wrfl: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Dell Inc. PowerEdge R740xd (Intel Xeon Gold 6144, 3.50GHz)

**SPEC CPU2017 Floating Point Speed Result**

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
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<tbody>
<tr>
<td>86.3</td>
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</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  

**Test Date:** Mar-2018  
**Hardware Availability:** Sep-2017  
**Software Availability:** Feb-2018

---

### Peak Optimization Flags (Continued)

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
- `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch`
- `-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3`
- `-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`
- `-align array32byte`

---

### Peak Other Flags

**C benchmarks:**
- `-m64 -std=c11`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64 -std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64 -std=c11`

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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