## Dell Inc.

**PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)**

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s 600</td>
<td>4.04</td>
<td>4.17</td>
</tr>
<tr>
<td>gcc_s 602</td>
<td>3.35</td>
<td>4.72</td>
</tr>
<tr>
<td>mcf_s 605</td>
<td>4.60</td>
<td>5.85</td>
</tr>
<tr>
<td>omnetpp_s 620</td>
<td>4.03</td>
<td>5.82</td>
</tr>
<tr>
<td>xalancbmk_s 623</td>
<td>4.48</td>
<td>5.82</td>
</tr>
<tr>
<td>x264_s 625</td>
<td>4.70</td>
<td>5.82</td>
</tr>
<tr>
<td>deepsjeng_s 631</td>
<td>2.54</td>
<td>2.52</td>
</tr>
<tr>
<td>leela_s 641</td>
<td>1.98</td>
<td>1.97</td>
</tr>
<tr>
<td>exchange2_s 648</td>
<td>6.18</td>
<td>8.12</td>
</tr>
<tr>
<td>xz_s 657</td>
<td>6.18</td>
<td>8.35</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name**: Intel Xeon Bronze 3104
- **Max MHz.**: 1700
- **Nominal**: 1700
- **Enabled**: 12 cores, 2 chips
- **Orderable**: 1,2 chips
- **Cache L1**: 32 KB I + 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 8.25 MB I+D on chip per chip
- **Other**: None
- **Memory**: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)
- **Storage**: 1 TB SATA SSD
- **Other**: None

**Software**

- **OS**: SUSE Linux Enterprise Server 12 SP3 (x86_64) 4.4.114-94.11-default
- **Compiler**: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel**: Yes
- **Firmware**: Version 1.3.7 released Feb-2018
- **File System**: btrfs
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 32/64-bit
- **Other**: jemalloc memory allocator library V5.0.1
# SPEC CPU2017 Integer Speed Result

## Dell Inc.

PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

**SPECspeed2017_int_base** = 4.04

**SPECspeed2017_int_peak** = 4.17

#### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>12</td>
<td>643</td>
<td>2.76</td>
<td>634</td>
<td>2.80</td>
<td>633</td>
<td>2.81</td>
<td>12</td>
<td>539</td>
<td>3.29</td>
<td>530</td>
<td>3.35</td>
<td>529</td>
<td>3.35</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>12</td>
<td>857</td>
<td>4.65</td>
<td>872</td>
<td>4.56</td>
<td>866</td>
<td>4.60</td>
<td>12</td>
<td>840</td>
<td>4.74</td>
<td>845</td>
<td>4.71</td>
<td>844</td>
<td>4.72</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>12</td>
<td>807</td>
<td>5.85</td>
<td>808</td>
<td>5.84</td>
<td>802</td>
<td>5.89</td>
<td>12</td>
<td>812</td>
<td>5.82</td>
<td>820</td>
<td>5.76</td>
<td>806</td>
<td>5.86</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>12</td>
<td>545</td>
<td>2.99</td>
<td>568</td>
<td>2.87</td>
<td>572</td>
<td>2.85</td>
<td>12</td>
<td>564</td>
<td>2.89</td>
<td>538</td>
<td>3.03</td>
<td>535</td>
<td>3.05</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>12</td>
<td>316</td>
<td>4.49</td>
<td>316</td>
<td>4.48</td>
<td>318</td>
<td>4.46</td>
<td>12</td>
<td>297</td>
<td>4.77</td>
<td>297</td>
<td>4.77</td>
<td>297</td>
<td>4.77</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>12</td>
<td>375</td>
<td>4.70</td>
<td>375</td>
<td>4.70</td>
<td>375</td>
<td>4.70</td>
<td>12</td>
<td>375</td>
<td>4.70</td>
<td>376</td>
<td>4.70</td>
<td>377</td>
<td>4.72</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>12</td>
<td>563</td>
<td>2.54</td>
<td>564</td>
<td>2.54</td>
<td>563</td>
<td>2.54</td>
<td>12</td>
<td>565</td>
<td>2.54</td>
<td>569</td>
<td>2.52</td>
<td>568</td>
<td>2.52</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>12</td>
<td>862</td>
<td>1.98</td>
<td>862</td>
<td>1.98</td>
<td>860</td>
<td>1.98</td>
<td>12</td>
<td>867</td>
<td>1.97</td>
<td>866</td>
<td>1.97</td>
<td>865</td>
<td>1.97</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>12</td>
<td>476</td>
<td>6.17</td>
<td>476</td>
<td>6.18</td>
<td>476</td>
<td>6.18</td>
<td>12</td>
<td>475</td>
<td>6.16</td>
<td>477</td>
<td>6.16</td>
<td>475</td>
<td>6.18</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>12</td>
<td>761</td>
<td>8.12</td>
<td>764</td>
<td>8.10</td>
<td>762</td>
<td>8.12</td>
<td>12</td>
<td>740</td>
<td>8.35</td>
<td>740</td>
<td>8.35</td>
<td>737</td>
<td>8.39</td>
</tr>
</tbody>
</table>

**SPECspeed2017_int_base** = 4.04

**SPECspeed2017_int_peak** = 4.17

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets; jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5; jemalloc: sources available via jemalloc.net

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
- sync; echo 3> /proc/sys/vm/drop_caches
Dell Inc.

PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

SPEC CPU2017 Integer Speed Result

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SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
2  "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1696.031

Platform Notes

BIOS settings:
Sub NUMA Cluster disabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1EE disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bce091c0f
running on linux-ejwa Thu Feb 15 02:05:36 2018
**SPEC CPU2017 Integer Speed Result**

**Dell Inc.**

PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
</table>
| 4.04                   | 4.17                   

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Feb-2018
Hardware Availability: Sep-2017
Tested by: Dell Inc.
Software Availability: Sep-2017

**Platform Notes (Continued)**

- **BogoMIPS:** 3392.06
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 8448K
- **NUMA node0 CPU(s):** 0,2,4,6,8,10
- **NUMA node1 CPU(s):** 1,3,5,7,9,11

**Flags:** fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcpid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb invpcid_single pln pts dtherm intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority vpd fpswb fpgasbase tsc_adjust bmi1 hle avx2 smep bmi2 ets invpcid rtm cqm mpx avx512f p fvdx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xgetbv1 cqm_llc cqm_occup_llc lpft ospke

/proces/cpuinfo cache data
  cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10
  node 0 size: 95349 MB
  node 0 free: 71909 MB
  node 1 cpus: 1 3 5 7 9 11
  node 1 size: 96750 MB
  node 1 free: 93556 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 196709968 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
  SUSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.

(Continued on next page)
#### Dell Inc.
PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

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</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Platform Notes (Continued)**

```
# Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-ejwa 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 15 01:43

SPEC is set to: /root/cpu2017
Filesystem     Type   Size  Used Avail Use% Mounted on
/dev/sda3      btrfs  855G   36G  820G   5% /
```

Additional information from `dmidecode` follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**BIOS** Dell Inc. 1.3.7 02/09/2018

**Memory:**
- 3x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666, configured at 2133
- 9x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2133
- 4x Not Specified Not Specified

(End of data from `sysinfo` program)

---

### Compiler Version Notes

```
<table>
<thead>
<tr>
<th>Compiler Version</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s(base)</td>
<td>602.gcc_s(base)</td>
</tr>
<tr>
<td>icc (ICC) 18.0.0 20170811</td>
<td></td>
</tr>
</tbody>
</table>

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

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```
<table>
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Dell Inc.
PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

SPEC CPU2017 Integer Speed Result

SPECspeed2017_int_base = 4.04
SPECspeed2017_int_peak = 4.17

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Feb-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
641.leela_s(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64

(Continued on next page)
Dell Inc.
PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

SPECspeed2017_int_base = 4.04
SPECspeed2017_int_peak = 4.17

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Base Portability Flags (Continued)

623.xalanbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Peak Compiler Invocation

C benchmarks:
icc
Dell Inc.
PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

SPECspeed2017_int_base = 4.04
SPECspeed2017_int_peak = 4.17

CPU2017 License: 55
Test Sponsor: Dell Inc.
Hardware Availability: Sep-2017
Test Date: Feb-2018
Tested by: Dell Inc.
Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Dell Inc.
PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)

SPECspeed2017_int_base = 4.04
SPECspeed2017_int_peak = 4.17

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Feb-2018
Tested by: Dell Inc.
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

641.leela_s: -m64

Fortran benchmarks (except as noted below):

The flags files that were used to format this result can be browsed at
## SPEC CPU2017 Integer Speed Result

**Dell Inc.**

**PowerEdge M640 (Intel Xeon Bronze 3104, 1.70 GHz)**

<table>
<thead>
<tr>
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<tr>
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</table>

You can also download the XML flags sources by saving the following links:

- [Dell-Platform-Flags-PowerEdge14G-revC.xml](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml)

### SPECspeed2017_int_base = 4.04

### SPECspeed2017_int_peak = 4.17

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