## SPEC® CPU2017 Integer Speed Result

**Huawei**

Huawei 1288H V5 (Intel Xeon Silver 4110)

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</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Silver 4110
- **Max MHz.:** 3000
- **Nominal:** 2100
- **Enabled:** 16 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 11 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 1200 GB SAS, 10000 RPM
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 0.31 Released Sep-2017
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1

---

**SPECspeed2017_int_base = 6.89**

**SPECspeed2017_int_peak = 7.12**
Huawei

Huawei 1288H V5 (Intel Xeon Silver 4110)

SPEC CPU2017 Integer Speed Result

Huawei

Huawei 1288H V5 (Intel Xeon Silver 4110)

SPECspeed2017_int_base = 6.89

SPECspeed2017_int_peak = 7.12

CPU2017 License: 3175
Test Date: Jan-2018
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Sep-2017

Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,
and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.

(Continued on next page)
Huawei

Huawei 1288H V5 (Intel Xeon Silver 4110)  SPECspeed2017_int_base = 6.89
SPECspeed2017_int_peak = 7.12

<table>
<thead>
<tr>
<th>CPU2017 License: 3175</th>
<th>Test Date: Jan-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Huawei</td>
<td>Hardware Availability: Jul-2017</td>
</tr>
<tr>
<td>Tested by: Huawei</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

**General Notes (Continued)**

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

**Platform Notes**

BIOS configuration:
Power Efficiency Mode Set to Custom
Hyper-Threading Set to Disable
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-hyq4 Wed Jan 31 22:23:44 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16

(Continued on next page)
## SPEC CPU2017 Integer Speed Result

**Huawei**

**Huawei 1288H V5 (Intel Xeon Silver 4110)**

<table>
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<tr>
<th>SPECspeed2017_int_base</th>
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<tr>
<td>6.89</td>
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**CPU2017 License:** 3175  
**Test Date:** Jan-2018  
**Test Sponsor:** Huawei  
**Hardware Availability:** Jul-2017  
**Tested by:** Huawei  
**Software Availability:** Sep-2017

**Platform Notes (Continued)**

- On-line CPU(s) list: 0-15
- Thread(s) per core: 1
- Core(s) per socket: 8
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
- Stepping: 4
- CPU MHz: 1100.000
- CPU max MHz: 2101.0000
- CPU min MHz: 800.0000
- BogoMIPS: 4199.99
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 11264K
- NUMA node0 CPU(s): 0-7
- NUMA node1 CPU(s): 8-15
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsdp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtral pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pni pclmulqdq dtes64_64 monitoring perfmonitor64 smep cmip smt dpm cmvlprem cmvlpso fpu vme de pse tsc msr pae mce cx8  

```
 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.  
 available: 2 nodes (0-1)  
 node 0 cpus: 0 1 2 3 4 5 6 7  
 node 0 size: 191498 MB  
 node 0 free: 190368 MB  
 node 1 cpus: 8 9 10 11 12 13 14 15  
 node 1 size: 193412 MB  
 node 1 free: 192378 MB  
 node distances:  
 node 0 1  
 0: 10 21  
 1: 21 10  
```

(Continued on next page)
Huawei

Huawei 1288H V5 (Intel Xeon Silver 4110)

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<tbody>
<tr>
<td>SPECspeed2017_int_base</td>
<td>6.89</td>
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</tbody>
</table>

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**Platform Notes (Continued)**

From /proc/meminfo

MemTotal: 394148704 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-hyq4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 31 15:53

SPEC is set to: /spec2017

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<th>Type</th>
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<th>Used</th>
<th>Avail</th>
<th>Use%</th>
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<td>/dev/sda2</td>
<td>xfs</td>
<td>828G</td>
<td>57G</td>
<td>772G</td>
<td>7%</td>
<td>/</td>
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</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.31 09/29/2017

Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

---

**Compiler Version Notes**

CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)

(Continued on next page)
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**Huawei 1288H V5 (Intel Xeon Silver 4110)**

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**CPU2017 License:** 3175

**Test Sponsor:** Huawei

**Test Date:** Jan-2018

**Hardware Availability:** Jul-2017

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**Software Availability:** Sep-2017

### Compiler Version Notes (Continued)

```plaintext
peak) 657.xz_s(base)

ICC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)

ICC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)

ICPC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
641.leela_s(peak)

ICPC (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

IFORT (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

**C benchmarks:**
- icc

**C++ benchmarks:**
- icpc

**Fortran benchmarks:**
- ifort
SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldeps -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64
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### Peak Compiler Invocation

**C benchmarks:**  
```  
icc  
```

**C++ benchmarks:**  
```  
icpc  
```

**Fortran benchmarks:**  
```  
ifort  
```

### Peak Portability Flags

- `600.perlbench_s`: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- `602.gcc_s`: `-DSPEC_LP64`
- `605.mcf_s`: `-DSPEC_LP64`
- `620.omnetpp_s`: `-DSPEC_LP64`
- `623.xalancbmk_s`: `-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
- `625.x264_s`: `-DSPEC_LP64`
- `631.deepsjeng_s`: `-DSPEC_LP64`
- `641.leela_s`: `-DSPEC_LP64`
- `648.exchange2_s`: `-DSPEC_LP64`
- `657.xz_s`: `-DSPEC_LP64`

### Peak Optimization Flags

**C benchmarks:**

```  
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -Qopenmp -DSPEC_OPENMP -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc  
```

```  
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -Qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc  
```

```  
605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -o3 -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -Qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc  
```
SPEC CPU2017 Integer Speed Result

Huawei
Huawei 1288H V5 (Intel Xeon Silver 4110)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>6.89</td>
<td>7.12</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jan-2018
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

-64
-std=c11

C++ benchmarks (except as noted below):

-64

623.xalancbmk_s: -m32

Fortran benchmarks:

-64

Peak Other Flags

C benchmarks:

-64

C++ benchmarks (except as noted below):

-64

623.xalancbmk_s: -m32

Fortran benchmarks:

-64
Huawei

Huawei 1288H V5 (Intel Xeon Silver 4110)  SPECspeed2017_int_base = 6.89  SPECspeed2017_int_peak = 7.12

CPU2017 License: 3175  Test Date: Jan-2018
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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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