# Lenovo Global Technology

**ThinkSystem SR570**  
*(1.70 GHz, Intel Xeon Bronze 3104)*

<table>
<thead>
<tr>
<th>Software Availability: Sep-2017</th>
<th>CPU2017 License: 9017</th>
<th>Tested by: Lenovo Global Technology</th>
<th>Test Sponsor: Lenovo Global Technology</th>
<th>Test Date: Jan-2018</th>
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<table>
<thead>
<tr>
<th>SPECspeed2017_int_base = 4.12</th>
<th>SPECspeed2017_int_peak = 4.23</th>
</tr>
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</table>

<table>
<thead>
<tr>
<th><strong>Hardware</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Xeon Bronze 3104</td>
</tr>
<tr>
<td><strong>Max MHz.:</strong> 1700</td>
</tr>
<tr>
<td><strong>Nominal:</strong> 1700</td>
</tr>
<tr>
<td><strong>Enabled:</strong> 12 cores, 2 chips</td>
</tr>
<tr>
<td><strong>Orderable:</strong> 1.2 chips</td>
</tr>
<tr>
<td><strong>Cache L1:</strong> 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>L2:</strong> 1 MB I+D on chip per core</td>
</tr>
<tr>
<td><strong>L3:</strong> 8.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
</tr>
<tr>
<td><strong>Memory:</strong> 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)</td>
</tr>
<tr>
<td><strong>Storage:</strong> 1 x 800 GB SAS SSD</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
</tr>
</tbody>
</table>

| **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64) |
| **Kernel:** 4.4.73-5-defautl |
| **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux |
| **Parallel:** Yes |
| **Firmware:** Lenovo BIOS Version TEE119J 1.20 released Sep-2017 |
| **File System:** btrfs |
| **System State:** Run level 3 (multi-user) |
| **Base Pointers:** 64-bit |
| **Peak Pointers:** 32/64-bit |
| **Other:** jemalloc: jemalloc memory allocator library V5.0.1 |

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![Graph showing SPEC performance results for various benchmarks.](image)

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![Table showing SPEC speed results.](image)
**SPEC CPU2017 Integer Speed Result**

**Lenovo Global Technology**

**ThinkSystem SR570**

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<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td>600.perlbench_s</td>
<td>12</td>
<td>628</td>
<td>2.83</td>
<td>629</td>
<td>2.82</td>
<td>626</td>
<td>2.83</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>12</td>
<td>860</td>
<td>4.63</td>
<td>863</td>
<td>4.61</td>
<td>861</td>
<td>4.63</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>12</td>
<td>811</td>
<td>5.82</td>
<td>809</td>
<td>5.84</td>
<td>807</td>
<td>5.85</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>12</td>
<td>545</td>
<td>2.99</td>
<td>562</td>
<td>2.90</td>
<td>544</td>
<td>3.00</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>12</td>
<td>317</td>
<td>4.48</td>
<td>319</td>
<td>4.44</td>
<td>320</td>
<td>4.42</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>12</td>
<td>325</td>
<td>5.43</td>
<td>325</td>
<td>5.43</td>
<td>325</td>
<td>5.42</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>12</td>
<td>569</td>
<td>2.52</td>
<td>569</td>
<td>2.52</td>
<td>569</td>
<td>2.52</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>12</td>
<td>856</td>
<td>1.99</td>
<td>855</td>
<td>1.99</td>
<td>855</td>
<td>1.99</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>12</td>
<td>482</td>
<td>6.10</td>
<td>477</td>
<td>6.16</td>
<td>479</td>
<td>6.14</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>12</td>
<td>741</td>
<td>8.34</td>
<td>745</td>
<td>8.29</td>
<td>742</td>
<td>8.34</td>
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</tbody>
</table>

**SPECspeed2017_int_base = 4.12**

**SPECspeed2017_int_peak = 4.23**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

- `LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"`
- `LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"`
- `OMP_STACKSIZE = "192M"`

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;

jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;


No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
**SPEC CPU2017 Integer Speed Result**

**Lenovo Global Technology**

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

---

### General Notes (Continued)

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page. The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

### Platform Notes

**BIOS configuration:**
- Choose Operating Mode set to Maximum Performance
- MONITOR/WAIT set to Enable
- Adjacent Cache Prefetch set to Disable
- XPT Prefetcher set to Enable
- LLC dead line alloc set to Disable
- Uncore Frequency Scaling set to disable
- Patrol Scrub set to disable

Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f  
runtime on linux-3fwh Fri Jan 12 22:22:00 2018

**SUT (System Under Test) info as seen by some common utilities.**
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
- 2 "physical id"s (chips)
- 12 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 6
  - siblings : 6
  - physical 0: cores 0 1 2 3 4 5
  - physical 1: cores 0 1 2 3 4 5

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 12

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(Continued on next page)
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SPEC CPU2017 Integer Speed Result

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Lenovo Global Technology

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CPU2017 License: 9017
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Platform Notes (Continued)

On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1696.003
BogoMIPS: 3392.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-5
NUMA node 0 CPU(s): 0-5
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref perf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb plc pts dtherm intel_pt
tpr_shadow vmm vmptrld cvttsidmulq fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
ers invpcid rdtsc cmpl mpx avx512f avx512fdx simd adx smap clflushopt clwb avx512cd
avx512bw avx512v1 xsaveopt xsaveopt xsave xgetbv1 cmq_l1c cmq_msr cmq_occu_l1c pku ospke

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5
  node 0 size: 96060 MB
  node 0 free: 95655 MB
  node 1 cpus: 6 7 8 9 10 11
  node 1 size: 96750 MB
  node 1 free: 96390 MB
  node distances:
    node 0 1
      0: 10 21
      1: 21 10

From /proc/meminfo

(Continued on next page)
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<td></td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

```
MemTotal: 197438180 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
```

From `/etc/*release` /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

`uname -a`:

```
Linux linux-3fwh 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4) x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 12 22:17
```

**SPEC is set to:** /home/cpu2017.1.0.2.ic18.0

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 744G 20G 724G 3% /home
```

Additional information from dmidecode follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS Lenovo -[TEE119J-1.20]- 09/06/2017
- Memory:
  - 4x NO DIMM NO DIMM
  - 12x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2133

*(End of data from sysinfo program)*

**Compiler Version Notes**

```
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
```

*(Continued on next page)*
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**Test Date:** Jan-2018  
**Tested by:** Lenovo Global Technology  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

### Compiler Version Notes (Continued)

---

```plaintext
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
  641.leela_s(base)
------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
  641.leela_s(peak)
------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC  648.exchange2_s(base, peak)
------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

**C benchmarks:**  
- icc

**C++ benchmarks:**  
- icpc

**Fortran benchmarks:**  
- ifort
Lenovo Global Technology
ThinkSystem SR570
(1.70 GHz, Intel Xeon Bronze 3104)

SPECspeed2017_int_base = 4.12
SPECspeed2017_int_peak = 4.23

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Nov-2017
Software Availability: Sep-2017

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalanbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64
Lenovo Global Technology

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**SPECs2017_int_base = 4.12**

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<td>Sep-2017</td>
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</table>

**Peak Compiler Invocation**

C benchmarks:
- icc

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

**Peak Portability Flags**

600.perlbmk_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

**Peak Optimization Flags**

C benchmarks:

600.perlbmk_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
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---

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**Peak Optimization Flags (Continued)**

625.x264_s: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
`-qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP`  
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`  
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`  
`-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP`  
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

623.xalancbmk_s: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`  
`-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`  
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`  
`-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP`  
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

623.xalancbmk_s: `-m32`

**Peak Other Flags**

C benchmarks:

`-m64 -std=c11`

C++ benchmarks (except as noted below):

`-m64`

623.xalancbmk_s: `-m32`

Fortran benchmarks:

`-m64`
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- Lenovo Global Technology

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- Sep-2017

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-01-12 09:21:59-0500.  
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