**Lenovo Global Technology**  
*ThinkSystem SR650 (2.10 GHz, Intel Xeon Silver 4110)*

<table>
<thead>
<tr>
<th>Spec Speed2017_fp_base</th>
<th>60.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Spec Speed2017_fp_peak</td>
<td>61.7</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jan-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Sep-2017

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_peak (61.7)</th>
</tr>
</thead>
<tbody>
<tr>
<td>16</td>
<td>603.bwaves_s</td>
</tr>
<tr>
<td></td>
<td>607.cactuBSSN_s</td>
</tr>
<tr>
<td>16</td>
<td>619.ibm_s</td>
</tr>
<tr>
<td></td>
<td>621.wrf_s</td>
</tr>
<tr>
<td>16</td>
<td>627.cam4_s</td>
</tr>
<tr>
<td></td>
<td>628.pop2_s</td>
</tr>
<tr>
<td>16</td>
<td>638.imagick_s</td>
</tr>
<tr>
<td></td>
<td>644.nab_s</td>
</tr>
<tr>
<td>16</td>
<td>649.fotonik3d_s</td>
</tr>
<tr>
<td></td>
<td>654.roms_s</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Silver 4110  
- **Max MHz.:** 3000  
- **Nominal:** 2100  
- **Enabled:** 16 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 11 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 800 GB SAS SSD  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Kernel:** 4.4.21-69-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
- **Compiler for Linux:**  
- **Fortran:** Version 18.0.0.128 of Intel Fortran  
- **Compiler for Linux:**  
- **Parallel:** Yes  
- **Firmware:** Lenovo BIOS Version IVE111C 1.00 released Jul-2017  
- **File System:** btrfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
## Lenovo Global Technology

ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>185</td>
<td>319</td>
<td>186</td>
<td>318</td>
<td><strong>185</strong></td>
<td><strong>318</strong></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>232</td>
<td>71.8</td>
<td>233</td>
<td>71.6</td>
<td><strong>233</strong></td>
<td><strong>71.7</strong></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>16</td>
<td>160</td>
<td>32.8</td>
<td>160</td>
<td>32.8</td>
<td><strong>160</strong></td>
<td><strong>32.8</strong></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>271</td>
<td>48.8</td>
<td>273</td>
<td>48.5</td>
<td><strong>272</strong></td>
<td><strong>48.6</strong></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>16</td>
<td><strong>268</strong></td>
<td><strong>33.1</strong></td>
<td>268</td>
<td>33.1</td>
<td>268</td>
<td>33.0</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>16</td>
<td>270</td>
<td>44.0</td>
<td>268</td>
<td>44.3</td>
<td><strong>270</strong></td>
<td><strong>44.0</strong></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>16</td>
<td><strong>343</strong></td>
<td><strong>42.1</strong></td>
<td>343</td>
<td>42.1</td>
<td>344</td>
<td>42.0</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>16</td>
<td><strong>229</strong></td>
<td><strong>76.1</strong></td>
<td>229</td>
<td>76.1</td>
<td>229</td>
<td>76.1</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>16</td>
<td>155</td>
<td>58.8</td>
<td>153</td>
<td>59.5</td>
<td><strong>154</strong></td>
<td><strong>59.3</strong></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>16</td>
<td>254</td>
<td>62.1</td>
<td>252</td>
<td>62.4</td>
<td>252</td>
<td>62.5</td>
</tr>
</tbody>
</table>

**Specspeed2017_fp_base = 60.2**  
**Specspeed2017_fp_peak = 61.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

Memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 60.2
SPECspeed2017_fp_peak = 61.7

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITORMWAIT set to Enable
Adjacent Cache Prefetch set to Disable
XPT Prefetcher set to Enable
Stale AtoS set to Enable
DCA set to Enable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Cyborg-SPECcpu2006-SUSE12SP2 Fri Jan 5 00:08:15 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:   0-15
Thread(s) per core:    1
Core(s) per socket:    8
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

**SPECspeed2017_fp_base** = 60.2
**SPECspeed2017_fp_peak** = 61.7

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jan-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.072
BogoMIPS: 4190.14
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrunc pdcm pcid dca ssse3 sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt tpr_shadow vmm xsaveopt xsaves xsaveopt xsaves xgetbv1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data
  cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7
  node 0 size: 193111 MB
  node 0 free: 190858 MB
  node 1 cpus: 8 9 10 11 12 13 14 15
  node 1 size: 193504 MB
  node 1 free: 191768 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 395894464 KB
  HugePages_Total: 0
  Hugepagesize: 2048 KB

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 60.2
SPECspeed2017_fp_peak = 61.7

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux Cyborg-SPECcpu2006-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC
  2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 4 18:28

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Lenovo -[IVE111C-1.00]- 07/17/2017
  Memory:
    24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
----------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

SPEC CPU2017 Floating Point Speed Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed2017_fp_base = 60.2
SPECspeed2017_fp_peak = 61.7

CPUT2017 License: 9017
Test Sponsor: Lenovo Global Technology
Hardware Availability: Aug-2017
Test Date: Jan-2018
Tested by: Lenovo Global Technology
Software Availability: Sep-2017

Compiler Version Notes (Continued)

==============================================================================
CC  619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
FC  607.cactuBSSN_s(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811

(Continued on next page)
# SPEC CPU2017 Floating Point Speed Result

**Lenovo Global Technology**

ThinkSystem SR650  
(2.10 GHz, Intel Xeon Silver 4110)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>60.2</td>
<td>61.7</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jan-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Sep-2017

## Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:

```
icc
```

Fortran benchmarks:

```ifort```

Benchmarks using both Fortran and C:

```fort gin```

Benchmarks using Fortran, C, and C++:

```icpc gic ifort```

## Base Portability Flags

- `603.bwaves_s`: `-DSPEC_LP64`  
- `607.cactuBSSN_s`: `-DSPEC_LP64`  
- `619.lbm_s`: `-DSPEC_LP64`  
- `621.wrf-s`: `-DSPEC_LP64`  
- `627.cam4_s`: `-DSPEC_LP64`  
- `628.pop2_s`: `-DSPEC_LP64`  
- `638.imagick_s`: `-DSPEC_LP64`  
- `644.nab_s`: `-DSPEC_LP64`  
- `649.fotonik3d_s`: `-DSPEC_LP64`  
- `654.roms_s`: `-DSPEC_LP64`  
`-DSPEC_CASE_FLAG`  
`-convert big_endian`  
`-assume byterecl`
## Lenovo Global Technology

**ThinkSystem SR650**

(2.10 GHz, Intel Xeon Silver 4110)

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Test Date</th>
<th>Hardware Availability</th>
<th>Software Availability</th>
</tr>
</thead>
<tbody>
<tr>
<td>9017</td>
<td>Jan-2018</td>
<td>Aug-2017</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Tested by</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lenovo Global Technology</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

### SPEC CPU2017 Floating Point Speed Result

**SPECspeed2017_fp_base = 60.2**

**SPECspeed2017_fp_peak = 61.7**

---

## Base Optimization Flags

**C benchmarks:**

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
```

**Fortran benchmarks:**

```
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte
```

**Benchmarks using both Fortran and C:**

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
```

**Benchmarks using Fortran, C, and C++:**

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
```

---

## Base Other Flags

**C benchmarks:**

```
-m64 -std=c11
```

**Fortran benchmarks:**

```
-m64
```

**Benchmarks using both Fortran and C:**

```
-m64 -std=c11
```

**Benchmarks using Fortran, C, and C++:**

```
-m64 -std=c11
```

---

## Peak Compiler Invocation

**C benchmarks:**

```
icc
```

**Fortran benchmarks:**

```
ifort
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 60.2
SPECspeed2017_fp_peak = 61.7

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(2.10 GHz, Intel Xeon Silver 4110)

SPECspeed2017_fp_base = 60.2
SPECspeed2017_fp_peak = 61.7

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
<th>Test Date:</th>
<th>Jan-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

Peak Optimization Flags (Continued)

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-01-04 11:08:14-0500.
Originally published on 2018-03-06.