# SPEC® CPU2017 Floating Point Rate Result



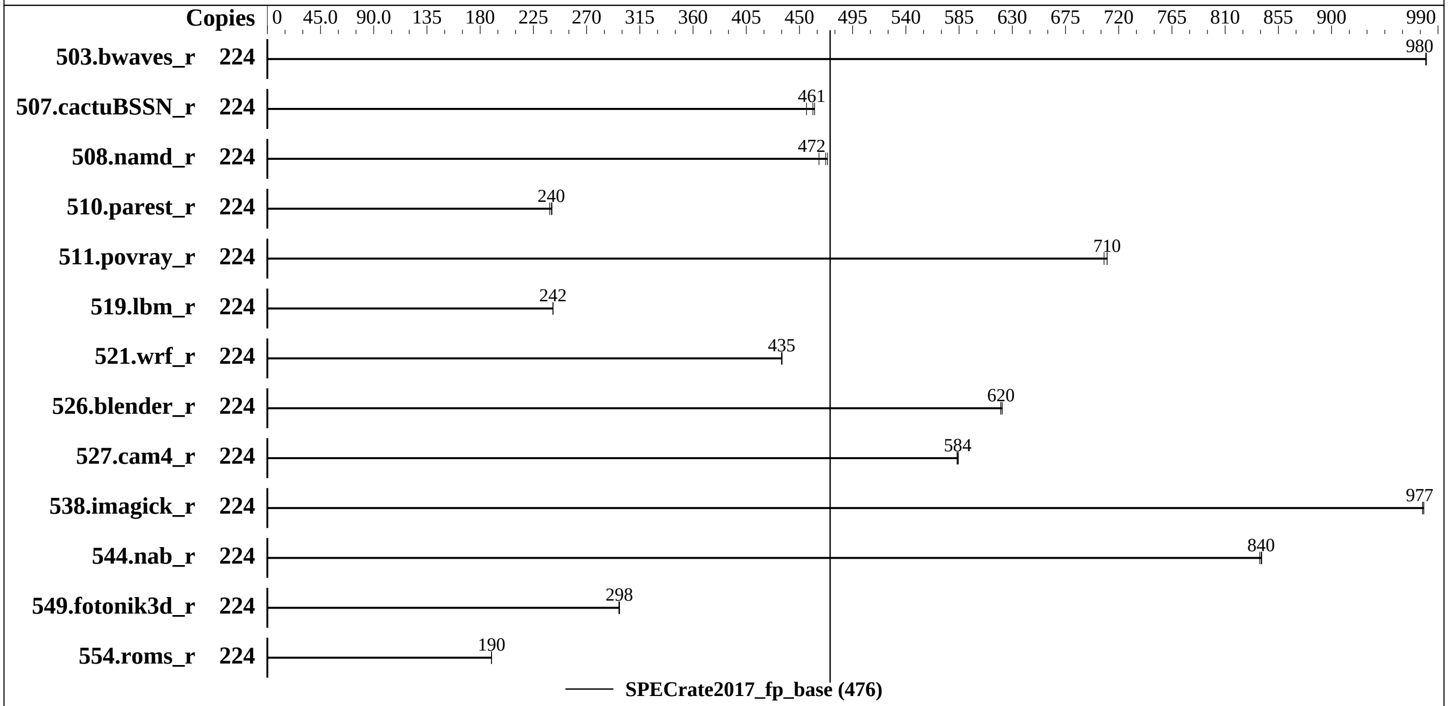
## Supermicro

SuperServer 2049U-TR4
(X11QPH+, Intel Xeon Platinum 8180)

SPECrate2017_fp_base = 476

SPECrate2017_fp_peak = Not Run

| | | | |
|---|---|---|---|
| **CPU2017 License:** | 001176 | **Test Date:** | Dec-2017 |
| **Test Sponsor:** | Supermicro | **Hardware Availability:** | Dec-2017 |
| **Tested by:** | Supermicro | **Software Availability:** | Sep-2017 |




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Platinum 8180 |
| Max MHz.: | 3800 |
| Nominal: | 2500 |
| Enabled: | 112 cores, 4 chips, 2 threads/core |
| Orderable: | 1,2,4 chips |
| Cache L1: | 32 KB I + 32 KB D on chip per core |
| L2: | 1 MB I+D on chip per core |
| L3: | 38.5 MB I+D on chip per chip |
| Other: | None |
| Memory: | 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R) |
| Storage: | 1 x 1.5 TB PCIe NVMe SSD |
| Other: | None |

### Software

| | |
|---|---|
| OS: | SUSE Linux Enterprise Server 12 SP2<br>4.4.21-69-default |
| Compiler: | C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;<br>Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux |
| Parallel: | No |
| Firmware: | Supermicro BIOS version 2.0a released Dec-2017 |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | Not Applicable |
| Other: | None |

# SPEC CPU2017 Floating Point Rate Result



## Supermicro

SuperServer 2049U-TR4
(X11QPH+, Intel Xeon Platinum 8180)

SPECrate2017_fp_base = 476

SPECrate2017_fp_peak = Not Run

**CPU2017 License:** 001176
**Test Sponsor:** Supermicro
**Tested by:** Supermicro

**Test Date:** Dec-2017
**Hardware Availability:** Dec-2017
**Software Availability:** Sep-2017

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 224 | 2292 | 980 | 2293 | 980 | **2292** | **980** | | | | | | | |
| 507.cactuBSSN_r | 224 | **615** | **461** | 613 | 463 | 622 | 456 | | | | | | | |
| 508.namd_r | 224 | 449 | 474 | **451** | **472** | 456 | 467 | | | | | | | |
| 510.parest_r | 224 | 2434 | 241 | 2454 | 239 | **2439** | **240** | | | | | | | |
| 511.povray_r | 224 | **736** | **710** | 736 | 710 | 739 | 708 | | | | | | | |
| 519.lbm_r | 224 | **977** | **242** | 978 | 242 | 977 | 242 | | | | | | | |
| 521.wrf_r | 224 | **1154** | **435** | 1153 | 435 | 1154 | 435 | | | | | | | |
| 526.blender_r | 224 | 549 | 622 | 550 | 620 | **550** | **620** | | | | | | | |
| 527.cam4_r | 224 | 670 | 584 | **671** | **584** | 672 | 583 | | | | | | | |
| 538.imagick_r | 224 | 570 | 978 | **570** | **977** | 570 | 977 | | | | | | | |
| 544.nab_r | 224 | 448 | 841 | **449** | **840** | 449 | 839 | | | | | | | |
| 549.fotonik3d_r | 224 | **2932** | **298** | 2936 | 297 | 2931 | 298 | | | | | | | |
| 554.roms_r | 224 | 1877 | 190 | 1876 | 190 | **1876** | **190** | | | | | | | |

**SPECrate2017_fp_base = 476**

**SPECrate2017_fp_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/cpu2017-1.0.2/lib/ia32:/home/cpu2017-1.0.2/lib/intel64:
 /home/cpu2017-1.0.2/je5.0.1-32:/home/cpu2017-1.0.2/je5.0.1-64"
 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>spec.cpu2017.notes_plat_000: BIOS Settings:
```

## Platform Notes

```
Intel Virtualization Technology= Disabled
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
Energy Performance BIAS setting = Performance
SNC = Enable
IMC Interleaving = 1-way Interleave
Stale AtoS = Enable
LLC dead line alloc = Disable
 Sysinfo program /home/cpu2017-1.0.2/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on linux-8u2w Fri Dec 22 09:14:51 2017

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
       4  "physical id"s (chips)
       224 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 28
       siblings  : 56
       physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
       physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
       physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
       physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30

 From lscpu:
      Architecture:          x86_64
      CPU op-mode(s):        32-bit, 64-bit
      Byte Order:            Little Endian
      CPU(s):                224
      On-line CPU(s) list:   0-223
      Thread(s) per core:    2
      Core(s) per socket:    28
      Socket(s):             4
      NUMA node(s):          8
      Vendor ID:             GenuineIntel
      CPU family:            6
      Model:                 85
      Model name:            Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
```

**(Continued on next page)**

## Platform Notes (Continued)

```
     Stepping:              4
     CPU MHz:               1000.000
     CPU max MHz:           2501.0000
     CPU min MHz:           1000.0000
     BogoMIPS:              4988.07
     Virtualization:        VT-x
     L1d cache:             32K
     L1i cache:             32K
     L2 cache:              1024K
     L3 cache:              39424K
     NUMA node0 CPU(s):     0-3,7-9,14-17,21-23,112-115,119-121,126-129,133-135
     NUMA node1 CPU(s):     4-6,10-13,18-20,24-27,116-118,122-125,130-132,136-139
     NUMA node2 CPU(s):     28-31,35-37,42-45,49-51,140-143,147-149,154-157,161-163
     NUMA node3 CPU(s):     32-34,38-41,46-48,52-55,144-146,150-153,158-160,164-167
     NUMA node4 CPU(s):     56-59,63-65,70-73,77-79,168-171,175-177,182-185,189-191
     NUMA node5 CPU(s):     60-62,66-69,74-76,80-83,172-174,178-181,186-188,192-195
     NUMA node6 CPU(s):     84-87,91-93,98-101,105-107,196-199,203-205,210-213,217-219
     NUMA node7 CPU(s):     88-90,94-97,102-104,108-111,200-202,206-209,214-216,220-223
     Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
     aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
     fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
     xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
     tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
     erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
     avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
    cache size : 39424 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 7 8 9 14 15 16 17 21 22 23 112 113 114 115 119 120 121 126 127 128
   129 133 134 135
   node 0 size: 192103 MB
   node 0 free: 184132 MB
   node 1 cpus: 4 5 6 10 11 12 13 18 19 20 24 25 26 27 116 117 118 122 123 124 125 130 131
   132 136 137 138 139
   node 1 size: 193528 MB
   node 1 free: 187469 MB
   node 2 cpus: 28 29 30 31 35 36 37 42 43 44 45 49 50 51 140 141 142 143 147 148 149 154
   155 156 157 161 162 163
   node 2 size: 193528 MB
   node 2 free: 187402 MB
   node 3 cpus: 32 33 34 38 39 40 41 46 47 48 52 53 54 55 144 145 146 150 151 152 153 158
```

**(Continued on next page)**

## Platform Notes (Continued)

```
    159 160 164 165 166 167
    node 3 size: 193528 MB
    node 3 free: 187516 MB
    node 4 cpus: 56 57 58 59 63 64 65 70 71 72 73 77 78 79 168 169 170 171 175 176 177 182
    183 184 185 189 190 191
    node 4 size: 193528 MB
    node 4 free: 187523 MB
    node 5 cpus: 60 61 62 66 67 68 69 74 75 76 80 81 82 83 172 173 174 178 179 180 181 186
    187 188 192 193 194 195
    node 5 size: 193528 MB
    node 5 free: 187413 MB
    node 6 cpus: 84 85 86 87 91 92 93 98 99 100 101 105 106 107 196 197 198 199 203 204 205
    210 211 212 213 217 218 219
    node 6 size: 193528 MB
    node 6 free: 187506 MB
    node 7 cpus: 88 89 90 94 95 96 97 102 103 104 108 109 110 111 200 201 202 206 207 208
    209 214 215 216 220 221 222 223
    node 7 size: 193525 MB
    node 7 free: 187542 MB
    node distances:
    node   0   1   2   3   4   5   6   7
      0:  10  11  21  21  21  21  21  21
      1:  11  10  21  21  21  21  21  21
      2:  21  21  10  11  21  21  21  21
      3:  21  21  11  10  21  21  21  21
      4:  21  21  21  21  10  11  21  21
      5:  21  21  21  21  11  10  21  21
      6:  21  21  21  21  21  21  10  11
      7:  21  21  21  21  21  21  11  10

  From /proc/meminfo
     MemTotal:       1583923224 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12 SP2

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
```

**(Continued on next page)**

## Platform Notes (Continued)

```
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-8u2w 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 22 04:01 last=5

SPEC is set to: /home/cpu2017-1.0.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/nvme0n1p4 xfs   1.5T  446G 1002G  31% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS American Megatrends Inc. 2.0a 12/05/2017
  Memory:
   48x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666

(End of data from sysinfo program)
```

## Compiler Version Notes

```
==============================================================================
 CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
```

(Continued on next page)

## Compiler Version Notes (Continued)

```
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  507.cactuBSSN_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

**(Continued on next page)**

# Base Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

# Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

# Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs

**(Continued on next page)**

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

## Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revB.xml

Tested with SPEC CPU2017 v1.0.2 on 2017-12-22 12:14:51-0500.
Report generated on 2018-10-31 17:01:57 by CPU2017 PS formatter v6067.
Originally published on 2018-01-10.