**Dell Inc.**

**PowerEdge R440 (Intel Xeon Silver 4114, 2.20 GHz)**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result (Copies)</th>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>83.3</td>
<td>93.1</td>
<td>Not Run</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>116</td>
<td></td>
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<tr>
<td>505.mcf_r</td>
<td>61.0</td>
<td>185</td>
<td>10.0</td>
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<tr>
<td>520.omnetpp_r</td>
<td>95.0</td>
<td></td>
<td>20.0</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>116</td>
<td></td>
<td>30.0</td>
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<tr>
<td>525.x264_r</td>
<td>61.0</td>
<td>185</td>
<td>40.0</td>
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<tr>
<td>531.deepsjeng_r</td>
<td>80.8</td>
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<td>50.0</td>
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<tr>
<td>541.leela_r</td>
<td>74.3</td>
<td>180</td>
<td>60.0</td>
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<td>548.exchange2_r</td>
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<td>70.0</td>
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<tr>
<td>557.xz_r</td>
<td>63.5</td>
<td></td>
<td>80.0</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Silver 4114
- **Max MHz.:** 3000
- **Nominal:** 2200
- **Enabled:** 20 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 13.75 MB I+D on chip per chip
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Storage:** 480 GB SATA SSD
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** No
- **Firmware:** Version 1.3.1 released Sep-2017
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1;
  jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
  jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
  jemalloc: sources available via jemalloc.net
Dell Inc.

PowerEdge R440 (Intel Xeon Silver 4114, 2.20 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Oct-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>385</td>
<td>182</td>
<td>390</td>
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<tr>
<td>531.deepsjeng_r</td>
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<td>554</td>
<td>82.7</td>
<td>568</td>
<td>80.7</td>
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<tr>
<td>541.leela_r</td>
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<td>74.3</td>
<td>891</td>
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<td>40</td>
<td>597</td>
<td>176</td>
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<td>175</td>
<td>598</td>
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<tr>
<td>557.xz_r</td>
<td>40</td>
<td>625</td>
<td>69.1</td>
<td>681</td>
<td>63.5</td>
<td>683</td>
<td>63.3</td>
</tr>
</tbody>
</table>

Specific CPU2017_int_base = 93.1
Specific CPU2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Platform Notes

BIOS settings:
Logical Processor Enabled

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Dell Inc.

PowerEdge R440 (Intel Xeon Silver 4114, 2.20 GHz)

SPECrate2017_int_base = 93.1
SPECrate2017_int_peak = Not Run

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Virtualization Technology Disabled
Sub NUMA Cluster Enabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C1E Disabled
C States set to Autonomous
Uncore Frequency set to Dynamic
Memory Patrol Scrub Disabled
Energy Efficiency Policy set to Performance
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-j1sy Tue Oct 31 15:28:44 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
Stepping: 4
CPU MHz: 2194.947
BogoMIPS: 4389.89

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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SPECrate2017_int_base = 93.1

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<td>Tested by: Dell Inc.</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38
NUMA node1 CPU(s): 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 33, 35, 37, 39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl apic sm xtopology nonstop_tsc
aen mpmpref perf_master perf_pmu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtop pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
ptr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavevc xgetbv1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data
    cache size : 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38
    node 0 size: 95269 MB
    node 0 free: 94809 MB
    node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39
    node 1 size: 96615 MB
    node 1 free: 96161 MB
    node distances:
        node 0 1
        0: 10 21
        1: 21 10

From /proc/meminfo
    MemTotal: 196489992 KB
    HugePages_Total: 0
    Hugepagesize: 2048 KB

/usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2

(Continued on next page)
Dell Inc.

PowerEdge R440 (Intel Xeon Silver 4114, 2.20 GHz)

SPECrate2017_int_base = 93.1

SPECrate2017_int_peak = Not Run

CPU2017 License: 55
Test Sponsor: Dell Inc.
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Software Availability: Sep-2017

Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux linux-j1sy 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 31 15:26

SPEC is set to: /root/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 371G 30G 342G 8% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.2.10 10/16/2017
Memory:
  12x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400
  4x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
    557.xz_r(base)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
    541.leela_r(base)
==============================================================================

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SPEC CPU2017 Integer Rate Result

Dell Inc.
PowerEdge R440 (Intel Xeon Silver 4114, 2.20 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Software Availability: Sep-2017

Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---------------------------------------------------------------------
FC 548.exchange2_r(base)
---------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
- icc

C++ benchmarks:
- icpc

Fortran benchmarks:
- ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
- -W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
# SPEC CPU2017 Integer Rate Result

**Dell Inc.**

PowerEdge R440 (Intel Xeon Silver 4114, 2.20 GHz)

| SPECrate2017_int_base = | 93.1 |
| SPECrate2017_int_peak = | Not Run |

**CPU2017 License:** 55  
**Test Date:** Oct-2017  
**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Sep-2017

**Tested by:** Dell Inc.  
**Software Availability:** Sep-2017

## Base Optimization Flags (Continued)

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`  
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

## Base Other Flags

**C benchmarks:**
- `-m64 -std=c11`

**C++ benchmarks:**
- `-m64`

**Fortran benchmarks:**
- `-m64`

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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