



# SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen10

(2.00 GHz, Intel Xeon Platinum 8164)

**SPECSspeed2017\_fp\_base = 119**

**SPECSspeed2017\_fp\_peak = 122**

CPU2017 License: 3

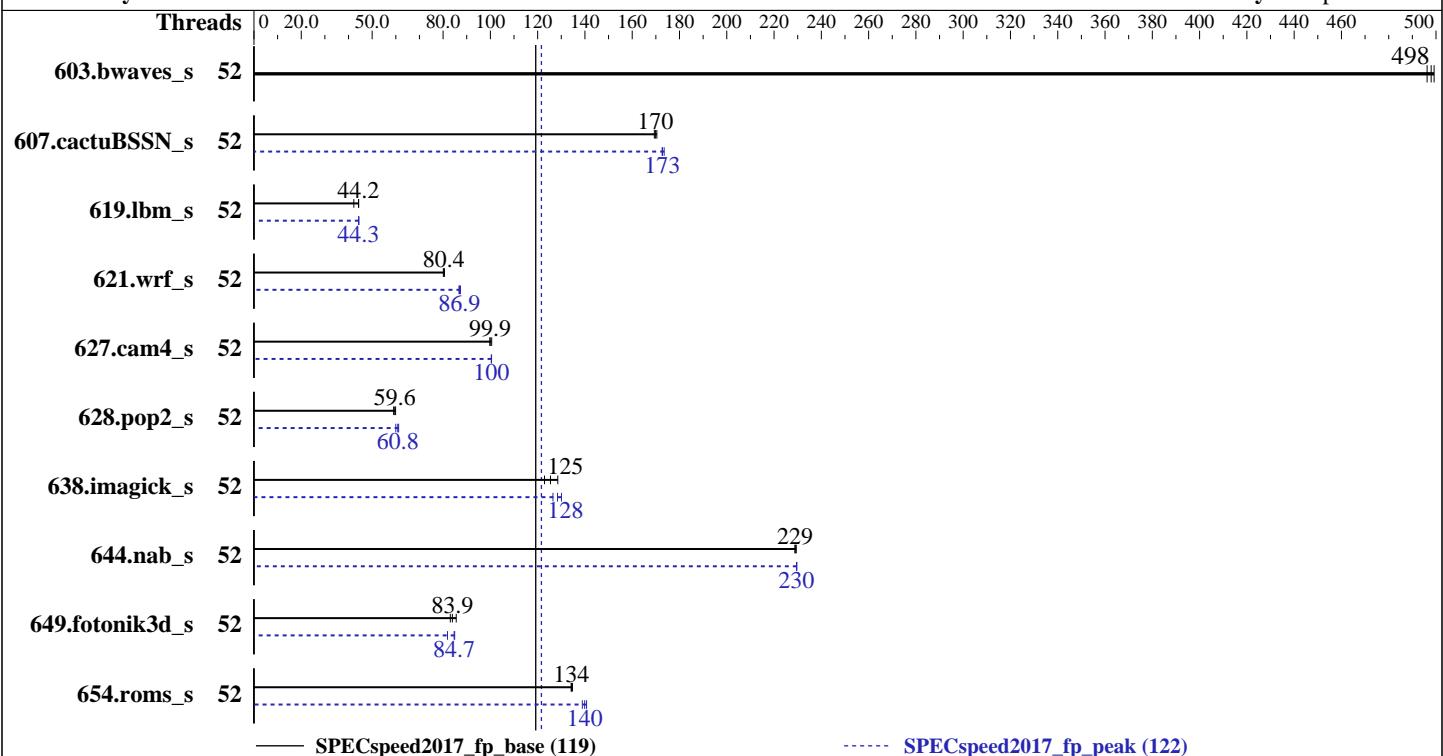
**Test Date:** Oct-2017

**Test Sponsor:** HPE

**Hardware Availability:** Oct-2017

**Tested by:** HPE

**Software Availability:** Sep-2017



— SPECSspeed2017\_fp\_base (119)

----- SPECSspeed2017\_fp\_peak (122)

## Hardware

CPU Name: Intel Xeon Platinum 8164  
 Max MHz.: 3700  
 Nominal: 2000  
 Enabled: 52 cores, 2 chips  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 35.75 MB I+D on chip per chip  
 Other: None  
 Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)  
 Storage: 1 x 960 GB SATA SSD, RAID 0  
 Other: None

## Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo), Kernel 3.10.0-514.6.1.el7.x86\_64  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: HPE BIOS Version U30 released Oct-2017 (tested with U30 9/29/2017)  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	52	<b>118</b>	<b>498</b>	119	496	118	499	52	<b>118</b>	<b>498</b>	119	496	118	499
607.cactuBSSN_s	52	<b>98.1</b>	<b>170</b>	97.8	170	98.4	169	52	<b>96.5</b>	<b>173</b>	96.5	173	96.1	174
619.lbm_s	52	118	44.3	124	42.2	<b>118</b>	<b>44.2</b>	52	<b>118</b>	<b>44.3</b>	118	44.4	118	44.3
621.wrf_s	52	<b>165</b>	<b>80.4</b>	164	80.6	165	80.1	52	152	86.7	<b>152</b>	<b>86.9</b>	151	87.4
627.cam4_s	52	88.8	99.8	88.1	101	<b>88.7</b>	<b>99.9</b>	52	<b>88.2</b>	<b>100</b>	88.3	100	88.2	101
628.pop2_s	52	<b>199</b>	<b>59.6</b>	201	59.1	198	59.9	52	198	60.0	<b>195</b>	<b>60.8</b>	194	61.2
638.imagick_s	52	117	123	112	129	<b>115</b>	<b>125</b>	52	111	130	<b>112</b>	<b>128</b>	114	126
644.nab_s	52	76.2	229	76.4	229	<b>76.3</b>	<b>229</b>	52	<b>76.1</b>	<b>230</b>	76.1	230	76.1	230
649.fotonik3d_s	52	<b>109</b>	<b>83.9</b>	110	83.1	107	85.6	52	<b>108</b>	<b>84.7</b>	107	84.9	111	81.9
654.roms_s	52	117	134	<b>117</b>	<b>134</b>	117	135	52	<b>113</b>	<b>140</b>	112	141	113	139
<b>SPECSpeed2017_fp_base = 119</b>														
<b>SPECSpeed2017_fp_peak = 122</b>														

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
```

## General Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=core,compact"

LD\_LIBRARY\_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64"

LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/spec2017/je5.0.1-32:/spec2017/je5.0.1-64"

OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4

## Platform Notes

BIOS Configuration:

Intel Hyperthreading set to Disabled

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

LLC Prefetcher set to Enabled

LLC Dead Line Allocation set to Disabled

Workload Pofile set to General Peak Frequency Compute

Energy/Performance Bias set to Maximum Performance

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## Platform Notes (Continued)

Uncore Frequency Scaling set to Auto

Workload Pofile set to General Peak Frequency Compute

NUMA Group Size Optimization set to Flat

Sysinfo program /spec2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on DL380-sys2-RHEL73 Mon Oct 9 01:48:40 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz
  2 "physical id"s (chips)
  52 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 26
  siblings   : 26
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
  29
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28
  29
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Byte Order:           Little Endian
CPU(s):               52
On-line CPU(s) list: 0-51
Thread(s) per core:  1
Core(s) per socket:  26
Socket(s):            2
NUMA node(s):         2
Vendor ID:            GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz
Stepping:              4
CPU MHz:              2000.000
BogoMIPS:             4004.75
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:              1024K
L3 cache:              36608K
```

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## Platform Notes (Continued)

NUMA node0 CPU(s): 0-25  
NUMA node1 CPU(s): 26-51

/proc/cpuinfo cache data  
cache size : 36608 KB

From numactl --hardware    WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)  
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25  
node 0 size: 97963 MB  
node 0 free: 94207 MB  
node 1 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51  
node 1 size: 98303 MB  
node 1 free: 95190 MB  
node distances:  
node 0 1  
0: 10 21  
1: 21 10

From /proc/meminfo  
MemTotal: 197569560 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*  
os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.3 (Maipo)"  
ID="rhel"  
ID\_LIKE="fedora"  
VERSION\_ID="7.3"  
PRETTY\_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"  
ANSI\_COLOR="0;31"  
CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.3:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.3:ga:server

uname -a:  
Linux DL380-sys2-RHEL73 3.10.0-514.6.1.el7.x86\_64 #1 SMP Sat Dec 10 11:15:38 EST 2016  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Oct 8 20:27

SPEC is set to: /spec2017

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## Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	889G	29G	861G	4%	/

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U30 09/29/2017

Memory:

24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666

(End of data from sysinfo program)

## Compiler Version Notes

=====

CC 619.lbm\_s(base) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)

=====

-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-----

=====

CC 619.lbm\_s(peak)

=====

-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-----

=====

FC 607.cactuBSSN\_s(base)

=====

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icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

-----

=====

FC 607.cactuBSSN\_s(peak)

=====

-----

icpc (ICC) 18.0.0 20170811  
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## Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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=====

FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)

---

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

=====

FC 603.bwaves\_s(peak) 649.fotonik3d\_s(peak) 654.roms\_s(peak)

---

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

=====

CC 621.wrf\_s(base) 627.cam4\_s(base, peak) 628.pop2\_s(base)

---

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

---

=====

CC 621.wrf\_s(peak) 628.pop2\_s(peak)

---

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

icc (ICC) 18.0.0 20170811

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## Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP

Fortran benchmarks:

-DSPEC\_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP  
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP  
-nostandard-realloc-lhs -align array32byte



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## Base Other Flags

C benchmarks:

-m64 -std=c11

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11

## Peak Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP
```

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## Peak Optimization Flags (Continued)

638.imagick\_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC\_OPENMP

644.nab\_s: Same as 638.imagick\_s

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC\_SUPPRESS\_OPENMP  
-DSPEC\_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-qopenmp -nostandard-realloc-lhs -align array32byte

654.roms\_s: Same as 649.fotonik3d\_s

Benchmarks using both Fortran and C:

621.wrf\_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC\_SUPPRESS\_OPENMP -qopenmp  
-DSPEC\_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4\_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC\_OPENMP -nostandard-realloc-lhs -align array32byte

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch  
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC\_SUPPRESS\_OPENMP -qopenmp -DSPEC\_OPENMP -nostandard-realloc-lhs  
-align array32byte

## Peak Other Flags

C benchmarks:

-m64 -std=c11

Fortran benchmarks:

-m64

(Continued on next page)



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## Peak Other Flags (Continued)

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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