### SPEC® CFP2006 Result

**Hewlett Packard Enterprise**  
*(Test Sponsor: HPE)*  
Synergy 480 Gen10  
*(3.60 GHz, Intel Xeon Gold 5122)*

<table>
<thead>
<tr>
<th>SPECfp®_rate2006</th>
<th>Not Run</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>545</td>
</tr>
</tbody>
</table>

**Test Sponsor:** HPE  
**Hardware Availability:** Nov-2017  
**Software Availability:** Apr-2017  
**Test date:** Dec-2017

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>HPE</td>
</tr>
<tr>
<td>Tested by:</td>
<td>HPE</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5122  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
- **CPU MHz:** 3600  
- **FPU:** Integrated  
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1, 2 chip(s)  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 1 MB I+D on chip per core

### Software

- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP2  
  Kernel 4.4.21-69-default  
- **Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
  Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
- **Auto Parallel:** No  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)

---

**Copies**

<table>
<thead>
<tr>
<th>Program</th>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>16</td>
</tr>
<tr>
<td>416.gamess</td>
<td>16</td>
</tr>
<tr>
<td>433.milc</td>
<td>16</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>16</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>16</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>16</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>16</td>
</tr>
<tr>
<td>444.namd</td>
<td>16</td>
</tr>
<tr>
<td>447.dealII</td>
<td>16</td>
</tr>
<tr>
<td>450.soplex</td>
<td>16</td>
</tr>
<tr>
<td>453.povray</td>
<td>16</td>
</tr>
<tr>
<td>454.calculix</td>
<td>16</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>16</td>
</tr>
<tr>
<td>465.tonto</td>
<td>16</td>
</tr>
<tr>
<td>470.lbm</td>
<td>16</td>
</tr>
<tr>
<td>481.wrf</td>
<td>16</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>16</td>
</tr>
</tbody>
</table>

**SPECfp_rate_base2006 = 545**

---

Continued on next page
Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10
(3.60 GHz, Intel Xeon Gold 5122)

**SPEC CFP2006 Result**

- **CPU2006 license:** 3
- **Test sponsor:** HPE
- **Tested by:** HPE

---

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>16</td>
<td>314</td>
<td>693</td>
<td>314</td>
<td>693</td>
<td>313</td>
<td>694</td>
</tr>
<tr>
<td>416.gamess</td>
<td>16</td>
<td>706</td>
<td>444</td>
<td>445</td>
<td>705</td>
<td>444</td>
<td></td>
</tr>
<tr>
<td>433.milc</td>
<td>16</td>
<td>194</td>
<td>757</td>
<td>194</td>
<td>756</td>
<td>194</td>
<td>757</td>
</tr>
<tr>
<td>434.reusmp</td>
<td>16</td>
<td>231</td>
<td>631</td>
<td>630</td>
<td>229</td>
<td>635</td>
<td></td>
</tr>
<tr>
<td>435.gromacs</td>
<td>16</td>
<td>194</td>
<td>589</td>
<td>201</td>
<td>568</td>
<td>200</td>
<td>572</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>16</td>
<td>287</td>
<td>666</td>
<td>290</td>
<td>660</td>
<td>287</td>
<td>665</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>16</td>
<td>406</td>
<td>371</td>
<td>407</td>
<td>369</td>
<td>403</td>
<td>373</td>
</tr>
<tr>
<td>444.namd</td>
<td>16</td>
<td>362</td>
<td>355</td>
<td>361</td>
<td>355</td>
<td>362</td>
<td>355</td>
</tr>
<tr>
<td>447.dealII</td>
<td>16</td>
<td>250</td>
<td>731</td>
<td>250</td>
<td>733</td>
<td>250</td>
<td>733</td>
</tr>
<tr>
<td>450.soplex</td>
<td>16</td>
<td>356</td>
<td>375</td>
<td>356</td>
<td>374</td>
<td>357</td>
<td>373</td>
</tr>
<tr>
<td>453.povray</td>
<td>16</td>
<td>134</td>
<td>634</td>
<td>134</td>
<td>634</td>
<td>134</td>
<td>635</td>
</tr>
<tr>
<td>454.calculix</td>
<td>16</td>
<td>184</td>
<td>718</td>
<td>185</td>
<td>713</td>
<td>185</td>
<td>712</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>16</td>
<td>552</td>
<td>307</td>
<td>552</td>
<td>308</td>
<td>552</td>
<td>308</td>
</tr>
<tr>
<td>465.tonto</td>
<td>16</td>
<td>296</td>
<td>532</td>
<td>293</td>
<td>537</td>
<td>289</td>
<td>545</td>
</tr>
<tr>
<td>470.lbm</td>
<td>16</td>
<td>382</td>
<td>576</td>
<td>382</td>
<td>575</td>
<td>382</td>
<td>575</td>
</tr>
<tr>
<td>481.wrf</td>
<td>16</td>
<td>222</td>
<td>807</td>
<td>222</td>
<td>804</td>
<td>223</td>
<td>802</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>16</td>
<td>650</td>
<td>479</td>
<td>649</td>
<td>481</td>
<td>647</td>
<td>482</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option ‘submit’ was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
Continued on next page
Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
runtime on sy480_m3_sles Fri Dec 22 12:59:12 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
   2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 4
      siblings : 8
      physical 0: cores 1 5 9 13
      physical 1: cores 1 5 9 13
   cache size : 16896 KB

From /proc/meminfo
   MemTotal: 395931368 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10
(3.60 GHz, Intel Xeon Gold 5122)

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 545

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Apr-2017

Platform Notes (Continued)

ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux sy480_m3_sles 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
      (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 22 03:32
SPEC is set to: /home/cpu2006
Filesysten Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 405G 107G 299G 27% /home

Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 11/14/2017
Memory:
   24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32;/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.htm.

Continued on next page
SPEC CFP2006 Result

Hewlett Packard Enterprise  
(Test Sponsor: HPE)
Synergy 480 Gen10  
(3.60 GHz, Intel Xeon Gold 5122)

SPECfp_rate2006 = Not Run  
SPECfp_rate_base2006 = 545

CPU2006 license: 3  
Test sponsor: HPE  
Tested by: HPE

Test date: Dec-2017  
Hardware Availability: Nov-2017  
Software Availability: Apr-2017

General Notes (Continued)

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Continued on next page
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10
(3.60 GHz, Intel Xeon Gold 5122)

SPECfp_rate2006 =  Not Run
SPECfp_rate_base2006 = 545

CPU2006 license: 3
Test date: Dec-2017
Test sponsor: HPE
Hardware Availability: Nov-2017
Tested by: HPE
Software Availability: Apr-2017

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 13 June 2018.