### SPEC® CFP2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL580 Gen10  
(2.30 GHz, Intel Xeon Gold 5118)

**SPECfp®_rate2006 = Not Run**  
**SPECfp_rate_base2006 = 1920**

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Oct-2017</th>
</tr>
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<tbody>
<tr>
<td>Test sponsor:</td>
<td>HPE</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 3  
**Tested by:** HPE

| Software | Operating System: | SUSE Linux Enterprise Server 12 (x86_64) SP3  
|----------|-------------------|
| Compiler: | C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |

#### Hardware

<table>
<thead>
<tr>
<th>Test</th>
<th>Copies</th>
<th>SPECfp_rate_base2006</th>
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<td>416.gamess</td>
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**Hardware**

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 5118</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2300</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>48 cores, 4 chips, 12 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1, 2, 4 chip(s)</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>1 MB I+D on chip per core</td>
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</tbody>
</table>

**Continued on next page**

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Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/  
Page 1
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds Base</th>
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<td><strong>1066</strong></td>
<td><strong>1760</strong></td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The `numactl` mechanism was used to bind copies to processors. The config file option 'submit' was used to generate `numactl` commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  

```
    shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
    runspec command invoked through numactl i.e.:  
    numactl --interleave=all runspec <etc>
    irqbalance disabled with "systemctl stop irqbalance"
```

Continued on next page
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL580 Gen10
(2.30 GHz, Intel Xeon Gold 5118)

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 1920

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Operating System Notes (Continued)

- Tuned profile set with "tuned-adm profile throughput-performance"
- VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
- Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Workload Profile set to General Throughput Compute
- Minimum Processor Idle Power Core C-State set to C1E State
- Sysinfo program /home/cpu2006/config/sysinfo.rev6993
- Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
- Running on linux-irgu Wed Oct 4 11:58:31 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
  - 4 "physical id"s (chips)
  - 96 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 12
  - siblings: 24
  - physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  - physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  - physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
  - physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
- cache size: 16896 KB

From /proc/meminfo
- MemTotal: 792281188 KB
- HugePages_Total: 0
- Hugepagesize: 2048 KB

/usr/bin/lsb_release -d
- SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
- SuSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 3
  - # This file is deprecated and will be removed in a future service pack or release.
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Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-irgu 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017
(b7ce4e4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 4 05:57

SPEC is set to: /home/cpu2006
Filesysterm Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 331G 117G 215G 36% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 08/18/2017
Memory:
48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on
past performance using the historical hardware and/or
software described on this result page.

Continued on next page
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General Notes (Continued)

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.htm.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamepp: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
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</table>

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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