



SPEC® CINT2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(2.10 GHz, Intel Xeon Gold 6152)

SPECint®2006 = Not Run

SPECint_base2006 = 77.0

CPU2006 license: 3

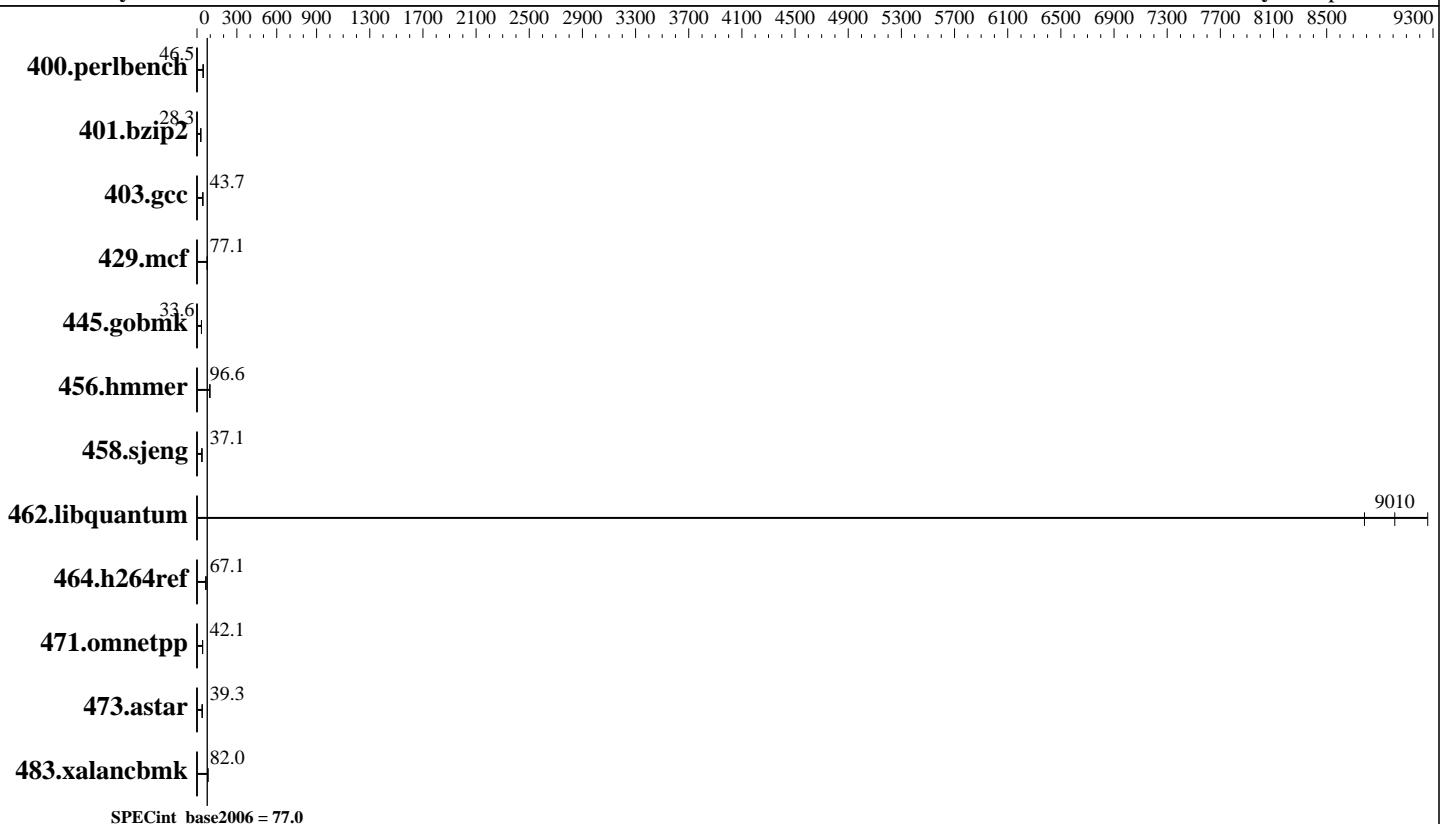
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6152
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 88 cores, 4 chips, 22 cores/chip
 CPU(s) orderable: 1, 2, 4 chip(s)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 30.25 MB I+D on chip per chip
 Other Cache: None
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
 Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 Compiler: Kernel 3.10.0-514.el7.x86_64
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(2.10 GHz, Intel Xeon Gold 6152)

SPECint2006 =

Not Run

SPECint_base2006 =

77.0

CPU2006 license: 3

Test date: Nov-2017

Test sponsor: HPE

Hardware Availability: Oct-2017

Tested by: HPE

Software Availability: Apr-2017

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	210	46.5	210	46.4	210	46.6						
401.bzip2	341	28.3	341	28.3	341	28.3						
403.gcc	184	43.7	184	43.7	184	43.7						
429.mcf	118	77.0	118	77.1	117	78.2						
445.gobmk	312	33.6	312	33.6	312	33.6						
456.hmmer	96.6	96.6	96.9	96.3	96.5	96.6						
458.sjeng	327	37.1	326	37.1	326	37.1						
462.libquantum	2.30	9010	2.24	9260	2.36	8780						
464.h264ref	330	67.1	330	67.0	329	67.2						
471.omnetpp	149	42.1	149	42.0	149	42.1						
473.astar	179	39.3	179	39.1	178	39.3						
483.xalancbmk	84.2	82.0	84.1	82.0	84.6	81.6						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

IRQ balance service was stop using "service irqbalance stop"

Tuned-adm profile was set to Throughput-Performance

Platform Notes

BIOS Configuration:

Intel Hyperthreading set to Disabled

Thermal Configuration set to Maximum Cooling

LLC Prefetch set to Enabled

LLC Dead Line Allocation set to Disabled

Stale A to S set to Enabled

Memory Patrol Scrubbing set to Disabled

Workload Profile set to General Peak Frequency Compute

Energy/Performance Bias set to Maximum Performance

Workload Profile set to Custom

NUMA Group Size Optimization set to Flat

Sysinfo program /home/cpu2006/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on localhost.localdomain Fri Nov 17 17:40:02 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(2.10 GHz, Intel Xeon Gold 6152)

SPECint2006 =

Not Run

SPECint_base2006 =

77.0

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date:

Nov-2017

Hardware Availability:

Oct-2017

Software Availability:

Apr-2017

Platform Notes (Continued)

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
        4 "physical id"s (chips)
        88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 22
    siblings   : 22
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
cache size : 30976 KB
```

```
From /proc/meminfo
MemTotal:      792069220 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.3 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.3"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 17 17:28
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb1        xfs   447G   8.6G  439G   2% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(2.10 GHz, Intel Xeon Gold 6152)

SPECint2006 =

Not Run

SPECint_base2006 =

77.0

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date:

Nov-2017

Hardware Availability:

Oct-2017

Software Availability:

Apr-2017

Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

OMP_NUM_THREADS = "88"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.htm>.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64



SPEC CINT2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(2.10 GHz, Intel Xeon Gold 6152)

SPECint2006 =

Not Run

SPECint_base2006 =

77.0

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date:

Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>



SPEC CINT2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10

(2.10 GHz, Intel Xeon Gold 6152)

SPECint2006 =

Not Run

SPECint_base2006 =

77.0

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date:

Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jun 14 11:29:28 2018 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 13 June 2018.