SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

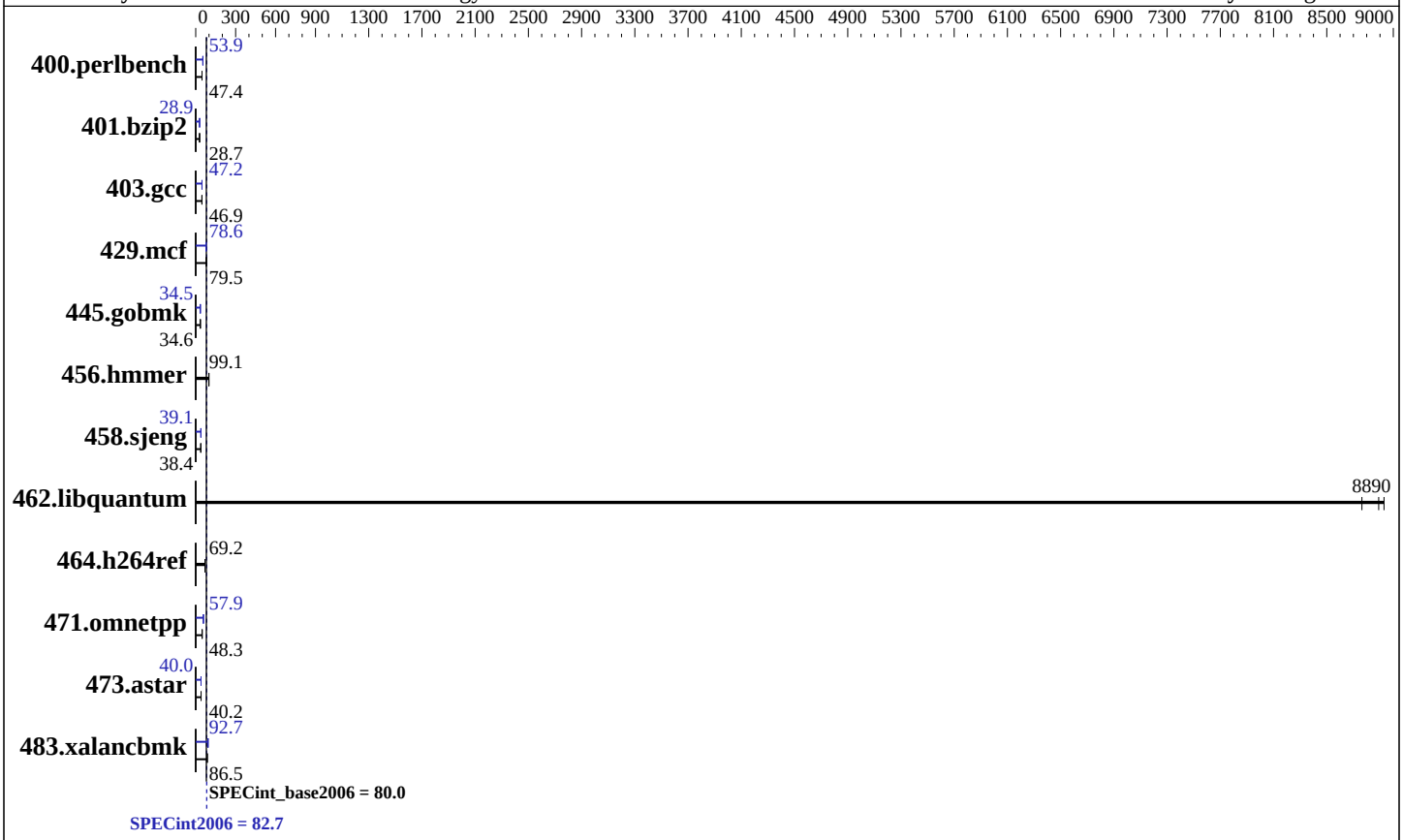
ThinkSystem SR860
(2.10 GHz, Intel Xeon Platinum 8176)

SPECint®2006 = 82.7

SPECint_base2006 = 80.0

CPU2006 license: 9017
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Aug-2017



Hardware

CPU Name:	Intel Xeon Platinum 8176
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	112 cores, 4 chips, 28 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	38.5 MB I+D on chip per chip
Other Cache:	None
Memory:	768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem:	1 x 800 GB SAS SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.4 (Maipo) Kernel 3.10.0-693.el7.x86_64
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860
(2.10 GHz, Intel Xeon Platinum 8176)

SPECint2006 = 82.7

SPECint_base2006 = 80.0

CPU2006 license: 9017
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Aug-2017

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**206**	**47.4**	206	47.4	207	47.2	181	54.0	181	53.8	**181**	**53.9**
401.bzip2	336	28.7	**336**	**28.7**	336	28.7	**334**	**28.9**	334	28.9	335	28.8
403.gcc	**172**	**46.9**	172	46.9	172	46.8	171	47.2	171	47.1	**171**	**47.2**
429.mcf	115	79.6	115	79.4	**115**	**79.5**	**116**	**78.6**	119	76.8	116	78.6
445.gobmk	**303**	**34.6**	303	34.6	303	34.6	304	34.5	304	34.6	**304**	**34.5**
456.hmmer	**94.2**	**99.1**	94.2	99.1	94.5	98.7	**94.2**	**99.1**	94.2	99.1	94.5	98.7
458.sjeng	315	38.4	315	38.5	**315**	**38.4**	**310**	**39.1**	310	39.1	310	39.1
462.libquantum	2.36	8760	**2.33**	**8890**	2.32	8930	2.36	8760	**2.33**	**8890**	2.32	8930
464.h264ref	320	69.2	**320**	**69.2**	320	69.2	320	69.2	**320**	**69.2**	320	69.2
471.omnetpp	130	48.1	**130**	**48.3**	129	48.3	**108**	**57.9**	108	58.0	110	57.0
473.astar	**175**	**40.2**	174	40.3	175	40.1	175	40.1	176	39.9	**175**	**40.0**
483.xalancbmk	79.7	86.6	**79.7**	**86.5**	80.2	86.1	74.9	92.1	**74.4**	**92.7**	74.3	92.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITORMWAIT set to Enable
Per Core P-state set to Disable
XPT Prefetcher set to Enable
 Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on SR860 Sat Dec  9 11:29:02 2017

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8176 CPU @ 2.10GHz
       4 "physical id"s (chips)
       112 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
```

Continued on next page

Platform Notes (Continued)

```
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 28
         siblings  : 28
         physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
         25 26 27 28 29 30
         physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
         25 26 27 28 29 30
         physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
         25 26 27 28 29 30
         physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
         25 26 27 28 29 30
      cache size : 39424 KB

   From /proc/meminfo
      MemTotal:       792255244 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   From /etc/*release* /etc/*version*
      os-release:
         NAME="Red Hat Enterprise Linux Server"
         VERSION="7.4 (Maipo)"
         ID="rhel"
         ID_LIKE="fedora"
         VARIANT="Server"
         VARIANT_ID="server"
         VERSION_ID="7.4"
         PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
      redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
      system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

   uname -a:
      Linux SR860 3.10.0-693.el7.x86_64 #1 SMP Thu Jul 6 19:56:57 EDT 2017 x86_64
      x86_64 x86_64 GNU/Linux

   run-level 3 Dec 9 11:27

   SPEC is set to: /home/cpu2006-1.2-ic17.0u3
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda4      xfs   686G  108G  579G  16% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS Lenovo -[TEE117I-1.10]- 10/19/2017
     Memory:
      48x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz
```

Continued on next page

Platform Notes (Continued)

```
(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,3"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"
OMP_NUM_THREADS = "112"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled by default.
 Filesystem page cache cleared with:
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-ra-region-strategy=block -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-E.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-E.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Dec 27 12:05:39 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 December 2017.