SPEC® CFP2006 Result

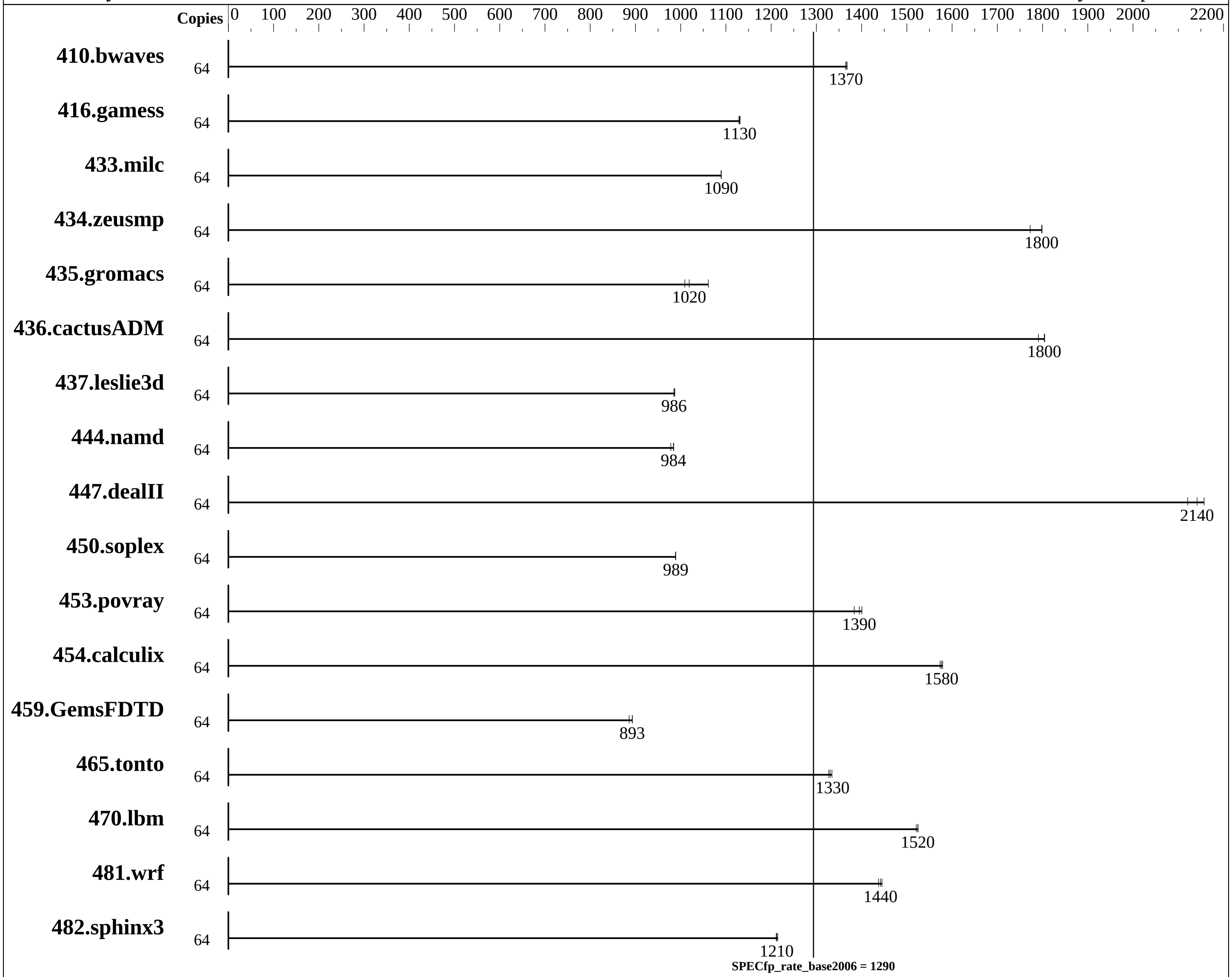
Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL385 Gen10
(2.20 GHz, AMD EPYC 7301)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 1290

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Nov-2017
Hardware Availability: Nov-2017
Software Availability: Sep-2017



Hardware

CPU Name:	AMD EPYC 7301
CPU Characteristics:	AMD Turbo CORE technology up to 2.70 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	32 cores, 2 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chip(s)
Primary Cache:	64 KB I + 32 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) SP3 Kernel 4.4.73-5-default
Compiler:	C/C++/Fortran: Version 4.5.2.1 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable

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L3 Cache: 64 MB I+D on chip per chip, 8 MB shared / 2 cores
Other Cache: None
Memory: 1 TB (16 x 64 GB 4Rx4 PC4-2666V-L)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Other Software: None

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	637	1360	**637**	**1370**	636	1370							
416.gamess	64	**1109**	**1130**	1108	1130	1110	1130							
433.milc	64	539	1090	**539**	**1090**	539	1090							
434.zeusmp	64	**324**	**1800**	324	1800	329	1770							
435.gromacs	64	431	1060	453	1010	**448**	**1020**							
436.cactusADM	64	427	1790	424	1800	**424**	**1800**							
437.leslie3d	64	611	985	610	987	**610**	**986**							
444.namd	64	525	978	**522**	**984**	521	985							
447.dealII	64	**342**	**2140**	339	2160	345	2120							
450.soplex	64	540	989	540	989	**540**	**989**							
453.povray	64	246	1380	**244**	**1390**	243	1400							
454.calculix	64	336	1570	**335**	**1580**	334	1580							
459.GemsFDTD	64	766	886	760	893	**760**	**893**							
465.tonto	64	472	1330	474	1330	**473**	**1330**							
470.lbm	64	578	1520	577	1520	**577**	**1520**							
481.wrf	64	**496**	**1440**	497	1440	495	1450							
482.sphinx3	64	1027	1210	**1029**	**1210**	1030	1210							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
```

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Operating System Notes (Continued)

```
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu
Linux governor set to performance with cpupower "cpupower frequency-set -r -g performance"
Transparent huge pages were enabled for this run (OS default)

Set vm/nr_hugepages=57344 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Platform Notes

```
 BIOS Configuration:
  Thermal Configuration set to Maximum Cooling
  Performance Determinism set to Power Deterministic
  Memory Patrol Scrubbing set to Disabled
  Workload Pofile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C6 State
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/home/cpu2006/amd1603-rate-libs-revB/32:/home/cpu2006/amd1603-rate-libs-revB/64"

The binaries were built with the x86 Open64 Compiler Suite,
which is only available from (and supported by) AMD at
http://developer.amd.com/tools-and-sdks/cpu-development/x86-open64-compiler-suite/
```

Base Compiler Invocation

C benchmarks:
`opencc`

C++ benchmarks:
`openCC`

Fortran benchmarks:
`openf95`

Benchmarks using both Fortran and C:
`opencc openf95`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`

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Base Portability Flags (Continued)

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm -WB, -Wl, -z,muldefs

C++ benchmarks:
-Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET -march=bdver2 -mno-fma4 -mno-xop -mno-tbm -WB, -Wl, -z,muldefs

Fortran benchmarks:
-Ofast -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2 -OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm -WB, -Wl, -z,muldefs

Benchmarks using both Fortran and C:
-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm -WB, -Wl, -z,muldefs -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2 -OPT:unroll_size=256

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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