



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant ML30 Gen9
(3.50 GHz, Intel Xeon E3-1230 v6)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 246

CPU2006 license: 3

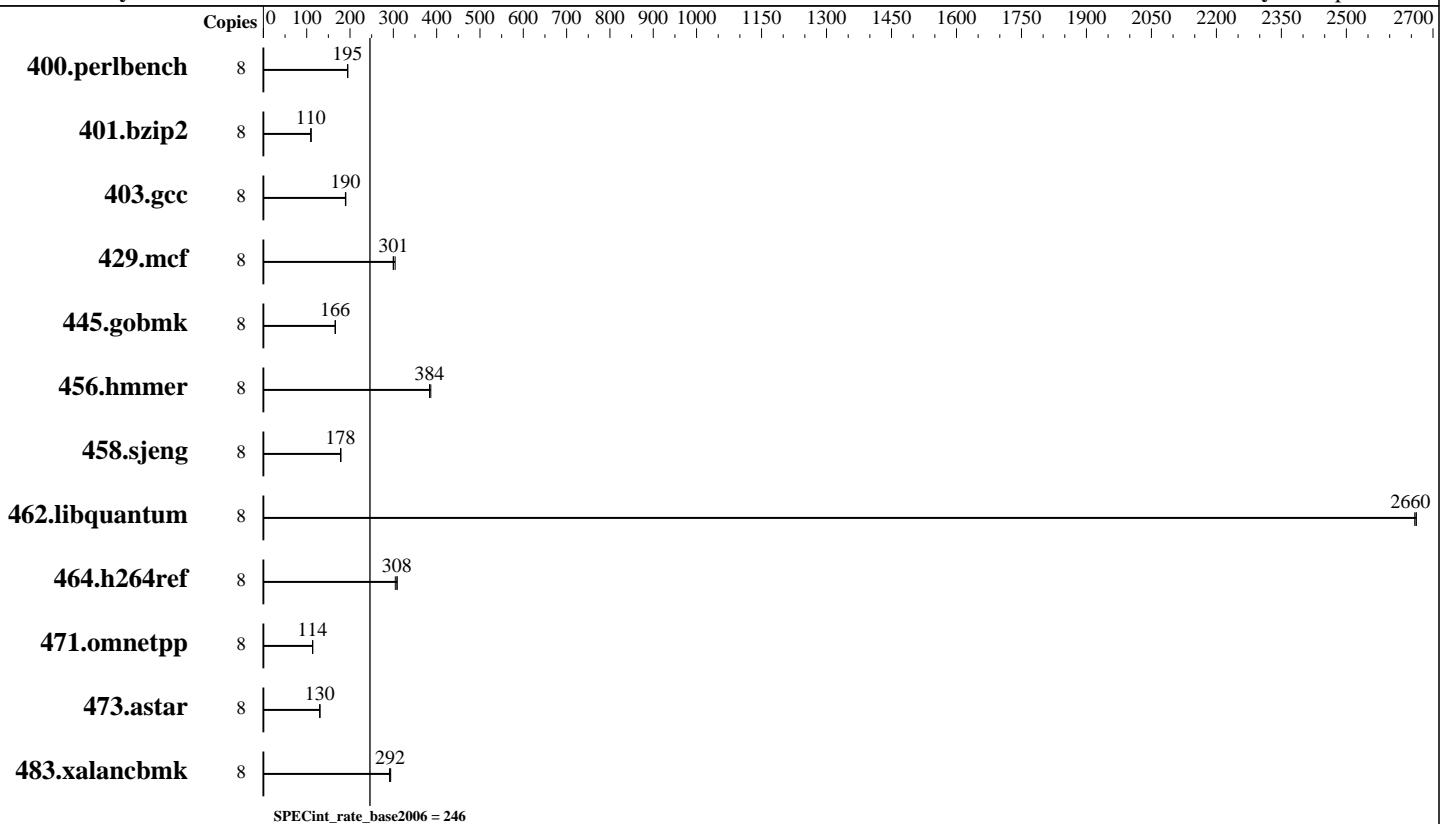
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017



Hardware

CPU Name: Intel Xeon E3-1230 v6
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2400T-E)
 Disk Subsystem: 1 x 800 GB SATA SSD, RAID 0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP3
 Kernel 4.4.73-5-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant ML30 Gen9
(3.50 GHz, Intel Xeon E3-1230 v6)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 246

CPU2006 license: 3

Test date: Nov-2017

Test sponsor: HPE

Hardware Availability: Oct-2017

Tested by: HPE

Software Availability: Sep-2017

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	402	195	402	194	401	195							
401.bzip2	8	702	110	705	110	702	110							
403.gcc	8	339	190	339	190	340	189							
429.mcf	8	243	300	243	301	240	304							
445.gobmk	8	505	166	505	166	506	166							
456.hmmer	8	195	384	193	386	195	384							
458.sjeng	8	543	178	543	178	541	179							
462.libquantum	8	62.4	2660	62.3	2660	62.3	2660							
464.h264ref	8	573	309	581	305	574	308							
471.omnetpp	8	440	114	440	114	440	114							
473.astar	8	431	130	431	130	432	130							
483.xalancbmk	8	189	291	188	294	189	292							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

Platform Notes

BIOS Configuration:
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh set to 1x Refresh
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-b589 Wed Nov 8 14:59:10 2017

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen9

(3.50 GHz, Intel Xeon E3-1230 v6)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 246

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E3-1230 v6 @ 3.50GHz
        1 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 8
    physical 0: cores 0 1 2 3
cache size : 6144 KB
```

```
From /proc/meminfo
MemTotal:       65752892 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
Linux linux-b589 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017
(b7ce4e4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 8 13:43
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda4        xfs   700G   6.3G  694G   1% /home
Additional information from dmidecode:
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen9

(3.50 GHz, Intel Xeon E3-1230 v6)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 246

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U23 10/02/2017

Memory:

4x HP 862690-091 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -D_FILE_OFFSET_BITS=64
  403.gcc: -D_FILE_OFFSET_BITS=64
  429.mcf: -D_FILE_OFFSET_BITS=64
 445.gobmk: -D_FILE_OFFSET_BITS=64
 456.hammer: -D_FILE_OFFSET_BITS=64
  458sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
 464.h264ref: -D_FILE_OFFSET_BITS=64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
  473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen9

(3.50 GHz, Intel Xeon E3-1230 v6)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 246

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>
<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-HSW-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>
<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-HSW-revG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Nov 29 12:30:09 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 November 2017.