**SPEC® CINT2006 Result**

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)

ProLiant DL360 Gen10
(2.60 GHz, Intel Xeon Gold 6142)

<table>
<thead>
<tr>
<th>Spec Test</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate2006</td>
<td>Not Run</td>
</tr>
<tr>
<td>SPECint_rate_base2006</td>
<td>1700</td>
</tr>
</tbody>
</table>

**Hardware**

<table>
<thead>
<tr>
<th>Description</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6142</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 3.70 GHz</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2600</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>32 cores, 2 chips, 16 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1, 2 chip(s)</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>22 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>1 x 480 GB SATA SSD, RAID 0</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Description</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>SUSE Linux Enterprise Server 12 (x86_64) SP2</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux</td>
</tr>
<tr>
<td>Auto Parallel</td>
<td>No</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other Software</td>
<td>Microquill SmartHeap V10.2</td>
</tr>
</tbody>
</table>

**Test Information**

- **CPU2006 license:** 3
- **Test sponsor:** HPE
- **Test date:** Nov-2017
- **Hardware Availability:** Oct-2017
- **Tested by:** HPE
- **Software Availability:** Sep-2017
**SPEC CINT2006 Result**

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(2.60 GHz, Intel Xeon Gold 6142)

**SPECint_rate2006 = Not Run**

**SPECint_rate_base2006 = 1700**

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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>64</td>
<td>510</td>
<td>1230</td>
<td>507</td>
<td>1230</td>
<td>507</td>
<td>1230</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>64</td>
<td>441</td>
<td>735</td>
<td>443</td>
<td>1160</td>
<td>445</td>
<td>1160</td>
</tr>
<tr>
<td>403.gcc</td>
<td>64</td>
<td>263</td>
<td>2220</td>
<td>262</td>
<td>2230</td>
<td>263</td>
<td>2220</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>64</td>
<td>618</td>
<td>1090</td>
<td>619</td>
<td>1080</td>
<td>618</td>
<td>1090</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>64</td>
<td>255</td>
<td>2340</td>
<td>256</td>
<td>2340</td>
<td>255</td>
<td>2340</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>64</td>
<td>687</td>
<td>1130</td>
<td>688</td>
<td>1130</td>
<td>689</td>
<td>1120</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>64</td>
<td>42.3</td>
<td>31400</td>
<td>42.5</td>
<td>31200</td>
<td>42.3</td>
<td>31400</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>64</td>
<td>746</td>
<td>1900</td>
<td>752</td>
<td>1880</td>
<td>755</td>
<td>1880</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>64</td>
<td>478</td>
<td>836</td>
<td>478</td>
<td>836</td>
<td>478</td>
<td>836</td>
</tr>
<tr>
<td>473.astar</td>
<td>64</td>
<td>485</td>
<td>926</td>
<td>486</td>
<td>924</td>
<td>486</td>
<td>924</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>64</td>
<td>238</td>
<td>1860</td>
<td>237</td>
<td>1870</td>
<td>236</td>
<td>1870</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set wth "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

**Platform Notes**

BIOS Configuration:
The Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E
Sysinfo program /cpu2006/config/sysinfo.rev6993

Continued on next page
Platform Notes (Continued)

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1) running on linux-0s5n Wed Nov 8 02:01:12 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
2 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

From /proc/meminfo

MemTotal: 197550460 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID=sles
ANSI_COLOR="0;32"
CPE_NAME=cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-0s5n 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 8 01:38

SPEC is set to: /cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb3 xfs 407G 139G 269G 35% /home
Additional information from dmidecode: Continued on next page
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---

**Platform Notes (Continued)**

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 09/29/2017  
Memory:  
24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

(End of data from sysinfo program)

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**General Notes**

Environment variables set by runspec before the start of the run:  
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

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**Base Compiler Invocation**

C benchmarks:  
`icc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32`

C++ benchmarks:  
`icpc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32`

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**Base Portability Flags**

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-D_FILE_OFFSET_BITS=64`
403.cray: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64`
458.sjeng: `-D_FILE_OFFSET_BITS=64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
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SPECint_rate2006 = Not Run
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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Nov-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/cpu2006/sh10.2 -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revF.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revF.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 29 November 2017.