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### SPECint® Result

**Huawei**

**Kunlun 9016 (Intel Xeon E7-8880 v4)**  

<table>
<thead>
<tr>
<th>SPECint®_rate2006 =</th>
<th>Not Run</th>
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<tbody>
<tr>
<td>SPECint_rate_base2006 =</td>
<td>12400</td>
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</table>

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** May-2017  
**Hardware Availability:** Jan-2016  
**Software Availability:** Sep-2016

**CPI 2006**

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<td>12400</td>
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</table>

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E7-8880 v4</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.30 GHz</td>
</tr>
<tr>
<td>CPU MHZ:</td>
<td>2200</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>352 cores, 16 chips, 22 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>4,8.16 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>55 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>2 x 600 GB SAS, 10K RPM</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>SUSE Linux Enterprise Server 12 (x86_64) SP1 3.12.49-11-default</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++:Version 17.0.0.098 of Intel C/C++ Compiler for Linux</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>ext4</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 5 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software:</td>
<td>Microquill SmartHeap V10.2</td>
</tr>
</tbody>
</table>

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info@spec.org  
http://www.spec.org/
Huawei
Kunlun 9016(Intel Xeon E7-8880 v4)

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SPECint_rate_base2006 = 12400

CPU2006 license: 3175
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>704</td>
<td>744</td>
<td>9240</td>
<td>668</td>
<td>10300</td>
<td>678</td>
<td>10200</td>
<td></td>
<td></td>
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<tr>
<td>401.bzip2</td>
<td>704</td>
<td>1086</td>
<td>6260</td>
<td>1085</td>
<td>6260</td>
<td>6250</td>
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<tr>
<td>403.gcc</td>
<td>704</td>
<td>747</td>
<td>7590</td>
<td>755</td>
<td>7500</td>
<td>761</td>
<td>7440</td>
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<tr>
<td>429.mcf</td>
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<td>424</td>
<td>15100</td>
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<td>15300</td>
<td>425</td>
<td>15100</td>
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<tr>
<td>445.gobmk</td>
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<td>954</td>
<td>7740</td>
<td>957</td>
<td>7720</td>
<td>953</td>
<td>7750</td>
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<tr>
<td>456.hmmer</td>
<td>704</td>
<td>346</td>
<td>19000</td>
<td>344</td>
<td>19100</td>
<td>344</td>
<td>19100</td>
<td></td>
<td></td>
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<tr>
<td>458.sjeng</td>
<td>704</td>
<td>853</td>
<td>9980</td>
<td>853</td>
<td>9990</td>
<td>853</td>
<td>9980</td>
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</tr>
<tr>
<td>462.libquantum</td>
<td>704</td>
<td>104</td>
<td>140000</td>
<td>104</td>
<td>140000</td>
<td>104</td>
<td>141000</td>
<td></td>
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<tr>
<td>464.h264ref</td>
<td>704</td>
<td>876</td>
<td>17800</td>
<td>874</td>
<td>17800</td>
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<tr>
<td>471.omnetpp</td>
<td>704</td>
<td>817</td>
<td>5390</td>
<td>818</td>
<td>5380</td>
<td>817</td>
<td>5380</td>
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<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>704</td>
<td>690</td>
<td>7160</td>
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<td>7140</td>
<td>692</td>
<td>7140</td>
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<td></td>
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<tr>
<td>483.xalancbmk</td>
<td>704</td>
<td>368</td>
<td>13200</td>
<td>373</td>
<td>13000</td>
<td>367</td>
<td>13200</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with:
cputpower --c all frequency-set -g performance

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Sysinfo program /spec/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
runtime on linux-ew80 Wed May 10 05:36:20 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz
16 "physical id"s (chips)
704 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page
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SPEC CINT2006 Result

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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 22
siblings : 44
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 8: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 9: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 10: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 11: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 12: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 13: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 14: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 15: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

cache size : 56320 KB

From /proc/meminfo
MemTotal: 2117101972 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
Continued on next page
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Platform Notes (Continued)

release.
# Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP1"
  VERSION_ID="12.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 May 10 05:35

SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 1.1T 95G 961G 9% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLXSV207 04/17/2017
Memory:
  128x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
  256x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
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Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.20170404.xml
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For other inquiries, please contact webmaster@spec.org.

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