



# SPEC® CFP2006 Result

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## Cisco Systems

Cisco UCS C240 M5 (Intel Xeon Gold 5115, 2.40 GHz)

**SPECfp®2006 = 126**

**SPECfp\_base2006 = 121**

CPU2006 license: 9019

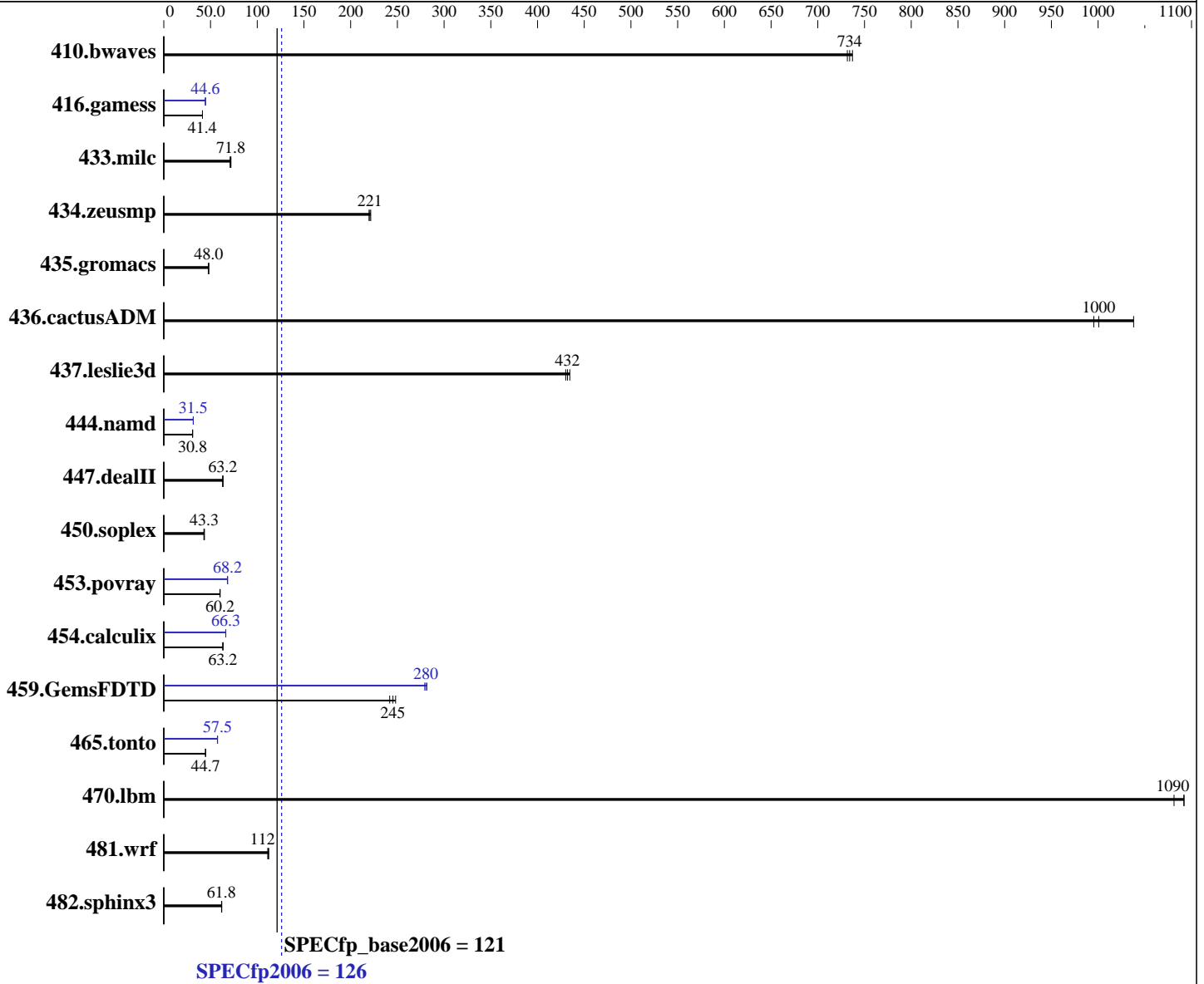
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Sep-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 5115  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64) 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 13.75 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R, running at 2400 MHz)  
Disk Subsystem: 1 x 480 GB SSD SAS  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	18.4	737	<b><u>18.5</u></b>	<b><u>734</u></b>	18.6	732	18.4	737	<b><u>18.5</u></b>	<b><u>734</u></b>	18.6	732
416.gamess	473	41.4	474	41.3	<b><u>473</u></b>	<b><u>41.4</u></b>	<b><u>439</u></b>	<b><u>44.6</u></b>	440	44.5	439	44.6
433.milc	129	70.9	<b><u>128</u></b>	<b><u>71.8</u></b>	128	71.8	129	70.9	<b><u>128</u></b>	<b><u>71.8</u></b>	128	71.8
434.zeusmp	<b><u>41.2</u></b>	<b><u>221</u></b>	41.4	220	41.1	221	<b><u>41.2</u></b>	<b><u>221</u></b>	41.4	220	41.1	221
435.gromacs	149	48.0	149	47.9	<b><u>149</u></b>	<b><u>48.0</u></b>	149	48.0	149	47.9	<b><u>149</u></b>	<b><u>48.0</u></b>
436.cactusADM	12.0	995	<b><u>11.9</u></b>	<b><u>1000</u></b>	11.5	1040	12.0	995	<b><u>11.9</u></b>	<b><u>1000</u></b>	11.5	1040
437.leslie3d	21.9	430	21.6	435	<b><u>21.8</u></b>	<b><u>432</u></b>	21.9	430	21.6	435	<b><u>21.8</u></b>	<b><u>432</u></b>
444.namd	260	30.8	<b><u>260</u></b>	<b><u>30.8</u></b>	260	30.8	254	31.6	254	31.5	<b><u>254</u></b>	<b><u>31.5</u></b>
447.dealII	181	63.3	<b><u>181</u></b>	<b><u>63.2</u></b>	181	63.1	181	63.3	<b><u>181</u></b>	<b><u>63.2</u></b>	181	63.1
450.soplex	<b><u>192</u></b>	<b><u>43.3</u></b>	192	43.4	194	42.9	<b><u>192</u></b>	<b><u>43.3</u></b>	192	43.4	194	42.9
453.povray	<b><u>88.4</u></b>	<b><u>60.2</u></b>	88.6	60.0	88.3	60.2	<b><u>78.0</u></b>	<b><u>68.2</u></b>	77.9	68.3	78.2	68.0
454.calculix	130	63.4	131	63.2	<b><u>131</u></b>	<b><u>63.2</u></b>	124	66.3	<b><u>124</u></b>	<b><u>66.3</u></b>	124	66.4
459.GemsFDTD	<b><u>43.3</u></b>	<b><u>245</u></b>	42.8	248	43.9	242	<b><u>37.8</u></b>	<b><u>280</u></b>	38.0	279	37.7	282
465.tonto	<b><u>220</u></b>	<b><u>44.7</u></b>	220	44.6	220	44.8	171	57.6	172	57.4	<b><u>171</u></b>	<b><u>57.5</u></b>
470.lbm	<b><u>12.6</u></b>	<b><u>1090</u></b>	12.7	1080	12.6	1090	<b><u>12.6</u></b>	<b><u>1090</u></b>	12.7	1080	12.6	1090
481.wrf	<b><u>99.9</u></b>	<b><u>112</u></b>	100	111	99.4	112	<b><u>99.9</u></b>	<b><u>112</u></b>	100	111	99.4	112
482.sphinx3	315	61.8	314	62.1	<b><u>315</u></b>	<b><u>61.8</u></b>	315	61.8	314	62.1	<b><u>315</u></b>	<b><u>61.8</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Intel HyperThreading Technology set to Disabled

CPU performance set to Enterprise

Power Performance Tuning set to OS

SNC set to Disabled

IMC Interleaving set to Auto

Patrol Scrub set to Disabled

Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

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SPECfp\_base2006 = 121

**CPU2006 license:** 9019  
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**Tested by:** Cisco Systems

**Test date:** Sep-2017  
**Hardware Availability:** Aug-2017  
**Software Availability:** Apr-2017

### Platform Notes (Continued)

running on linux-j64x Sat Sep 2 22:31:06 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 10
 physical 0: cores 0 1 2 3 4 8 9 10 11 12
 physical 1: cores 0 1 2 3 4 8 9 10 11 12
 cache size : 14080 KB
```

```
From /proc/meminfo
MemTotal: 394864920 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-j64x 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 2 22:23
```

```
SPEC is set to: /home/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
Continued on next page
```



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### Platform Notes (Continued)

/dev/sdb7 xfs 416G 55G 362G 14% /home  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C240M5.3.1.1d.0.0615170707 06/15/2017

Memory:

24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

### Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0  
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3  
-auto -unroll4

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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