**SPEC® CINT2006 Result**

**Dell Inc.**

PowerEdge M640 (Intel Xeon Gold 5115, 2.40 GHz)

<table>
<thead>
<tr>
<th>Test sponsor:</th>
<th>Dell Inc.</th>
<th>Tested by:</th>
<th>Dell Inc.</th>
<th>Hardware Availability:</th>
<th>Sep-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test date:</td>
<td>Aug-2017</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SPECint_rate2006 =</td>
<td>1010</td>
<td>SPECint_rate_base2006 =</td>
<td>963</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 5115</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
</tr>
<tr>
<td>CPU MHZ:</td>
<td>2400</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>20 cores, 2 chips, 10 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1.2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>13.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400 MT/s)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 960 GB SATA SSD</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>SUSE Linux Enterprise Server 12 SP3 4.4.70-2-default</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>File System:</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software:</td>
<td>Microquill SmartHeap V10.2</td>
</tr>
</tbody>
</table>
## Dell Inc.

### PowerEdge M640 (Intel Xeon Gold 5115, 2.40 GHz)

**SPEC_rate2006 = 1010**  
**SPEC_rate_base2006 = 963**

**CPU2006 license:** 55  
**Test date:** Aug-2017  
**Test sponsor:** Dell Inc.  
**Hardware Availability:** Sep-2017  
**Tested by:** Dell Inc.  
**Software Availability:** Sep-2017

## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>40</td>
<td>541</td>
<td>723</td>
<td>542</td>
<td>722</td>
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<td>451</td>
<td>867</td>
<td>448</td>
<td>872</td>
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<td>401.bzip2</td>
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<td>904</td>
<td>427</td>
<td>904</td>
<td>427</td>
<td>40</td>
<td>861</td>
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<td>403.gcc</td>
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<td>451</td>
<td>714</td>
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<td>712</td>
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<td>450</td>
<td>715</td>
<td>448</td>
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<tr>
<td>429.mcf</td>
<td>40</td>
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<td>445.gobmk</td>
<td>40</td>
<td>670</td>
<td>626</td>
<td>671</td>
<td>625</td>
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<td>621</td>
<td>672</td>
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<tr>
<td>456.hmmer</td>
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<td>263</td>
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<td>1670</td>
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<td>458.sjeng</td>
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<td>692</td>
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<td>462.libquantum</td>
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<td>84.4</td>
<td>9820</td>
<td>84.0</td>
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<td>778</td>
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<td>471.omnetpp</td>
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<td>463</td>
<td>539</td>
<td>464</td>
<td>538</td>
<td>467</td>
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<tr>
<td>473.astar</td>
<td>40</td>
<td>501</td>
<td>560</td>
<td>500</td>
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<td>501</td>
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<td>502</td>
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<tr>
<td>483.xalancbmk</td>
<td>40</td>
<td>239</td>
<td>1150</td>
<td>239</td>
<td>1150</td>
<td>40</td>
<td>239</td>
<td>1150</td>
<td>239</td>
<td>1150</td>
<td>238</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

- BIOS settings:  
  - Sub NUMA Cluster disabled  
  - Virtualization Technology disabled  
  - System Profile set to Custom  
  - CPU Performance set to Maximum Performance  
  - C States set to Autonomous  
  - C1E disabled  
  - Energy Efficient Turbo disabled  
  - Uncore Frequency set to Dynamic  
  - Energy Efficiency Policy set to Performance  
  - Memory Patrol Scrub disabled  
  - Logical Processor enabled  
  - CPU Interconnect Bus Link Power Management disabled  
  - PCI ASPM L1 Link Power Management disabled  

Sysinfo program /root/cpu2006-1.2_1c17u3/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-287k Sat Aug 26 21:40:03 2017  
Continued on next page

Page 2
Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 10
  siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 14080 KB

From /proc/meminfo
  MemTotal:       196682068 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
    os-release:
      NAME="SLES"
      VERSION="12-SP3"
      VERSION_ID="12.3"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-287k 4.4.70-2-default #1 SMP Wed Jun 7 15:12:06 UTC 2017
  (4502c76) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 26 21:39

SPEC is set to: /root/cpu2006-1.2_ic17u3
  Filesystem Type  Size  Used Avail Use% Mounted on
  /dev/sda3  btrfs  928G 7.3G  919G 1%  /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
Continued on next page
Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.0 08/10/2017
Memory:
  12x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666 MHz, configured at 2400
  MHz
  4x Not Specified Not Specified
(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH="/root/cpu2006-1.2_ic17u3/lib/ia32:/root/cpu2006-1.2_ic17u3/lib/intel64:/root/cpu2006-1.2_ic17u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runcmd command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
  icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
242.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64

Continued on next page
Dell Inc.

PowerEdge M640 (Intel Xeon Gold 5115, 2.40 GHz)

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SPECint_rate_base2006 = 963

CPU2006 license: 55
Test sponsor: Dell Inc.
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Test date: Aug-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

### Base Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

### Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

### Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

### Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
 icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

### Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64

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Dell Inc. PowerEdge M640 (Intel Xeon Gold 5115, 2.40 GHz)

SPECint_rate2006 = 1010
SPECint_rate_base2006 = 963

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

CPU2006 license: 55
Test date: Aug-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll14 -auto-ilp32
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)
SPEC CINT2006 Result

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PowerEdge M640 (Intel Xeon Gold 5115, 2.40 GHz)

SPECint_rate2006 = 1010
SPECint_rate_base2006 = 963

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CPU2006 license: 55
Test sponsor: Dell Inc.
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Test date: Aug-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

471.omnetpp (continued):
   -qopt-ra-region-strategy=block
   -qopt-mem-layout-trans=3 -Wl,-z,muldefs
   -L/sh10.2 -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml

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For other inquiries, please contact webmaster@spec.org.

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