SPEC® CINT2006 Result

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Cisco Systems

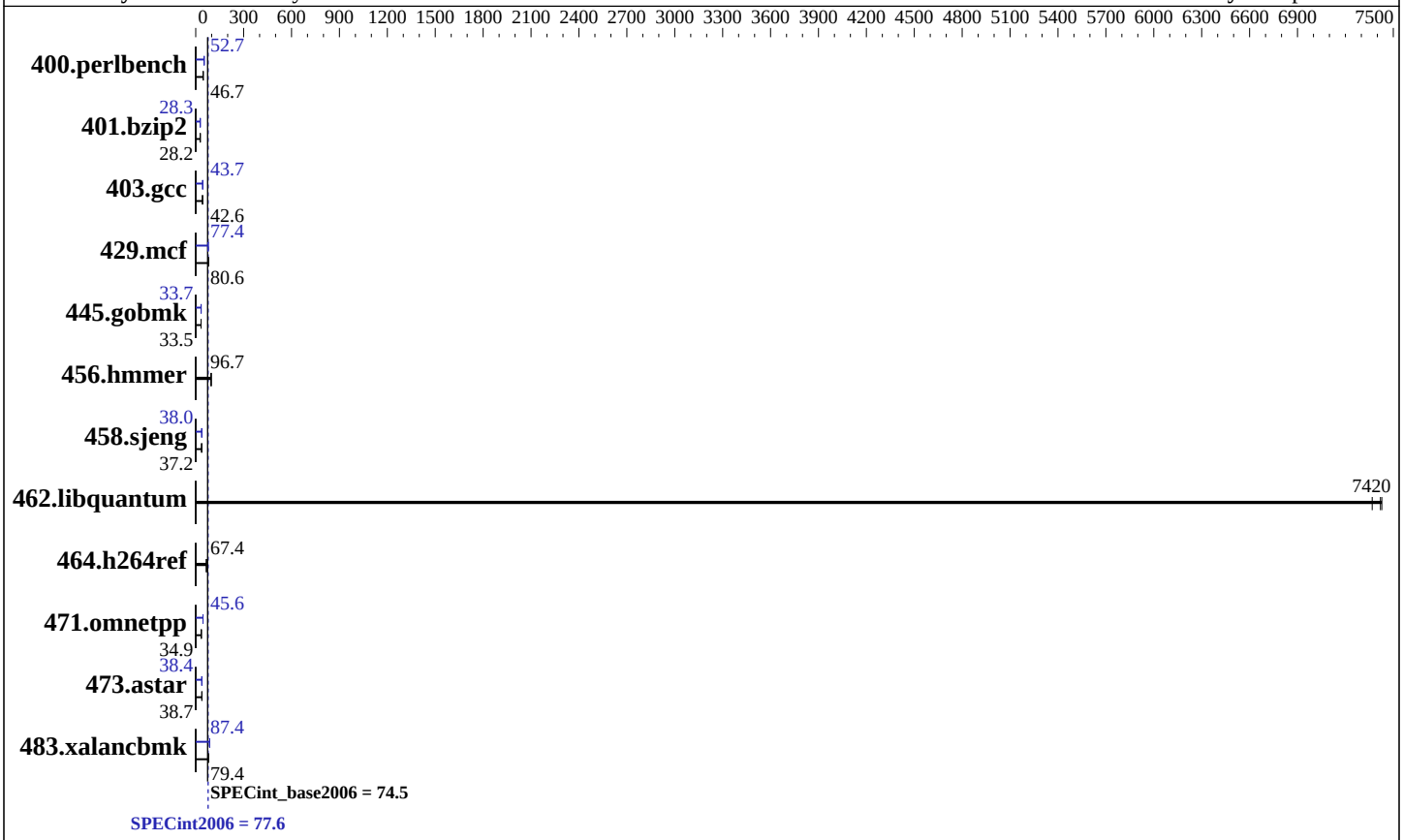
Cisco UCS C240 M5 (Intel Xeon Gold 6130, 2.10GHz)

SPECint®2006 = 77.6

SPECint_base2006 = 74.5

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Jul-2017
Hardware Availability: Aug-2017
Software Availability: Apr-2017



Hardware

CPU Name:	Intel Xeon Gold 6130
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	32 cores, 2 chips, 16 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	22 MB I+D on chip per chip
Other Cache:	None
Memory:	384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)
Disk Subsystem:	1 x 300 GB SAS 15K RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-514.el7.x86_64
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	208	46.9	**209**	**46.7**	210	46.6	185	52.8	**185**	**52.7**	186	52.6
401.bzip2	**342**	**28.2**	342	28.2	341	28.3	342	28.3	**341**	**28.3**	341	28.3
403.gcc	189	42.6	**189**	**42.6**	189	42.6	184	43.7	**184**	**43.7**	183	43.9
429.mcf	113	80.6	**113**	**80.6**	118	77.2	116	78.6	**118**	**77.4**	118	77.1
445.gobmk	**314**	**33.5**	314	33.5	314	33.4	**311**	**33.7**	311	33.8	311	33.7
456.hmmer	**96.5**	**96.7**	96.5	96.6	96.4	96.7	**96.5**	**96.7**	96.5	96.6	96.4	96.7
458.sjeng	**325**	**37.2**	325	37.2	325	37.2	319	38.0	319	38.0	**319**	**38.0**
462.libquantum	2.81	7370	**2.79**	**7420**	2.79	7430	2.81	7370	**2.79**	**7420**	2.79	7430
464.h264ref	329	67.3	328	67.4	**328**	**67.4**	329	67.3	328	67.4	**328**	**67.4**
471.omnetpp	**179**	**34.9**	178	35.0	179	34.9	137	45.7	**137**	**45.6**	137	45.6
473.astar	181	38.7	181	38.7	**181**	**38.7**	**183**	**38.4**	182	38.5	183	38.4
483.xalancbmk	**86.9**	**79.4**	86.9	79.4	86.8	79.5	79.0	87.3	78.7	87.6	**78.9**	**87.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS
SNC set to Disabled
IMC Interleaving set to Auto
Patrol Scrub set to Disabled
 Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on C240-RHEL73 Sun Jul 30 22:11:54 2017

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
       2 "physical id"s (chips)
       32 "processors"
```

Continued on next page

Platform Notes (Continued)

```
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 16
         siblings  : 16
         physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
         physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
      cache size : 22528 KB

   From /proc/meminfo
      MemTotal:       394867452 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   From /etc/*release* /etc/*version*
      os-release:
         NAME="Red Hat Enterprise Linux Server"
         VERSION="7.3 (Maipo)"
         ID="rhel"
         ID_LIKE="fedora"
         VERSION_ID="7.3"
         PRETTY_NAME=Storage
         ANSI_COLOR="0;31"
         CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
      redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
      system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

   uname -a:
      Linux C240-RHEL73 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Jul 30 05:22

   SPEC is set to: /home/cpu2006-1.2
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sdb5      xfs   169G   17G  153G  10% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS Cisco Systems, Inc. C240M5.3.1.1d.0.0615170707 06/15/2017Cisco Systems,
     Inc. C240M5.3.1.1d.0.0615170707 06/15/2017
     Memory:
      48x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

   (End of data from sysinfo program)
   The correct amount of Memory installed is 384 GB (24 x 16 GB)
   and the dmidecode is reporting invalid number of DIMMs installed
```

Continued on next page

Platform Notes (Continued)

```
Installed Memory:
24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "32"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-qopt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div -auto-ilp32 -qopt-prefetch`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -qopt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)

456.hmmer: `basepeak = yes`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-qopt-ra-region-strategy=block -Wl,-z,muldefs -L/sh10.2 -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -Wl,-z,muldefs -L/sh10.2 -lsmartheap`

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml`

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