SPEC® CFP2006 Result
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Lenovo Global Technology

Lenovo ThinkServer RS160
(3.50 GHz, Intel Xeon E3-1230 v6)

$SPECfp^{®}\_rate2006 =$ 208

$SPECfp\_rate\_base2006 =$ 201

CPU2006 license: 9017
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test date: Jul-2017
Hardware Availability: Jun-2017
Software Availability: Nov-2016

Copies

Benchmark	Copies	Value
410.bwaves	4	156
410.bwaves	8	150
416.gamess	8	231
416.gamess	8	228
433.milc	8	157
434.zeusmp	8	262
435.gromacs	8	293
435.gromacs	8	288
436.cactusADM	8	273
437.leslie3d	4	114
437.leslie3d	8	105
444.namd	8	182
444.namd	8	178
447.dealII	8	356
450.soplex	4	130
450.soplex	8	117
453.povray	8	351
453.povray	8	300
454.calculix	8	357
459.GemsFDTD	4	94.2
459.GemsFDTD	8	87.6
465.tonto	8	239
465.tonto	8	218
470.lbm	8	204
481.wrf	8	186
482.sphinx3	8	205

SPECfp_rate_base2006 = 201

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Hardware

CPU Name: Intel Xeon E3-1230 v6
CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz: 3500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86_64)
Kernel 4.4.21-69-default
Compiler: C/C++: Version 17.0.0.098 of Intel C/C++
Compiler for Linux;
Fortran: Version 17.0.0.098 of Intel Fortran
Compiler for Linux
Auto Parallel: No
File System: btrfs
System State: Run level 3 (multi-user)

Continued on next page

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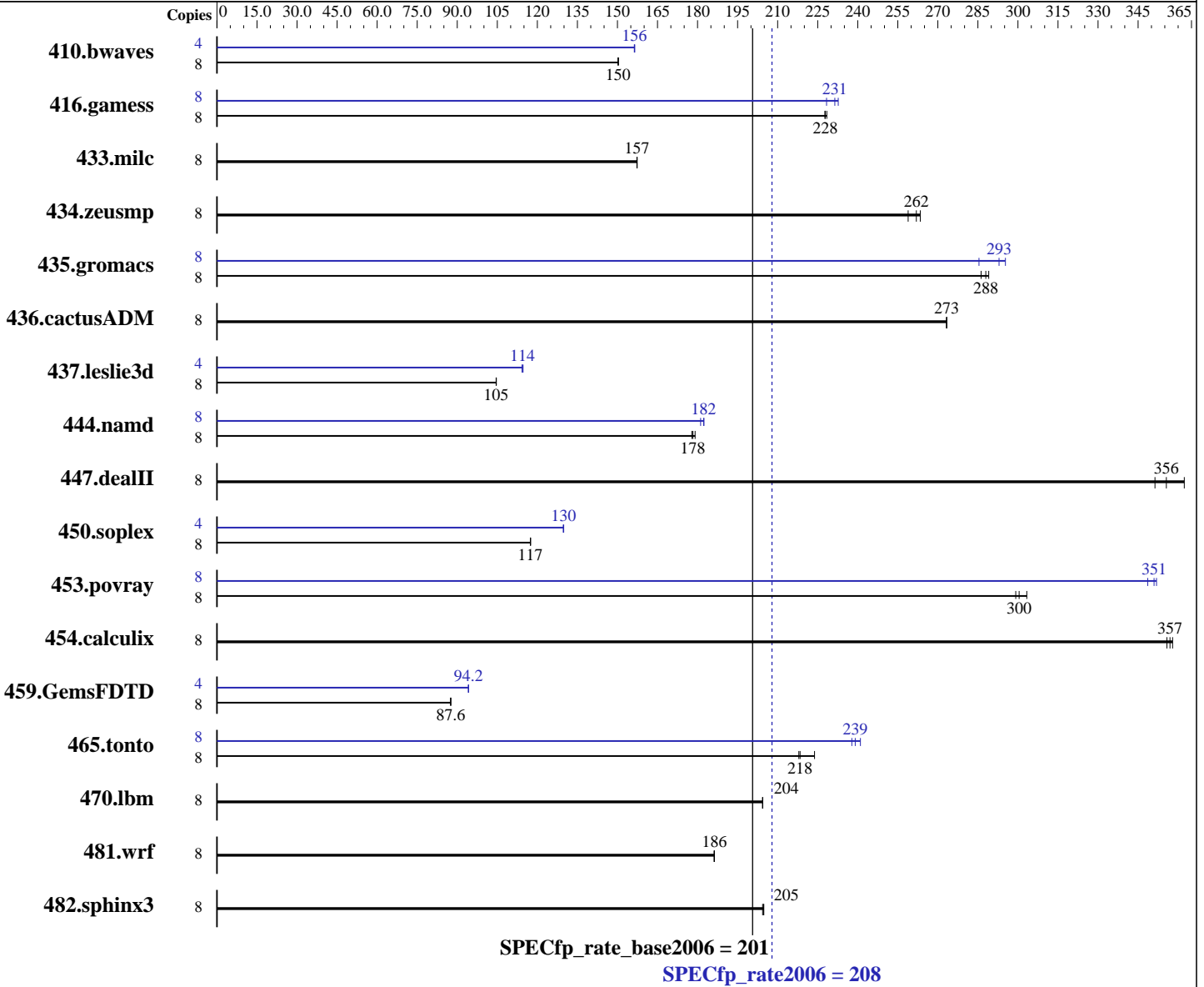|	
CPU2006 license: 9017	**Test date:** Jul-2017
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L3 Cache:	8 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	32 GB (2 x 16 GB 2Rx8 PC4-2400T-E)	Other Software:	None
Disk Subsystem:	1 x 960 GB SATA SSD		
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	723	150	724	150	**723**	**150**	4	**348**	**156**	347	157	348	156
416.gamess	8	688	228	686	228	**687**	**228**	8	673	233	**677**	**231**	686	228
433.milc	8	467	157	467	157	**467**	**157**	8	467	157	467	157	**467**	**157**
434.zeusmp	8	**278**	**262**	276	263	281	259	8	**278**	**262**	276	263	281	259
435.gromacs	8	198	289	200	286	**198**	**288**	8	**195**	**293**	200	285	193	295
436.cactusADM	8	350	273	**350**	**273**	350	273	8	350	273	**350**	**273**	350	273
437.leslie3d	8	719	105	719	105	**719**	**105**	4	329	114	328	115	**329**	**114**
444.namd	8	358	179	361	178	**360**	**178**	8	352	182	354	181	**352**	**182**
447.dealII	8	**257**	**356**	253	362	261	351	8	**257**	**356**	253	362	261	351
450.soplex	8	**568**	**117**	568	117	568	117	4	257	130	**257**	**130**	257	130
453.povray	8	140	303	**142**	**300**	142	299	8	122	349	**121**	**351**	121	352
454.calculix	8	184	358	185	356	**185**	**357**	8	184	358	185	356	**185**	**357**
459.GemsFDTD	8	**969**	**87.6**	969	87.6	970	87.5	4	450	94.3	451	94.2	**451**	**94.2**
465.tonto	8	361	218	352	224	**360**	**218**	8	**329**	**239**	331	238	327	241
470.lbm	8	**538**	**204**	538	204	538	204	8	**538**	**204**	538	204	538	204
481.wrf	8	480	186	480	186	**480**	**186**	8	480	186	480	186	**480**	**186**
482.sphinx3	8	761	205	763	204	**762**	**205**	8	761	205	763	204	**762**	**205**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Boot performance mode set to Turbo Performance
Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993

Continued on next page

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Platform Notes (Continued)

```
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on RS160 Wed Jul 19 22:33:46 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E3-1230 v6 @ 3.50GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:       32888952 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux RS160 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 19 14:58

SPEC is set to: /home/cpu2006-1.2-ic17.0
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda2       btrfs  895G   51G  843G   6% /home
Additional information from dmidecode:
```

Continued on next page

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Platform Notes (Continued)

```
   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS American Megatrends Inc. VB1TS223 03/16/2017
  Memory:
  2x Micron 18ASF2G72AZ-2G3B1 16 GB 2 rank 2400 MHz
  2x Not Specified Not Specified

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"


 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled by default
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:
```
     icc -m64
```

C++ benchmarks:
```
     icpc -m64
```

Fortran benchmarks:
```
     ifort -m64
```

Benchmarks using both Fortran and C:
```
     icc -m64 ifort -m64
```

Base Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
   444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
   465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m64
```
```
  450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc -m64 ifort -m64
```

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Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -D_FILE_OFFSET_BITS=64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
      433.milc: basepeak = yes

      470.lbm: basepeak = yes

   482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
      444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -fno-alias -auto-ilp32
                -qopt-mem-layout-trans=3

     447.dealII: basepeak = yes

     450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -qopt-malloc-options=3
                -qopt-mem-layout-trans=3

    453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
                -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3
```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2)
`-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-unroll2 -inline-level=0 -scalar-rep-`

434.zeusmp: basepeak = yes

437.leslie3d: `Same as 410.bwaves`

459.GemsFDTD: `Same as 410.bwaves`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2)
`-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-unroll4 -auto -inline-calloc`
`-qopt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2)
`-par-num-threads=1`(pass 1) `-qopt-prefetch -auto-ilp32`
`-qopt-mem-layout-trans=3`

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-KBL-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-KBL-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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