



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 4030

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate_base2006 = 3820

CPU2006 license: 55

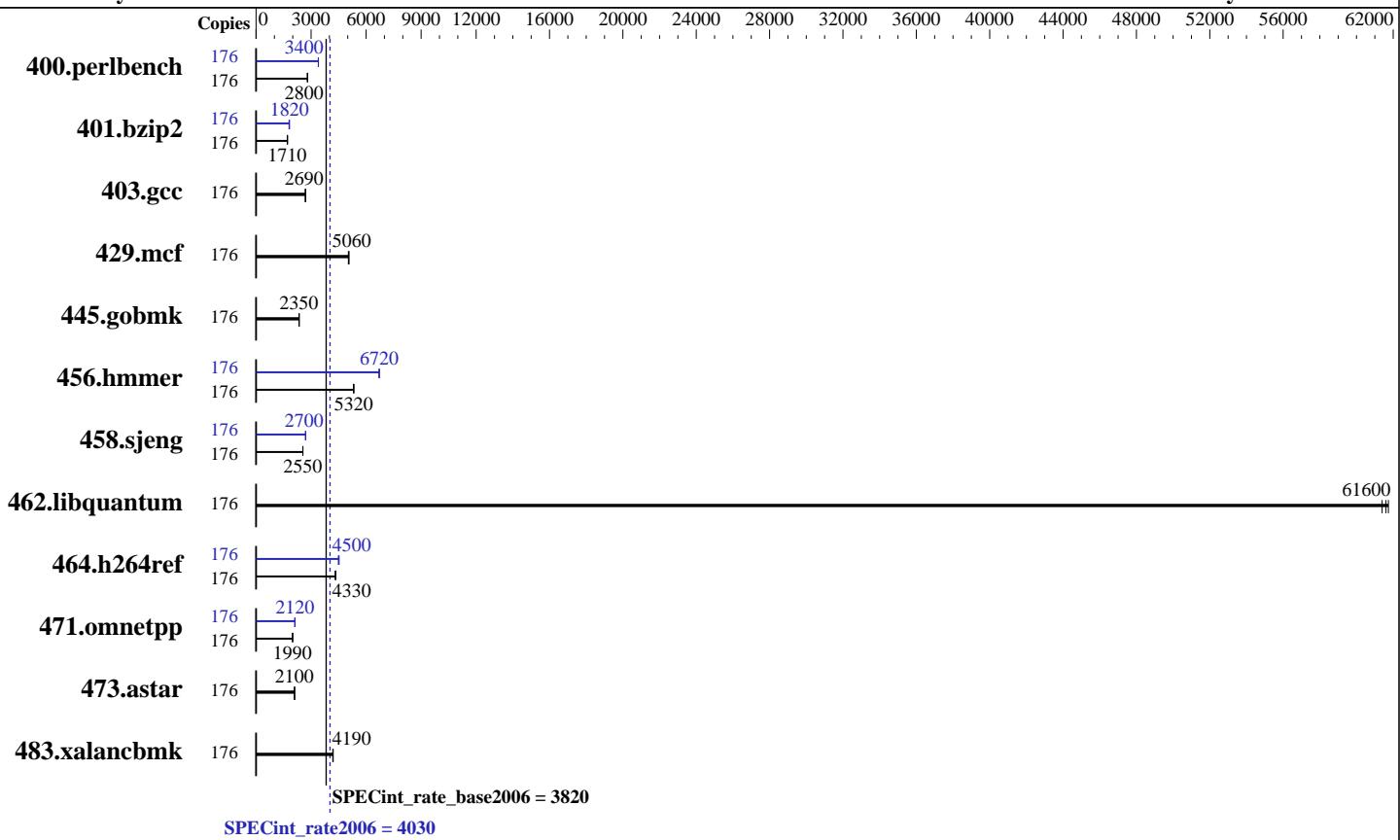
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: May-2017

Hardware Availability: Jul-2017

Software Availability: Nov-2016



Hardware

CPU Name:	Intel Xeon Gold 6152
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	88 cores, 4 chips, 22 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	30.25 MB I+D on chip per chip
Other Cache:	None
Memory:	768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem:	1 x 960 GB SATA SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default
Compiler:	C/C++: Version 17.0.1.132 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 4030

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate_base2006 = 3820

CPU2006 license: 55

Test date: May-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	176	613	2800	612	2810	615	2800	176	507	3390	505	3400	506	3400
401.bzip2	176	988	1720	999	1700	994	1710	176	944	1800	934	1820	934	1820
403.gcc	176	524	2700	526	2690	528	2690	176	524	2700	526	2690	528	2690
429.mcf	176	319	5030	317	5060	317	5060	176	319	5030	317	5060	317	5060
445.gobmk	176	787	2350	786	2350	787	2350	176	787	2350	786	2350	787	2350
456.hammer	176	309	5320	309	5320	308	5340	176	244	6720	245	6700	244	6720
458.sjeng	176	834	2550	835	2550	834	2550	176	789	2700	789	2700	788	2700
462.libquantum	176	59.2	61600	59.1	61700	59.4	61400	176	59.2	61600	59.1	61700	59.4	61400
464.h264ref	176	895	4350	902	4320	900	4330	176	867	4490	865	4500	860	4530
471.omnetpp	176	553	1990	552	1990	552	1990	176	519	2120	520	2120	519	2120
473.astar	176	588	2100	589	2100	589	2100	176	588	2100	589	2100	589	2100
483.xalancbmk	176	290	4190	290	4180	289	4210	176	290	4190	290	4180	289	4210

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
Sysinfo program /root/Desktop/SPECcpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-yxvl Mon May 8 17:09:47 2017

This section contains SUT (System Under Test) info as seen by
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 4030

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate_base2006 = 3820

CPU2006 license: 55

Test date: May-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
        4 "physical id"s (chips)
        176 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 22
    siblings : 44
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
    28
    cache size : 30976 KB
```

```
From /proc/meminfo
MemTotal:      791001148 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-yxvl 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 7 21:32
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate2006 = 4030

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: May-2017

Hardware Availability: Jul-2017

Software Availability: Nov-2016

Platform Notes (Continued)

```
SPEC is set to: /root/Desktop/SPECcpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        btrfs  836G  416G  419G  50% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.5.5 04/27/2017

Memory:

48x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/Desktop/SPECcpu2006/lib/ia32:/root/Desktop/SPECcpu2006/lib/intel64:/root/Desktop/SPECcpu2006/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hammer: -D_FILE_OFFSET_BITS=64
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate2006 = 4030

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: May-2017

Hardware Availability: Jul-2017

Software Availability: Nov-2016

Base Portability Flags (Continued)

458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmr: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate2006 = 4030

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: May-2017

Hardware Availability: Jul-2017

Software Availability: Nov-2016

Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
               -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
            -qopt-mem-layout-trans=3

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hammer: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
             -qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -unroll14 -auto-ilp32
            -qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
              -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -unroll12 -qopt-mem-layout-trans=3

```

C++ benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R940 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate2006 = 4030

CPU2006 license: 55

Test date: May-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

Peak Optimization Flags (Continued)

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
             -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2)
             -qopt-ra-region-strategy=block
             -qopt-mem-layout-trans=3 -Wl,-z,muldefs
             -L/sh10.2 -lsmartheap
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Jul 12 12:12:47 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 11 July 2017.