SPEC® CINT2006 Result

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NEC Corporation

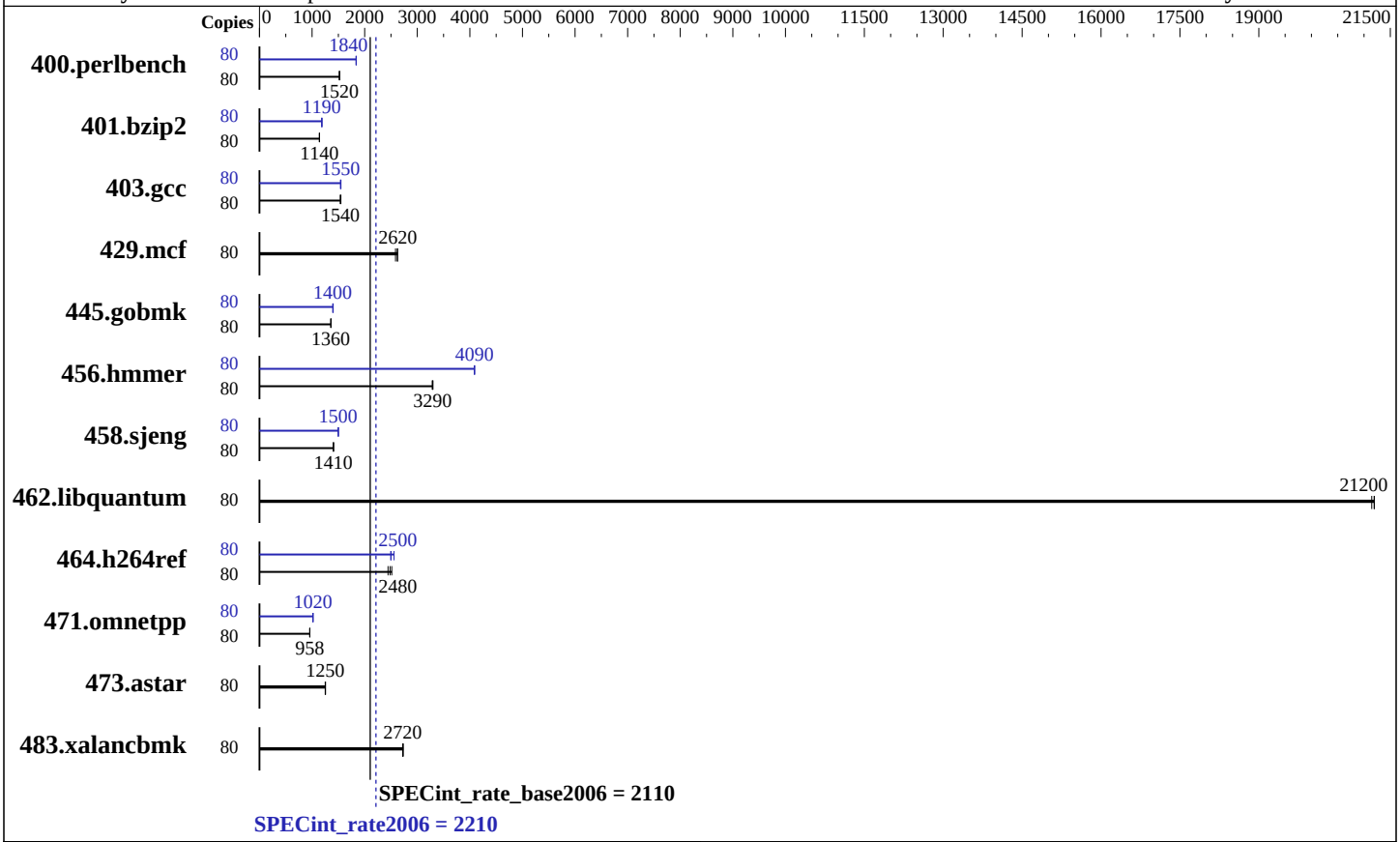
Express5800/A2040d (Intel Xeon E7-8891 v4)

SPECint®_rate2006 = 2210

SPECint_rate_base2006 = 2110

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: May-2017
Hardware Availability: Sep-2016
Software Availability: Nov-2016



Hardware

CPU Name:	Intel Xeon E7-8891 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	60 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (32 x 32 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	1 x 600 GB SAS, 15000 RPM, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.3 (Maipo) Kernel 3.10.0-514.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	513	1520	**513**	**1520**	515	1520	80	**425**	**1840**	425	1840	426	1840
401.bzip2	80	**677**	**1140**	676	1140	678	1140	80	650	1190	**651**	**1190**	652	1180
403.gcc	80	**418**	**1540**	416	1550	419	1540	80	**416**	**1550**	416	1550	418	1540
429.mcf	80	282	2590	278	2630	**278**	**2620**	80	282	2590	278	2630	**278**	**2620**
445.gobmk	80	**618**	**1360**	618	1360	618	1360	80	**601**	**1400**	599	1400	601	1400
456.hmmer	80	**227**	**3290**	227	3280	226	3300	80	182	4090	**183**	**4090**	183	4080
458.sjeng	80	688	1410	**688**	**1410**	688	1410	80	**646**	**1500**	646	1500	646	1500
462.libquantum	80	78.4	21100	**78.2**	**21200**	78.2	21200	80	78.4	21100	**78.2**	**21200**	78.2	21200
464.h264ref	80	724	2440	704	2520	**713**	**2480**	80	709	2500	**708**	**2500**	692	2560
471.omnetpp	80	524	954	522	958	**522**	**958**	80	**492**	**1020**	492	1020	491	1020
473.astar	80	449	1250	448	1250	**448**	**1250**	80	449	1250	448	1250	**448**	**1250**
483.xalancbmk	80	203	2720	**203**	**2720**	202	2730	80	203	2720	**203**	**2720**	202	2730

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Memory RAS Mode: Independent mode
 VT-x : Disabled
 Processor C6 Report : Disabled
 OS Performance Tuning : Disabled
 Energy Performance : Performance
 Patrol Scrub : Disabled
 Demand Scrub : Disabled
 Memory P.E. Retry : Disabled
 Cluster On Die : Enabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPEC/libs/32:/SPEC/libs/64:/SPEC/sh"
```

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General Notes (Continued)

```
Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
Transparent Huge Pages enabled by default

Filesystem page cache cleared with:
echo 1 >       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-D_FILE_OFFSET_BITS=64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64`
458.sjeng: `-D_FILE_OFFSET_BITS=64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
458.sjeng: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-auto-ilp32`

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Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040d-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040d-RevB.xml

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