



# SPEC® CINT2006 Result

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Fujitsu

**SPECint®2006 = 77.0**

PRIMERGY TX1320 M3, Intel Xeon E3-1280 v6, 3.9GHz

**SPECint\_base2006 = 74.6**

CPU2006 license: 19

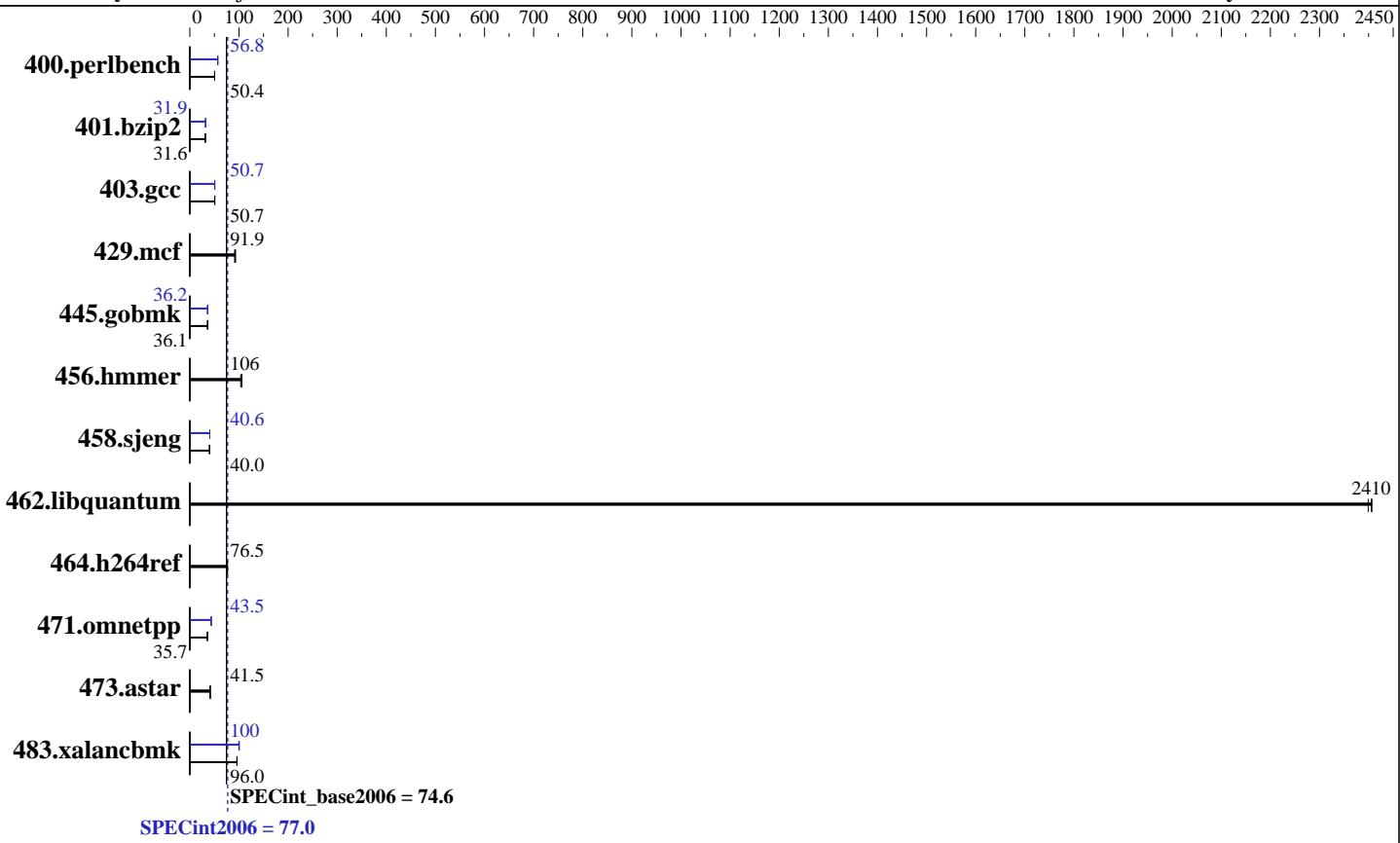
**Test date:** Mar-2017

Test sponsor: Fujitsu

**Hardware Availability:** May-2017

Tested by: Fujitsu

**Software Availability:** Nov-2016



## Hardware

CPU Name: Intel Xeon E3-1280 v6  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz  
 CPU MHz: 3900  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2400T-E)  
 Disk Subsystem: 1 x 1TB, SATA III, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64) 4.4.21-68-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	193	50.5	<b>194</b>	<b>50.4</b>	194	50.4	172	56.8	<b>172</b>	<b>56.8</b>	172	56.9
401.bzip2	307	31.5	305	31.6	<b>306</b>	<b>31.6</b>	303	31.9	<b>302</b>	<b>31.9</b>	302	32.0
403.gcc	158	50.8	<b>159</b>	<b>50.7</b>	159	50.6	159	50.7	159	50.8	<b>159</b>	<b>50.7</b>
429.mcf	<b>99.2</b>	<b>91.9</b>	99.8	91.4	97.9	93.1	<b>99.2</b>	<b>91.9</b>	99.8	91.4	97.9	93.1
445.gobmk	290	36.2	290	36.1	<b>290</b>	<b>36.1</b>	<b>290</b>	<b>36.2</b>	290	36.2	290	36.1
456.hmmer	90.0	104	<b>88.2</b>	<b>106</b>	88.1	106	90.0	104	<b>88.2</b>	<b>106</b>	88.1	106
458.sjeng	302	40.0	302	40.0	<b>302</b>	<b>40.0</b>	298	40.6	<b>298</b>	<b>40.6</b>	298	40.6
462.libquantum	<b>8.61</b>	<b>2410</b>	8.61	2410	8.64	2400	<b>8.61</b>	<b>2410</b>	8.61	2410	8.64	2400
464.h264ref	290	76.4	289	76.5	<b>289</b>	<b>76.5</b>	290	76.4	289	76.5	<b>289</b>	<b>76.5</b>
471.omnetpp	175	35.7	176	35.6	<b>175</b>	<b>35.7</b>	144	43.5	<b>144</b>	<b>43.5</b>	144	43.5
473.astar	169	41.6	170	41.3	<b>169</b>	<b>41.5</b>	169	41.6	170	41.3	<b>169</b>	<b>41.5</b>
483.xalancbmk	71.9	95.9	<b>71.9</b>	<b>96.0</b>	71.8	96.0	<b>69.2</b>	99.7	<b>68.9</b>	<b>100</b>	68.5	101

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with :
cpupower -c all frequency-set -g performance
cpupower idle-set -d 2
cpupower idle-set -d 3
cpupower idle-set -d 4
echo always > /sys/kernel/mm/transparent_hugepage/enabled
KMP_AFFINITY = "granularity=fine,scatter"
OMP_NUM_THREADS = "4"
```

## Platform Notes

BIOS Settings:

Hyper-threading = Disabled

Sysinfo program /home/benchmark/speccpu-20160922-updated/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-lrfj Sat Mar 4 23:07:20 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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## Platform Notes (Continued)

```
model name : Intel(R) Xeon(R) CPU E3-1280 v6 @ 3.90GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings   : 4
  physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:       65834924 kB
HugePages_Total:        0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-1rfj 4.4.21-68-default #1 SMP Tue Oct 18 18:19:37 UTC 2016
(63cf368) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 4 18:54

SPEC is set to: /home/benchmark/speccpu-20160922-updated
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   890G  8.3G  881G   1% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.
```

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.0.0 for D3373-B1x  
02/20/2017

Memory:

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## Platform Notes (Continued)

4x Samsung M391A2K43BB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/benchmark/speccpu-20160922-updated/libs/32:/home/benchmark/speccpu-20160922-updated/libs/64:/home/benchmark/speccpu-20160922-updated/sh10.2"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

This result was measured on the PRIMERGY TX1320 M3. The PRIMERGY TX1320 M3 and the PRIMERGY TX1330 M3 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

```
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

```
473.astar: icpc -m64
```

## Peak Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
403.gcc: -DSPEC_CPU_LP64
```

```
429.mcf: -DSPEC_CPU_LP64
```

```
445.gobmk: -D_FILE_OFFSET_BITS=64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

```
464.h264ref: -DSPEC_CPU_LP64
```

```
471.omnetpp: -D_FILE_OFFSET_BITS=64
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
               -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -qopt-prefetch
```

```
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div -auto-ilp32 -qopt-prefetch
```

```
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -qopt-malloc-options=3 -auto-ilp32
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2)
```

```
456.hammer: basepeak = yes
```

```
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -unroll4
```

```
462.libquantum: basepeak = yes
```

```
464.h264ref: basepeak = yes
```

C++ benchmarks:

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
              -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -qopt-ra-region-strategy=block
              -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
                 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevE.xml>

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