SPEC® CINT2006 Result

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Cisco Systems

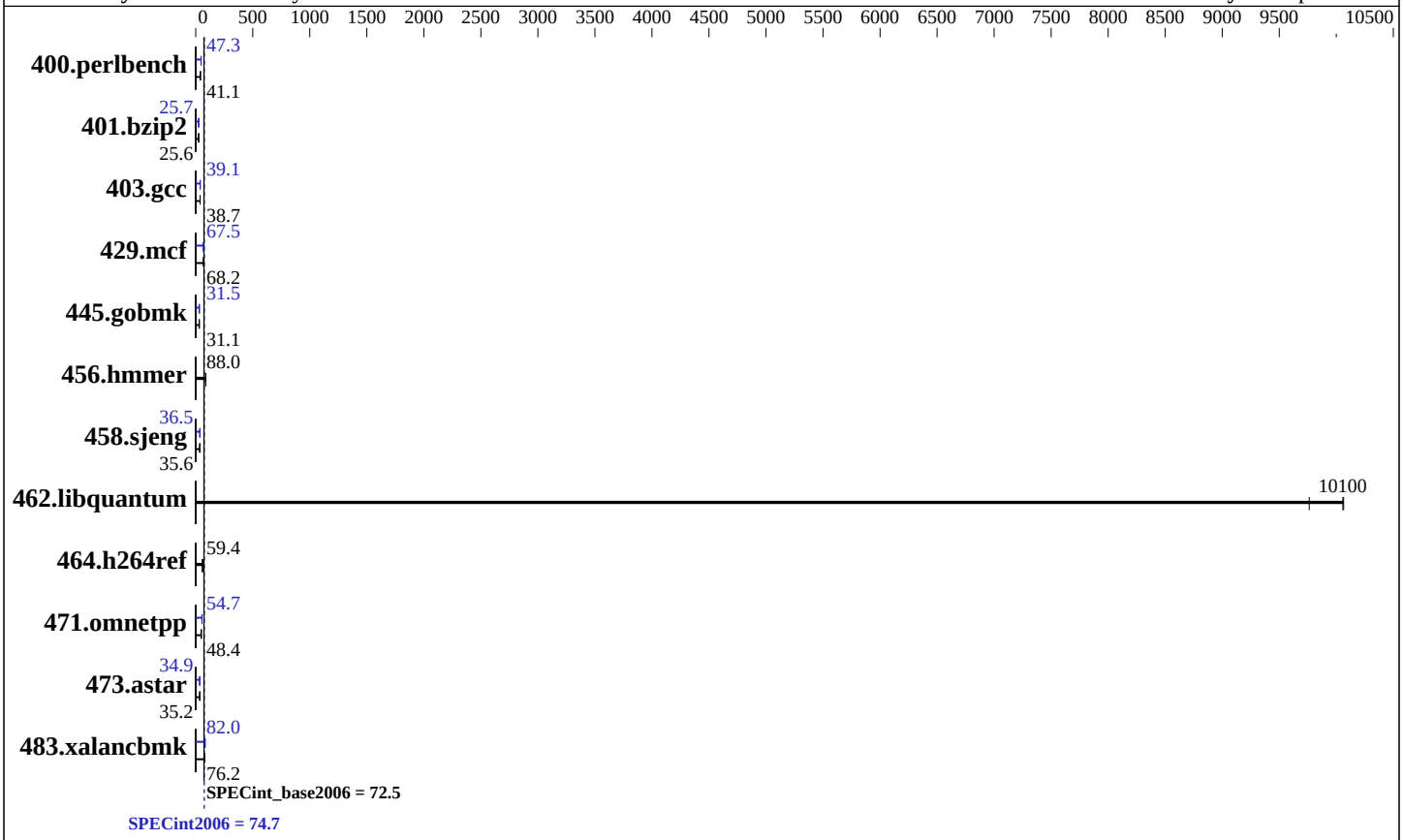
Cisco UCS C220 M4 (Intel Xeon E5-2699A v4, 2.40 GHz)

SPECint®2006 = 74.7

SPECint_base2006 = 72.5

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Jan-2017
Hardware Availability: Oct-2016
Software Availability: Sep-2016



Hardware

CPU Name:	Intel Xeon E5-2699A v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	44 cores, 2 chips, 22 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	55 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem:	1 x 300 GB SAS, 15K RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default
Compiler:	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	240	40.7	237	41.1	**238**	**41.1**	207	47.3	**207**	**47.3**	207	47.3
401.bzip2	**376**	**25.6**	376	25.6	376	25.7	**376**	**25.7**	376	25.6	375	25.7
403.gcc	**208**	**38.7**	208	38.7	208	38.6	**206**	**39.1**	206	39.1	204	39.4
429.mcf	134	68.0	132	69.2	**134**	**68.2**	**135**	**67.5**	134	67.8	137	66.8
445.gobmk	338	31.0	337	31.1	**338**	**31.1**	**333**	**31.5**	332	31.6	333	31.5
456.hmmer	106	87.9	**106**	**88.0**	106	88.2	106	87.9	**106**	**88.0**	106	88.2
458.sjeng	**340**	**35.6**	340	35.6	341	35.5	332	36.5	332	36.5	**332**	**36.5**
462.libquantum	**2.06**	**10100**	2.06	10100	2.12	9760	**2.06**	**10100**	2.06	10100	2.12	9760
464.h264ref	374	59.2	372	59.5	**373**	**59.4**	374	59.2	372	59.5	**373**	**59.4**
471.omnetpp	129	48.5	129	48.3	**129**	**48.4**	**114**	**54.7**	115	54.6	114	54.9
473.astar	199	35.2	**200**	**35.2**	200	35.0	**201**	**34.9**	201	35.0	203	34.5
483.xalancbmk	91.6	75.3	**90.5**	**76.2**	90.5	76.3	**84.2**	**82.0**	83.8	82.3	84.2	81.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Intel Hyper-Threading Technology option set to Disabled
CPU performance set to Enterprise
Power Technology set to Energy Efficient
Energy Performance set to Balanced Performance
Memory RAS configuration set to Maximum Performance
Memory Power Saving Mode set to Disabled
QPI Snoop Mode set to Home Directory Snoop with OSB
 Sysinfo program /home/CISCO_Benchmarks/cpu2006/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux-f3gd Fri Jan 20 07:06:46 2017

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz
       2 "physical id"s (chips)
```

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Platform Notes (Continued)

```
      44 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 22
      siblings  : 22
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
   cache size : 56320 KB

From /proc/meminfo
   MemTotal:       264366144 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-f3gd 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 17 00:00

SPEC is set to: /home/CISCO_Benchmarks/cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sdb1      xfs   238G  108G  131G  46% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.
```

Continued on next page

Platform Notes (Continued)

```
    BIOS Cisco Systems, Inc. C220M4.2.0.9.42.021920161702 02/19/2016
    Memory:
     16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz
     8x NO DIMM NO DIMM

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/CISCO_Benchmarks/cpu2006/libs/32:/home/CISCO_Benchmarks/cpu2006/libs/64:/home/CISCO_Benchmarks/cpu2006/sh10.2"
OMP_NUM_THREADS = "44"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
        403.gcc: -DSPEC_CPU_LP64
        429.mcf: -DSPEC_CPU_LP64
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
      473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

C++ benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-qopt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div -auto-ilp32 -qopt-prefetch`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -qopt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)

456.hmmer: `basepeak = yes`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-qopt-ra-region-strategy=block -Wl,-z,muldefs -L/sh10.2 -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -Wl,-z,muldefs -L/sh10.2 -lsmartheap`

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml`

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