Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
Synergy 480 Gen9  
(2.60 GHz, Intel Xeon E5-2697A v4)  

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
<th>1530</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>1480</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 3  
**Test date:** Nov-2016  
**Hardware Availability:** Dec-2016  
**Test sponsor:** HPE  
**Software Availability:** Sep-2016  
**Tested by:** HPE

### Software

- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP1  
  Kernel 3.12.49-11-default  
- **Compiler:** C/C++: Version 17.0.0.098 of Intel C++ Studio XE for Linux  
- **Auto Parallel:** No  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V10.2

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E5-2697A v4</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characters:</td>
<td>Intel Turbo Boost Technology up to 3.60 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2600</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>32 cores, 2 chips, 16 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1.2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>40 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 600 GB 10 K SAS, RAID 0</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

---

### Graph

<table>
<thead>
<tr>
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</tr>
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</table>

---

**Note:** All performance results are for comparison purposes only and represent average performance. Variations may occur due to different hardware configurations and workloads.
**SPEC CINT2006 Result**

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**SPECint_rate2006 = 1530**  
**SPECint_rate_base2006 = 1480**

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- **Test date:** Nov-2016  
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- **Software Availability:** Sep-2016

---

## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>64</td>
<td>534</td>
<td>1170</td>
<td>534</td>
<td>1170</td>
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<td>1360</td>
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<tr>
<td>401.bzip2</td>
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<td>841</td>
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<td>839</td>
<td>736</td>
<td>841</td>
<td>734</td>
<td>64</td>
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<tr>
<td>403.mcf</td>
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<td>309</td>
<td>1890</td>
<td>307</td>
<td>1900</td>
<td>64</td>
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<td>1900</td>
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<td>445.gobmk</td>
<td>64</td>
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<td>1020</td>
<td>658</td>
<td>1020</td>
<td>657</td>
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<tr>
<td>458.sjeng</td>
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<td>462.libquantum</td>
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<tr>
<td>471.omnetpp</td>
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<td><strong>559</strong></td>
<td><strong>716</strong></td>
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<td>473.astar</td>
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<tr>
<td>483.xalancbmk</td>
<td>64</td>
<td>285</td>
<td>1550</td>
<td>286</td>
<td>1550</td>
<td>286</td>
<td>1550</td>
<td>64</td>
<td>285</td>
<td>1550</td>
<td>286</td>
<td>1550</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
```bash  
echo always > /sys/kernel/mm/transparent_hugepage/enabled  
```

Filesystem page cache cleared with:  
```bash  
echo 1 > /proc/sys/vm/drop_caches  
```

runspec command invoked through numactl i.e.:  
```bash  
umactl --interleave=all runspec <etc>  
```

---

## Platform Notes

**BIOS Configuration:**  
Power Profile set to Custom  
Power Regulator to Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C6 State  
Minimum Processor Idle Power Package C-State set to No Package State  
Energy/Performance Bias set to Maximum Performance  
Collaborative Power Control set to Disabled  
QPI Snoop Configuration set to Cluster on Die  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  

Continued on next page
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SPECint_rate2006 = 1530  
SPECint_rate_base2006 = 1480

CPU2006 license: 3  
Test sponsor: HPE  
Tested by: HPE  

Platform Notes (Continued)

Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-8p7b Fri Nov 11 08:38:16 2016

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2697A v4 @ 2.60GHz  
2 "physical id"s (chips)  
64 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The  
following excerpts from /proc/cpuinfo might not be reliable. Use with  
caution.)  
cpu cores : 16  
siblings  : 32  
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
cache size : 20480 KB

From /proc/meminfo  
MemTotal: 26454332 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d  
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:  
SUSE Linux Enterprise Server 12 (x86_64)  
VERSION = 12  
PATCHLEVEL = 1  
# This file is deprecated and will be removed in a future service pack or  
release.  
# Please check /etc/os-release for details about this release.

os-release:  
NAME="SLES"  
VERSION="12-SP1"  
VERSION_ID="12.1"  
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"  
ID="sles"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:  
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 11 08:37

Continued on next page
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Hardware Availability: Dec-2016
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Platform Notes (Continued)

SPEC is set to: /home/cpu2006
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda4  xfs   517G  3.9G  513G  1%  /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I37 09/14/2016
Memory:
8x UNKNOWN NOT AVAILABLE
16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 256 and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32
401.bzip2: -D\_FILE\_OFFSET\_BITS=64
403.gcc: -D\_FILE\_OFFSET\_BITS=64
429.mcf: -D\_FILE\_OFFSET\_BITS=64
445.gobmk: -D\_FILE\_OFFSET\_BITS=64
456.hmmer: -D\_FILE\_OFFSET\_BITS=64
458.sjeng: -D\_FILE\_OFFSET\_BITS=64

Continued on next page
SPEC CINT2006 Result
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Base Portability Flags (Continued)

462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
- qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
- qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

Continued on next page
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Tested by: HPE

Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto-ilp32
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

Continued on next page
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**Peak Optimization Flags (Continued)**

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-ra-region-strategy=block -qopt-mem-layout-trans=3 -Wl,-zmuldefs -L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.html

http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.xml

http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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