SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

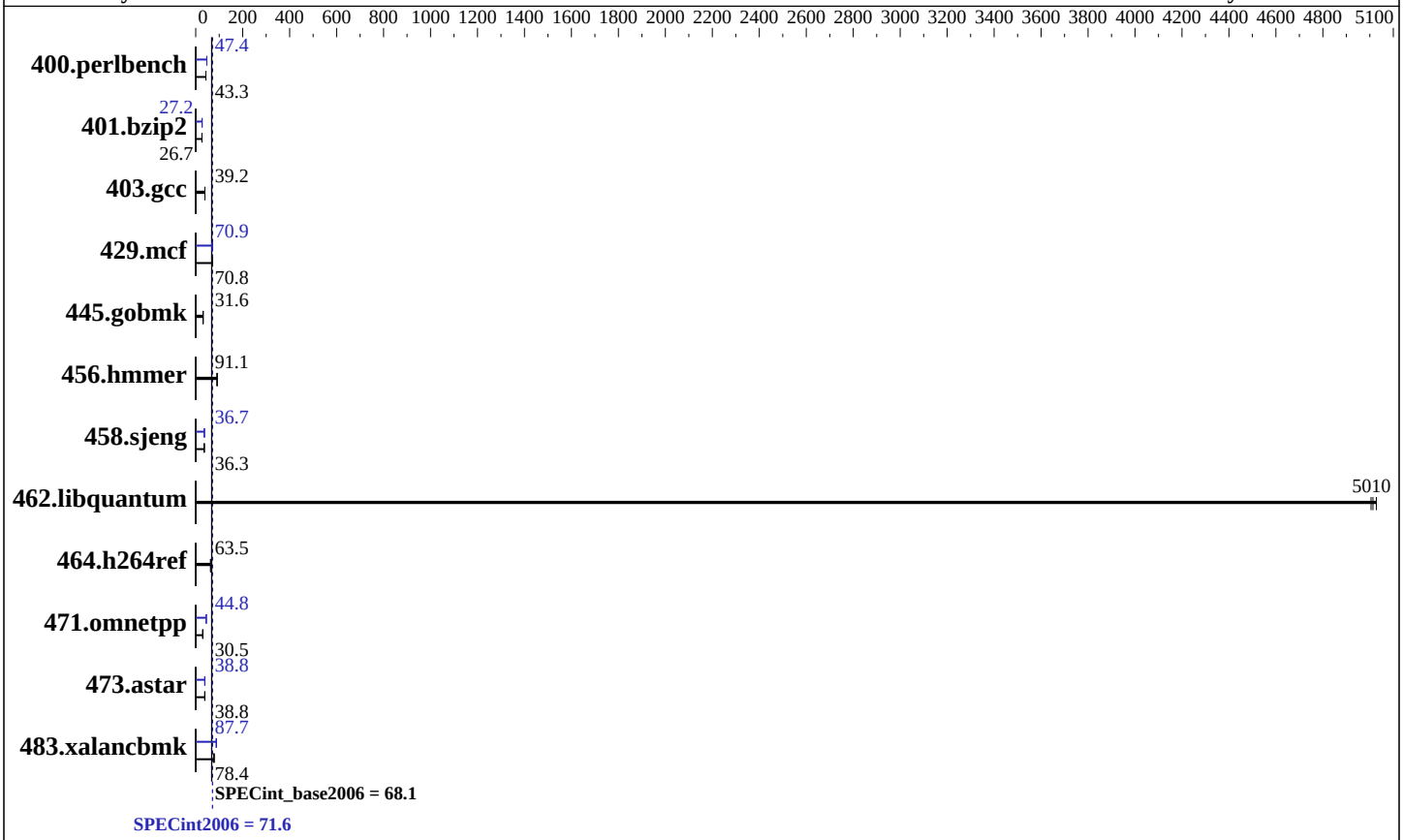
Huawei RH1288 V3 (Intel Xeon E5-2643 v4)

SPECint®2006 = 71.6

SPECint_base2006 = 68.1

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Oct-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016



Hardware

CPU Name:	Intel Xeon E5-2643 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (8 x 32 GB 2Rx8 PC4-2400T-R)
Disk Subsystem:	2 x 600 GB SAS, 10K RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

Huawei RH1288 V3 (Intel Xeon E5-2643 v4)

SPECint2006 = 71.6

SPECint_base2006 = 68.1

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Oct-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**226**	**43.3**	225	43.5	226	43.3	206	47.4	207	47.3	**206**	**47.4**
401.bzip2	363	26.6	361	26.7	**362**	**26.7**	355	27.2	**355**	**27.2**	355	27.2
403.gcc	**205**	**39.2**	206	39.1	205	39.2	**205**	**39.2**	206	39.1	205	39.2
429.mcf	**129**	**70.8**	129	70.8	130	70.3	131	69.9	**129**	**70.9**	128	71.2
445.gobmk	**332**	**31.6**	332	31.6	333	31.5	**332**	**31.6**	332	31.6	333	31.5
456.hmmer	102	91.2	102	91.0	**102**	**91.1**	102	91.2	102	91.0	**102**	**91.1**
458.sjeng	333	36.3	**333**	**36.3**	333	36.3	329	36.7	**329**	**36.7**	329	36.7
462.libquantum	4.14	5010	4.12	5030	**4.13**	**5010**	4.14	5010	4.12	5030	**4.13**	**5010**
464.h264ref	350	63.3	**349**	**63.5**	347	63.7	350	63.3	**349**	**63.5**	347	63.7
471.omnetpp	**205**	**30.5**	211	29.6	205	30.5	**140**	**44.8**	140	44.6	139	44.9
473.astar	181	38.8	**181**	**38.8**	181	38.7	181	38.7	181	38.8	**181**	**38.8**
483.xalancbmk	**88.0**	**78.4**	87.5	78.8	90.5	76.2	**78.7**	**87.7**	78.7	87.7	78.6	87.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Set Power Efficiency Mode to Peformance
Set Snoop Mode to ES mode
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-1g2g Wed Oct 26 17:15:31 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
      2 "physical id"s (chips)
      12 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
```

Continued on next page

Platform Notes (Continued)

```
        siblings  : 6
        physical 0: cores 0 1 2 3 6 7
        physical 1: cores 0 1 2 3 6 7
     cache size : 20480 KB

  From /proc/meminfo
     MemTotal:       264078020 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12 SP1

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 1
        # This file is deprecated and will be removed in a future service pack or
        release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12-SP1"
        VERSION_ID="12.1"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp1"

  uname -a:
     Linux linux-1g2g 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
     (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Oct 26 11:42

  SPEC is set to: /spec16
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda1      ext4  542G   15G  526G   3% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Insyde Corp. 3.31 08/22/2016
    Memory:
     8x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz
     8x NO DIMM NO DIMM

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "12"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-opt-prefetch`
`-ansi-alias`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Nov 15 16:07:44 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 November 2016.