



SPEC® CFP2006 Result

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Acer Incorporated
AW2000h-AW370h F3

SPECfp®_rate2006 = 354
SPECfp_rate_base2006 = 347

CPU2006 license: 97

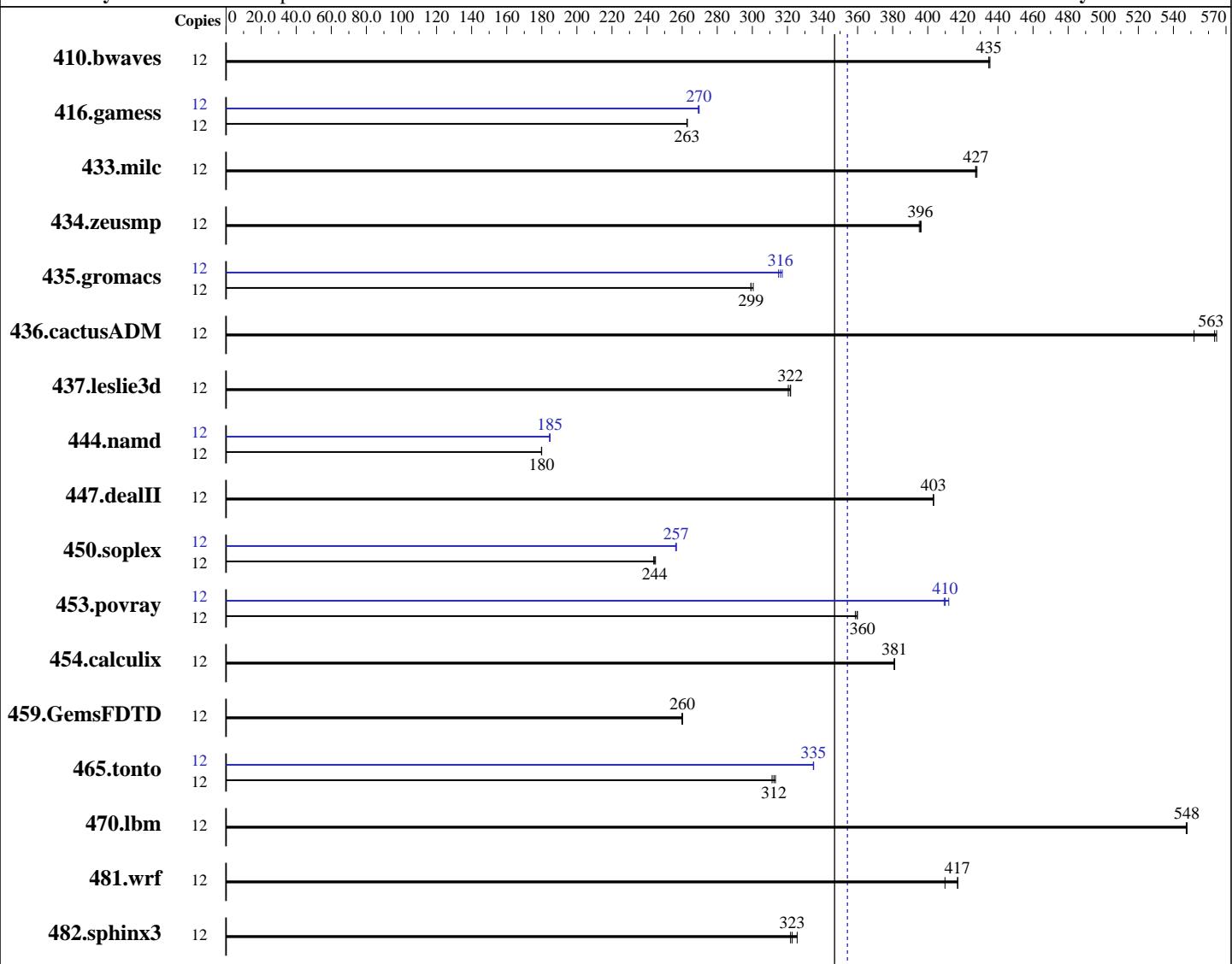
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Sep-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



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Hardware

CPU Name: Intel Xeon E5-2603 v4
CPU Characteristics:
CPU MHz: 1700
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
Compiler: 3.10.0-327.el7.x86_64
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs

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Continued on next page



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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2400T-R , running at 1866 MHz)
Disk Subsystem: 1 x 4000 GB SATA , 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	375	435	375	435	375	435	12	375	435	375	435	375	435	375	435
416.gamess	12	894	263	894	263	894	263	12	872	270	871	270	873	269		
433.milc	12	258	427	257	428	258	427	12	258	427	257	428	258	427		
434.zeusmp	12	276	396	276	395	276	396	12	276	396	276	395	276	396		
435.gromacs	12	286	299	285	301	286	299	12	271	316	270	317	272	315		
436.cactusADM	12	254	565	260	552	254	563	12	254	565	260	552	254	563		
437.leslie3d	12	352	320	351	322	350	322	12	352	320	351	322	350	322		
444.namd	12	535	180	535	180	535	180	12	521	185	522	184	521	185		
447.dealII	12	341	403	340	403	340	403	12	341	403	340	403	340	403		
450.soplex	12	411	244	409	245	410	244	12	390	257	390	256	390	257		
453.povray	12	177	360	178	360	178	359	12	155	412	156	410	156	409		
454.calculix	12	260	381	260	381	260	381	12	260	381	260	381	260	381		
459.GemsFDTD	12	490	260	489	260	489	260	12	490	260	489	260	489	260		
465.tonto	12	378	312	379	311	377	313	12	353	335	353	335	353	335		
470.lbm	12	301	547	301	548	301	548	12	301	547	301	548	301	548		
481.wrf	12	321	417	327	410	322	417	12	321	417	327	410	322	417		
482.sphinx3	12	725	323	727	322	718	326	12	725	323	727	322	718	326		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date::: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri Sep 30 15:11:12 2016
Continued on next page



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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v4 @ 1.70GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 6
        siblings : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      131745224 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 30 05:45

```
SPEC is set to: /usr/cpu2006
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs   1000G   64G  937G   7% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Continued on next page



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Platform Notes (Continued)

BIOS Intel Corporation SE5C610.86B.01.01.0015.012820160943 01/28/2016

Memory:

16x NO DIMM NO DIMM

8x Samsung M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz, configured at 1866 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64

Continued on next page



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Base Portability Flags (Continued)

```
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -D_FILE_OFFSET_BITS=64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -opt-malloc-options=3
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -unroll14 -ansi-alias

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Peak Optimization Flags (Continued)

Fortran benchmarks:

```
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
             -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-prefetch -auto-ilp32
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.3-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.3-revA.xml>



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Tested with SPEC CPU2006 v1.2.

Report generated on Tue Nov 29 19:07:42 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 November 2016.