Cisco Systems
Cisco UCS B200 M4 (Intel Xeon E5-2667 v4, 3.20 GHz)

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Aug-2016
Hardware Availability: Apr-2016
Software Availability: Dec-2015

SPECint\_rate\_2006 = 941
SPECint\_rate\_base\_2006 = 897

---

<table>
<thead>
<tr>
<th>SPECint_rate_2006 = 941</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5-2667 v4</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 3.60 GHz</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>3200</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>16 cores, 2 chips, 8 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>1 x 600 GB SAS HDD, 10K RPM</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Operating System</th>
<th>SUSE Linux Enterprise Server 12 SP1 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel</td>
<td>No</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software</td>
<td>Microquill SmartHeap V10.2</td>
</tr>
</tbody>
</table>
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SPEC CINT2006 Result

Test date: Aug-2016
Hardware Availability: Apr-2016
Software Availability: Dec-2015

RESULTS TABLE

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>32</td>
<td>483</td>
<td>647</td>
<td>482</td>
<td>649</td>
<td>481</td>
<td>650</td>
<td>32</td>
<td>390</td>
<td>802</td>
<td>392</td>
<td>798</td>
<td>392</td>
<td>798</td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>32</td>
<td>711</td>
<td>434</td>
<td>709</td>
<td>435</td>
<td>708</td>
<td>436</td>
<td>32</td>
<td>689</td>
<td>448</td>
<td>690</td>
<td>448</td>
<td>691</td>
<td>447</td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>32</td>
<td>392</td>
<td>658</td>
<td>388</td>
<td>663</td>
<td>393</td>
<td>656</td>
<td>32</td>
<td>392</td>
<td>657</td>
<td>389</td>
<td>663</td>
<td>391</td>
<td>659</td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>32</td>
<td>254</td>
<td>1150</td>
<td>254</td>
<td>1150</td>
<td>254</td>
<td>1150</td>
<td>32</td>
<td>254</td>
<td>1150</td>
<td>254</td>
<td>1150</td>
<td>254</td>
<td>1150</td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>32</td>
<td>569</td>
<td>590</td>
<td>569</td>
<td>590</td>
<td>570</td>
<td>589</td>
<td>32</td>
<td>556</td>
<td>603</td>
<td>558</td>
<td>602</td>
<td>558</td>
<td>602</td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>32</td>
<td>217</td>
<td>1380</td>
<td>215</td>
<td>1390</td>
<td>215</td>
<td>1390</td>
<td>32</td>
<td>179</td>
<td>1670</td>
<td>179</td>
<td>1670</td>
<td>179</td>
<td>1670</td>
<td></td>
<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>32</td>
<td>631</td>
<td>614</td>
<td>632</td>
<td>613</td>
<td>632</td>
<td>613</td>
<td>32</td>
<td>596</td>
<td>650</td>
<td>596</td>
<td>649</td>
<td>596</td>
<td>649</td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>32</td>
<td>72.6</td>
<td>9130</td>
<td>72.5</td>
<td>9140</td>
<td>72.5</td>
<td>9140</td>
<td>32</td>
<td>72.6</td>
<td>9130</td>
<td>72.5</td>
<td>9140</td>
<td>72.5</td>
<td>9140</td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>32</td>
<td>680</td>
<td>1040</td>
<td>648</td>
<td>1090</td>
<td>643</td>
<td>1100</td>
<td>32</td>
<td>632</td>
<td>1120</td>
<td>636</td>
<td>1110</td>
<td>655</td>
<td>1080</td>
<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>32</td>
<td>460</td>
<td>435</td>
<td>460</td>
<td>435</td>
<td>460</td>
<td>435</td>
<td>32</td>
<td>433</td>
<td>462</td>
<td>433</td>
<td>462</td>
<td>432</td>
<td>463</td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>32</td>
<td>423</td>
<td>531</td>
<td>423</td>
<td>531</td>
<td>423</td>
<td>531</td>
<td>32</td>
<td>423</td>
<td>531</td>
<td>423</td>
<td>531</td>
<td>423</td>
<td>531</td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>32</td>
<td>201</td>
<td>1100</td>
<td>201</td>
<td>1100</td>
<td>201</td>
<td>1100</td>
<td>32</td>
<td>201</td>
<td>1100</td>
<td>201</td>
<td>1100</td>
<td>201</td>
<td>1100</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
CPU performance set to Enterprise
Power Technology set to Energy Efficient
Energy Performance BIAS setting set to Balanced Performance
Memory RAS configuration set to Maximum Performance
Memory Power Saving Mode set to Disabled
QPI Snoop Mode set to Home Directory Snoop with OSB
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2667 v4 @ 3.20GHz
Continued on next page
Cisco Systems
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<table>
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<tr>
<th>SPECint_rate2006</th>
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<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>897</td>
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Platform Notes (Continued)

2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 2 3 4 8 10 11 12
physical 1: cores 0 2 3 4 8 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal: 264567760 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 31 23:07

SPEC is set to: /opt/cpu2006-1.2

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 xfs 559G 11G 549G 2% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B200M4.3.1.1.11.110420151758 11/04/2015
Memory:
16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

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Platform Notes (Continued)

8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
# SPEC CINT2006 Result

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## Peak Other Flags

**C benchmarks:**

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at  
http://www.spec.org/cpu2006/flags:Cisco-Platform-Settings-V1.2-revE.html

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2006/flags:Intel-ic16.0-official-linux64.xml  
http://www.spec.org/cpu2006/flags:Cisco-Platform-Settings-V1.2-revE.xml

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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