SPEC® CFP2006 Result

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Dell Inc.

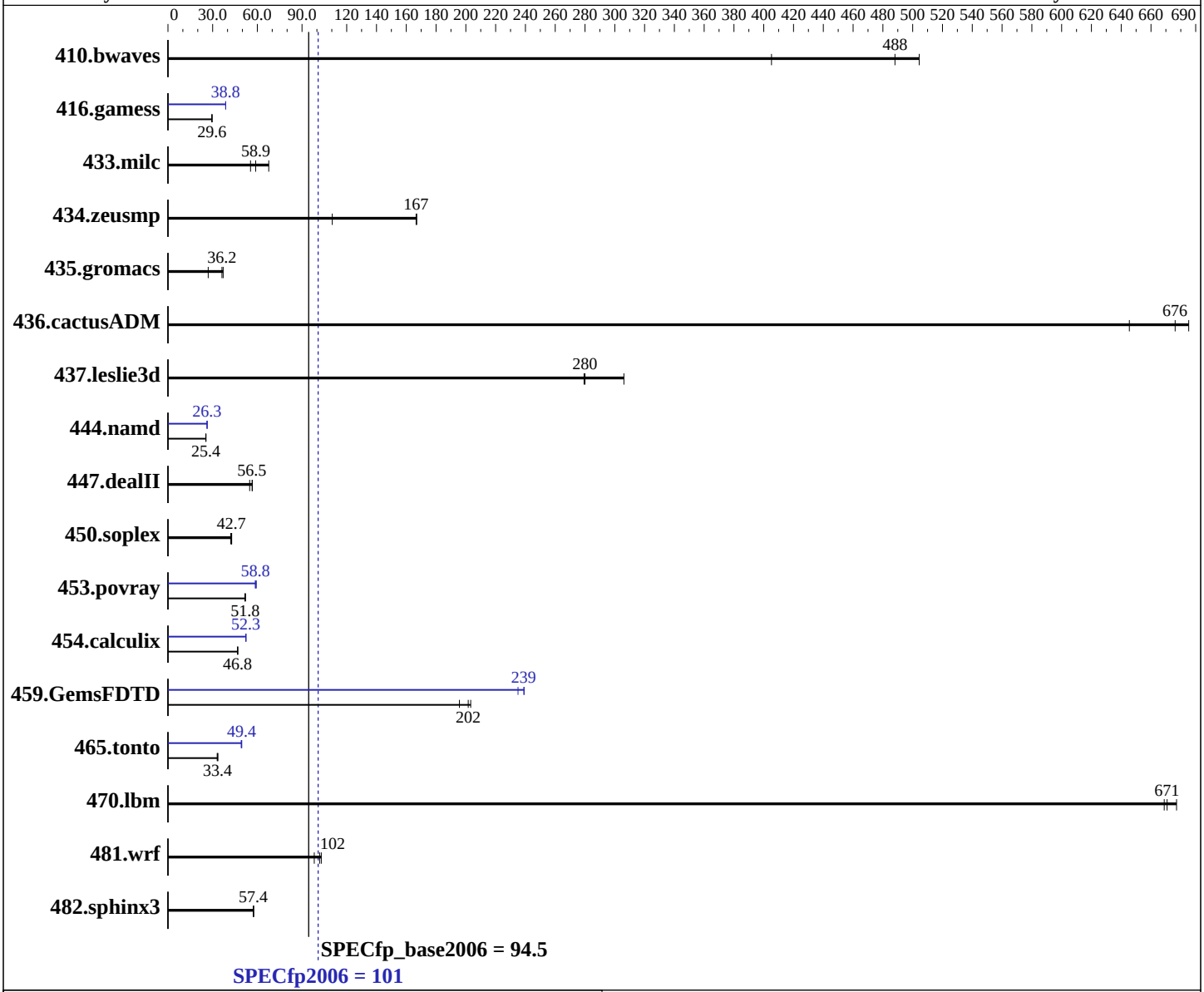
PowerEdge FC430 (Intel Xeon E5-2630L v4, 1.80 GHz)

SPECfp®2006 = 101

SPECfp_base2006 = 94.5

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jun-2016
Hardware Availability: Jun-2016
Software Availability: Nov-2015



Hardware

CPU Name:	Intel Xeon E5-2630L v4
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs

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L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (8 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem:	1 x 240 GB SATA SSD
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.5	405	**27.8**	**488**	26.9	504	33.5	405	**27.8**	**488**	26.9	504
416.gamess	662	29.6	**662**	**29.6**	664	29.5	506	38.7	505	38.8	**505**	**38.8**
433.milc	166	55.4	136	67.7	**156**	**58.9**	166	55.4	136	67.7	**156**	**58.9**
434.zeusmp	82.5	110	54.5	167	**54.6**	**167**	82.5	110	54.5	167	**54.6**	**167**
435.gromacs	264	27.0	**197**	**36.2**	193	37.1	264	27.0	**197**	**36.2**	193	37.1
436.cactusADM	18.5	645	17.4	685	**17.7**	**676**	18.5	645	17.4	685	**17.7**	**676**
437.leslie3d	33.6	279	30.7	306	**33.6**	**280**	33.6	279	30.7	306	**33.6**	**280**
444.namd	**315**	**25.4**	316	25.4	314	25.5	**305**	**26.3**	306	26.2	305	26.3
447.dealII	**203**	**56.5**	209	54.9	202	56.7	**203**	**56.5**	209	54.9	202	56.7
450.soplex	195	42.8	198	42.1	**195**	**42.7**	195	42.8	198	42.1	**195**	**42.7**
453.povray	**103**	**51.8**	102	52.1	103	51.7	**90.4**	**58.8**	89.7	59.3	90.7	58.6
454.calculix	176	46.8	**176**	**46.8**	176	46.9	**158**	**52.3**	158	52.3	158	52.4
459.GemsFDTD	54.2	196	52.2	203	**52.6**	**202**	44.4	239	45.1	235	**44.4**	**239**
465.tonto	298	33.0	294	33.5	**295**	**33.4**	200	49.2	199	49.5	**199**	**49.4**
470.lbm	20.3	677	**20.5**	**671**	20.5	669	20.3	677	**20.5**	**671**	20.5	669
481.wrf	109	103	114	98.2	**110**	**102**	109	103	114	98.2	**110**	**102**
482.sphinx3	340	57.3	**340**	**57.4**	338	57.6	340	57.3	**340**	**57.4**	338	57.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Snoop Mode set to Home Snoop
Virtualization Technology disabled
System Profile set to custom
CPU Power Management set to Maximum Performance
C States set to Autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
```

Continued on next page

Platform Notes (Continued)

```
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Wed Jun 29 22:44:34 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2630L v4 @ 1.80GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 20
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       264039396 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
   EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 29 16:59

SPEC is set to: /root/cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   234G  9.7G  225G   5% /
Additional information from dmidecode:
```

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Platform Notes (Continued)

```
      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Dell Inc. 2.2.1 06/07/2016
    Memory:
     8x 00CE00B300CE M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz, configured at 2133
     MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "20"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main

Continued on next page

Base Portability Flags (Continued)

436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

Continued on next page

Peak Optimization Flags (Continued)

465.tonto (continued):
`-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml`

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Tested with SPEC CPU2006 v1.2.
Report generated on Wed Aug 24 13:13:23 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 August 2016.