SPEC® CFP2006 Result

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Lenovo Group Limited

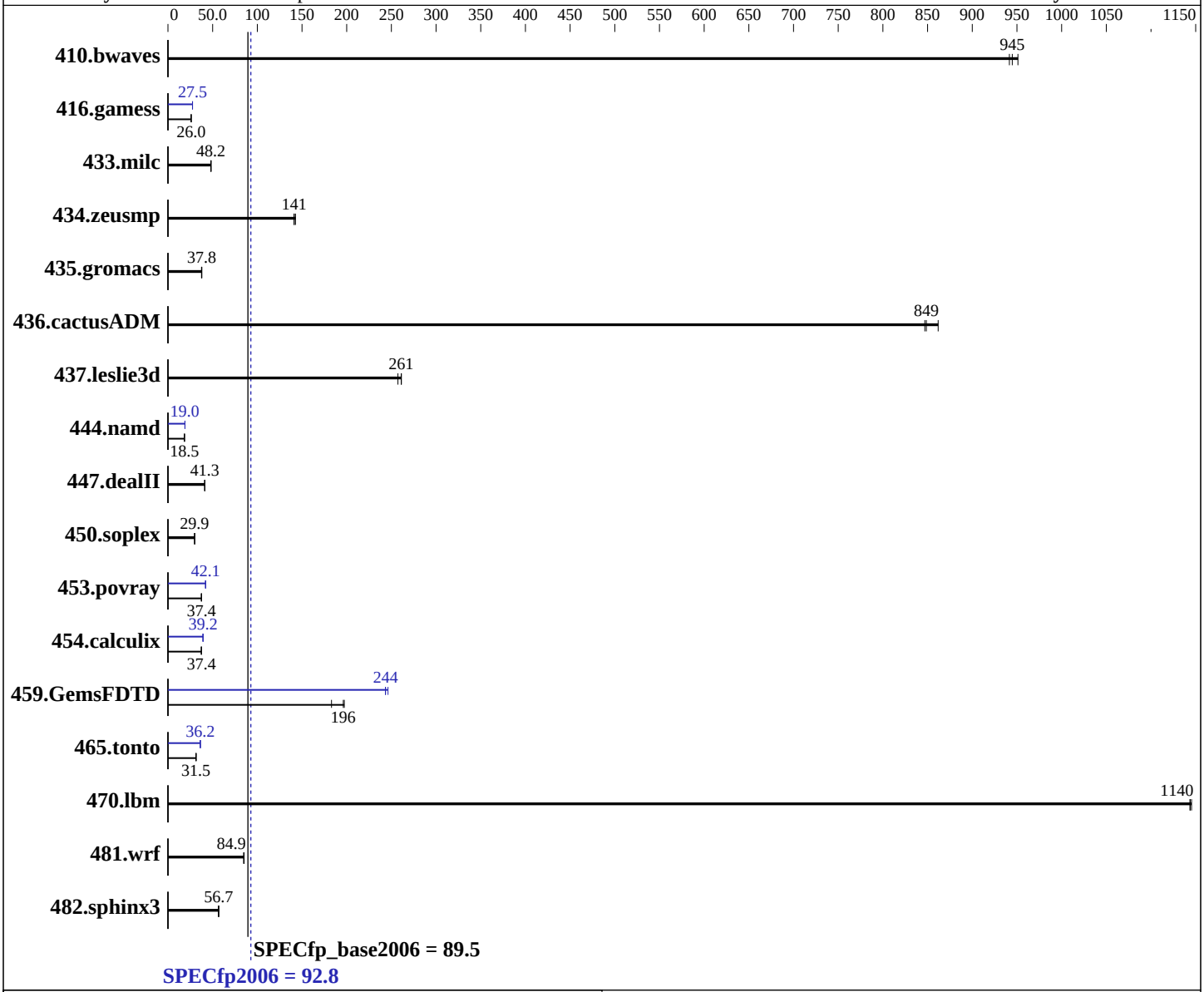
Lenovo System x3850 X6
(2.10 GHz, Intel Xeon E7-4809 v4)

SPECfp®2006 = 92.8

SPECfp_base2006 = 89.5

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Jul-2016
Hardware Availability: Jun-2016
Software Availability: Dec-2015



Hardware

CPU Name:	Intel Xeon E7-4809 v4
CPU Characteristics:	
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	14.3	951	**14.4**	**945**	14.4	942	14.3	951	**14.4**	**945**	14.4	942
416.gamess	**754**	**26.0**	752	26.0	755	25.9	**713**	**27.5**	714	27.4	711	27.5
433.milc	190	48.2	191	48.0	**191**	**48.2**	190	48.2	191	48.0	**191**	**48.2**
434.zeusmp	64.5	141	63.8	143	**64.4**	**141**	64.5	141	63.8	143	**64.4**	**141**
435.gromacs	**189**	**37.8**	190	37.5	189	37.9	**189**	**37.8**	190	37.5	189	37.9
436.cactusADM	**14.1**	**849**	14.1	847	13.9	862	**14.1**	**849**	14.1	847	13.9	862
437.leslie3d	**36.0**	**261**	36.5	257	36.0	261	**36.0**	**261**	36.5	257	36.0	261
444.namd	435	18.5	434	18.5	**434**	**18.5**	421	19.0	**421**	**19.0**	422	19.0
447.dealII	279	41.0	277	41.3	**277**	**41.3**	279	41.0	277	41.3	**277**	**41.3**
450.soplex	**279**	**29.9**	279	29.9	280	29.8	**279**	**29.9**	279	29.9	280	29.8
453.povray	142	37.6	142	37.4	**142**	**37.4**	**126**	**42.1**	126	42.2	127	42.0
454.calculix	221	37.3	220	37.4	**221**	**37.4**	**210**	**39.2**	210	39.3	211	39.2
459.GemsFDTD	53.8	197	58.0	183	**54.1**	**196**	43.6	243	43.1	246	**43.6**	**244**
465.tonto	315	31.2	**313**	**31.5**	312	31.6	**272**	**36.2**	272	36.2	272	36.2
470.lbm	12.0	1150	12.0	1140	**12.0**	**1140**	12.0	1150	12.0	1140	**12.0**	**1140**
481.wrf	**132**	**84.9**	132	84.8	131	85.2	**132**	**84.9**	132	84.8	131	85.2
482.sphinx3	**344**	**56.7**	342	57.0	345	56.4	**344**	**56.7**	342	57.0	345	56.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Configuration:
 Operating Mode set to "Maximum Performance"
 Hyper-Threading set to Disable
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on Draco-SLES12SP1 Fri Jul  8 06:29:41 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E7-4809 v4 @ 2.10GHz
          4 "physical id"s (chips)
          32 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 8
          siblings  : 8
          physical 0: cores 0 1 2 3 4 5 6 7
          physical 1: cores 0 1 2 3 4 5 6 7
          physical 2: cores 0 1 2 3 4 5 6 7
          physical 3: cores 0 1 2 3 4 5 6 7
       cache size : 20480 KB

    From /proc/meminfo
       MemTotal:       529167140 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       SuSE-release:
          SUSE Linux Enterprise Server 12 (x86_64)
          VERSION = 12
          PATCHLEVEL = 1
          # This file is deprecated and will be removed in a future service pack or
          release.
          # Please check /etc/os-release for details about this release.
       os-release:
          NAME="SLES"
          VERSION="12-SP1"
          VERSION_ID="12.1"
          PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
          ID="sles"
          ANSI_COLOR="0;32"
          CPE_NAME="cpe:/o:suse:sles:12:sp1"

    uname -a:
       Linux Draco-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
       (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Jul 7 22:43

    SPEC is set to: /home/cpu2006-1.2-ic16.0
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda4      xfs   701G   57G  644G   9% /home
    Additional information from dmidecode:

       Warning: Use caution when you interpret this section. The 'dmidecode' program
       reads system data which is "intended to allow hardware to be accurately
```

Continued on next page

Platform Notes (Continued)

```
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS LENOVO -[A9E135CUS-3.10]- 06/16/2016
    Memory:
     32x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 1333 MHz
     64x NO DIMM Unknown

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "32"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages disabled with:
 echo never > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
```

Continued on next page

Base Portability Flags (Continued)

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page

Peak Optimization Flags (Continued)

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml`

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