Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.30 GHz, Intel Xeon E5-2697 v4)

SPECint®2006 = 73.1
SPECint_base2006 = 70.9

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

CPU Name: Intel Xeon E5-2697 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
CPU(s) orderable: 1.2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 45 MB I+D on chip per core
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)
Compiler: C/C++ Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
SPEC CINT2006 Result

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Specint Test Date: Jun-2016
Hardware Availability: Mar-2016
Software Availability: Nov-2015

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>237</td>
<td>41.2</td>
<td>235</td>
<td>41.7</td>
<td>234</td>
<td>41.7</td>
<td>215</td>
<td>45.4</td>
<td>215</td>
<td>45.4</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>381</td>
<td>25.3</td>
<td>381</td>
<td>25.3</td>
<td>382</td>
<td>25.3</td>
<td>381</td>
<td>25.3</td>
<td>381</td>
<td>25.3</td>
</tr>
<tr>
<td>403.mcc</td>
<td>213</td>
<td>37.8</td>
<td>213</td>
<td>37.8</td>
<td>213</td>
<td>37.8</td>
<td>205</td>
<td>39.2</td>
<td>206</td>
<td>39.2</td>
</tr>
<tr>
<td>404.gobmk</td>
<td>346</td>
<td>63.7</td>
<td>143</td>
<td>63.6</td>
<td>141</td>
<td>64.5</td>
<td>143</td>
<td>63.7</td>
<td>143</td>
<td>63.6</td>
</tr>
<tr>
<td>453.mcf</td>
<td>143</td>
<td>63.7</td>
<td>143</td>
<td>63.6</td>
<td>141</td>
<td>64.5</td>
<td>143</td>
<td>63.7</td>
<td>143</td>
<td>63.6</td>
</tr>
<tr>
<td>455.hmmer</td>
<td>106</td>
<td>88.2</td>
<td>339</td>
<td>88.2</td>
<td>339</td>
<td>88.2</td>
<td>106</td>
<td>88.2</td>
<td>106</td>
<td>88.2</td>
</tr>
<tr>
<td>456.sjeng</td>
<td>339</td>
<td>35.7</td>
<td>339</td>
<td>35.7</td>
<td>339</td>
<td>35.7</td>
<td>335</td>
<td>36.1</td>
<td>335</td>
<td>36.1</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>2.61</td>
<td>7950</td>
<td>2.61</td>
<td>7950</td>
<td>2.61</td>
<td>7950</td>
<td>2.61</td>
<td>7950</td>
<td>2.61</td>
<td>7950</td>
</tr>
<tr>
<td>463.h264ref</td>
<td>379</td>
<td>58.4</td>
<td>379</td>
<td>58.4</td>
<td>379</td>
<td>58.4</td>
<td>379</td>
<td>58.4</td>
<td>379</td>
<td>58.4</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>127</td>
<td>49.0</td>
<td>127</td>
<td>49.2</td>
<td>127</td>
<td>49.2</td>
<td>113</td>
<td>55.3</td>
<td>113</td>
<td>55.3</td>
</tr>
<tr>
<td>473.astar</td>
<td>188</td>
<td>37.4</td>
<td>190</td>
<td>36.9</td>
<td>188</td>
<td>37.3</td>
<td>188</td>
<td>37.4</td>
<td>190</td>
<td>36.9</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>87.5</td>
<td>78.8</td>
<td>87.5</td>
<td>78.9</td>
<td>87.5</td>
<td>78.8</td>
<td>79.1</td>
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<td>78.9</td>
<td>87.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
- HP Power Profile set to Custom
- HP Power Regulator to HP Static High Performance Mode
- Minimum Processor Idle Power Core C-State set to C1E State
- Minimum Processor Idle Power Package C-State set to No Package State
- QPI Snoop Configuration set to Home Snoop
- Collaborative Power Control set to Disabled
- Thermal Configuration set to Maximum Cooling
- Processor Power and Utilization Monitoring set to Disabled
- Memory Refresh Rate set to 1x Refresh
- Intel Hyperthreading set to Disabled

Sysinfo program /home/intel_binary/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1

This section contains SUT (System Under Test) info as seen by
Continued on next page
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2697 v4 @ 2.30GHz
  2 "physical id"s (chips)
  36 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 18
    siblings  : 18
    physical 0: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    physical 1: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    cache size : 46080 KB

From /proc/meminfo
  MemTotal: 528066204 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
  redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
  Linux DL380-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 26 09:51

SPEC is set to: /home/intel_binary/cpu2006
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda5 xfs 318G 144G 175G 46% /home

Additional information from dmidecode:

  Warning: Use caution when you interpret this section. The 'dmidecode' program
  reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to
  hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 05/05/2016
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Platform Notes (Continued)

Memory:
8x UNKNOWN NOT AVAILABLE
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/intel_binary/cpu2006/libs/32:/home/intel_binary/cpu2006/libs/64:/home/intel_binary/cpu2006/sh"
OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
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Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: basepeak = yes

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias

-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca
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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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