



SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

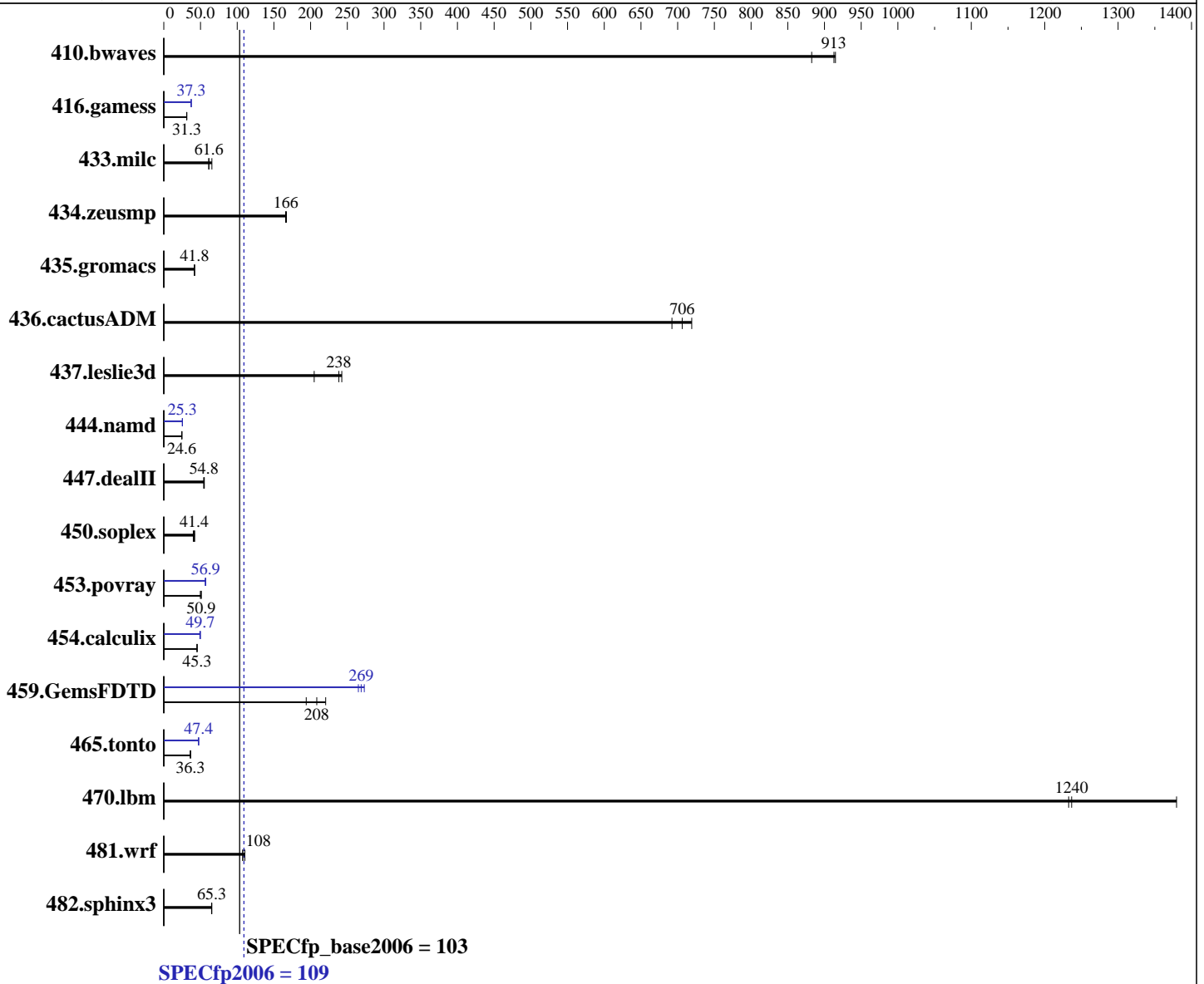
Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E5-4650 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: btrfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>14.9</u>	<u>913</u>	14.9	915	15.4	883	<u>14.9</u>	<u>913</u>	14.9	915	15.4	883
416.gamess	627	31.2	626	31.3	<u>627</u>	<u>31.3</u>	<u>524</u>	<u>37.3</u>	524	37.3	524	37.3
433.milc	140	65.5	<u>149</u>	<u>61.6</u>	150	61.1	140	65.5	<u>149</u>	<u>61.6</u>	150	61.1
434.zeusmp	54.8	166	54.5	167	<u>54.7</u>	<u>166</u>	54.8	166	54.5	167	<u>54.7</u>	<u>166</u>
435.gromacs	<u>171</u>	<u>41.8</u>	170	41.9	171	41.7	<u>171</u>	<u>41.8</u>	170	41.9	171	41.7
436.cactusADM	16.6	719	<u>16.9</u>	<u>706</u>	17.3	692	16.6	719	<u>16.9</u>	<u>706</u>	17.3	692
437.leslie3d	38.8	242	45.9	205	<u>39.4</u>	<u>238</u>	38.8	242	45.9	205	<u>39.4</u>	<u>238</u>
444.namd	<u>326</u>	<u>24.6</u>	326	24.6	326	24.6	<u>316</u>	<u>25.3</u>	316	25.4	317	25.3
447.dealII	210	54.4	207	55.2	<u>209</u>	<u>54.8</u>	210	54.4	207	55.2	<u>209</u>	<u>54.8</u>
450.soplex	208	40.2	<u>202</u>	<u>41.4</u>	198	42.1	208	40.2	<u>202</u>	<u>41.4</u>	198	42.1
453.povray	107	49.8	104	51.0	<u>104</u>	<u>50.9</u>	94.3	56.4	<u>93.6</u>	<u>56.9</u>	93.0	57.2
454.calculix	182	45.4	<u>182</u>	<u>45.3</u>	182	45.2	166	49.8	<u>166</u>	<u>49.7</u>	168	49.1
459.GemsFDTD	54.7	194	<u>50.9</u>	<u>208</u>	48.1	221	<u>39.5</u>	<u>269</u>	38.8	273	40.0	265
465.tonto	270	36.5	<u>271</u>	<u>36.3</u>	272	36.1	208	47.4	<u>208</u>	<u>47.4</u>	208	47.4
470.lbm	9.96	1380	<u>11.1</u>	<u>1240</u>	11.1	1230	9.96	1380	<u>11.1</u>	<u>1240</u>	11.1	1230
481.wrf	101	110	104	108	<u>104</u>	<u>108</u>	101	110	104	108	<u>104</u>	<u>108</u>
482.sphinx3	300	65.0	<u>299</u>	<u>65.3</u>	298	65.4	300	65.0	<u>299</u>	<u>65.3</u>	298	65.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Snoop Mode set to Opportunistic Snoop Broadcast
 Virtualization Technology disabled
 System Profile set to custom
 CPU Performance set to Maximum Performance
 C States set to Autonomous
 C1E disabled
 Energy Efficient Turbo disabled
 Uncore Frequency set to Dynamic
 Energy Efficiency Policy set to Performance

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Platform Notes (Continued)

Memory Patrol Scrub disabled
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-9j51 Mon May 9 07:15:31 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4650 v4 @ 2.20GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 14
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal: 529326752 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-9j51 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
Continued on next page
```



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Platform Notes (Continued)

run-level 3 May 9 01:26

SPEC is set to: /root/cpu2006-1.2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xfs	225G	11G	215G	5%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.0.1 03/31/2016

Memory:

16x 00AD00B300AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz

16x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz

16x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 109

PowerEdge M830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 103

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

Peak Optimization Flags (Continued)

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jun 28 17:30:11 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 28 June 2016.