## SPEC® CFP2006 Result

### Huawei

**Huawei RH2288H V3 (Intel Xeon E5-2699 v4)**

<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>NC</th>
</tr>
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<tbody>
<tr>
<td>SPECfp_base2006</td>
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**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Jan-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Mar-2016

### Software binaries

- 410.bwaves
- 416.gamess
- 433.milc
- 434.zeusmp
- 435.gromacs
- 436.cactusADM
- 437.leslie3d
- 444.namd
- 447.dealII
- 450.soplex
- 453.povray
- 454.calculix
- 459.GemsFDTD
- 465.tonto
- 481.wrf
- 482.sphinx3

### Notes

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon E5-2699 v4</td>
<td>Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64</td>
</tr>
<tr>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz</td>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>CPU MHz: 2200</td>
<td>Auto Parallel: Yes</td>
</tr>
<tr>
<td>FPU: Integrated</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>CPU(s) orderable: 1.2 chip</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Secondary Cache: 256 KB I+D on chip per core</td>
<td>Other Software: None</td>
</tr>
<tr>
<td>L3 Cache: 55 MB I+D on chip per chip</td>
<td>Other Hardware: None</td>
</tr>
<tr>
<td>Other Cache: None</td>
<td></td>
</tr>
<tr>
<td>Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
<td></td>
</tr>
<tr>
<td>Disk Subsystem: 1 x 500 GB SATA, 7200 RPM</td>
<td></td>
</tr>
<tr>
<td>Other Hardware: None</td>
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</tbody>
</table>

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Non-Compliant
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2699 v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
CPU2006 license: 3175
Test sponsor: Huawei

Test date: Jun-2016
Hardware Availability: Mar-2016
Test date: Jun-2016
Hardware Availability: Mar-2016

Tested by: Huawei
Software Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

Results Table

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<tr>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25#$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Wed Jun 1 02:27:10 2016

Continued on next page
Huawei RH2288H V3 (Intel Xeon E5-2699 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016
Test date: Jun-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
- physical id" (chips)
- 44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores : 22
- siblings : 22
- physical 0: cores 0 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
- physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
- cache size : 56320 KB

From /proc/meminfo
- MemTotal:       263566508 kB
- HugePages_Total:       0
- Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
- os release:
  - NAME="Red Hat Enterprise Linux Server"
  - VERSION="7.2 (Maipo)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="7.2"
  - PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
  - ANSI_COLOR="0;31"
  - CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
  - redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
  - system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
- Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 31 11:46
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability. Specifcaly, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
Huawei RH2288H V3 (Intel Xeon E5-2699 v4)

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
Huawei RH2288H V3 (Intel Xeon E5-2699 v4)

<table>
<thead>
<tr>
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**Base Optimization Flags (Continued)**

Benchmarks using both Fortran and C:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-parallel`  
- `-opt-prefetch`  
- `-ansi-alias`

**Peak Compiler Invocation**

- **C benchmarks:**  
  ```
  icc -m64
  ```
- **C++ benchmarks:**  
  ```
  icpc -m64
  ```
- **Fortran benchmarks:**  
  ```
  ifort -m64
  ```

Benchmarks using both Fortran and C:
- **C benchmarks:**  
  ```
  icc -m64 ifort -m64
  ```

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
- **433.milc:**  
  ```
  basepeak = yes
  ```
- **701.lbm:**  
  ```
  basepeak = yes
  ```
- **482.sphinx3:**  
  ```
  basepeak = yes
  ```

C++ benchmarks:
- **444.namd:**  
  ```
  -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
  -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
  -auto-ilp32
  ```  
  Continued on next page
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2699 v4)

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

Continued on next page
Huawei RH2288H V3 (Intel Xeon E5-2699 v4) SPECfp2006 = NC
SPECfp_base2006 = NC

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml