## SPEC® CINT2006 Result

**Fujitsu**

PRIMERGY CX2550 M2, Intel Xeon E5-2697 v4, 2.30 GHz

**SPECint**\_rate2006 = 1570  
**SPECint\_rate\_base2006 = 1510**

<table>
<thead>
<tr>
<th>Test sponsor</th>
<th>Fujitsu</th>
<th>Tested by</th>
<th>Fujitsu</th>
<th>Test date</th>
<th>May-2016</th>
<th>Hardware Availability</th>
<th>Apr-2016</th>
<th>Software Availability</th>
<th>Sep-2015</th>
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<td><strong>CPU Name:</strong></td>
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<td><strong>CPU Characteristics:</strong></td>
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<td><strong>Secondary Cache:</strong></td>
<td>256 KB I+D on chip per core</td>
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<td><strong>L3 Cache:</strong></td>
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<td><strong>Operating System:</strong></td>
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<td>Linux Kernel 3.12.49-11-default</td>
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<td><strong>Compiler:</strong></td>
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<td>Microquill SmartHeap V10.2</td>
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*Copyright 2006-2016 Standard Performance Evaluation Corporation*

*info@spec.org*  
*http://www.spec.org/*
Fujitsu

PRIMERGY CX2550 M2, Intel Xeon E5-2697 v4, 2.30 GHz

RESULTS TABLE

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</tbody>
</table>

RESULTS APPEAR IN THE ORDER IN WHICH THEY WERE RUN. BOLD UNDERLINED TEXT INDICATES A MEDIAN MEASUREMENT.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
  Energy Performance = Performance
  Utilization Profile = Unbalanced
  QPI snoop mode: Cluster on Die
  COD Enable = Enabled, Early Snoop = Disabled, Home Snoop Dir OSB = Disabled
  CPU C1E Support = Disabled

Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on CX2550M2 Fri May 20 11:00:00 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2697 v4 @ 2.30GHz
  2 "physical id"s (chips)  

Continued on next page
### Platform Notes (Continued)

72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 18
siblings  : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 23040 KB
```

From /proc/meminfo

```
MemTotal:       264315684 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 20 10:58 last=5
```

SPEC is set to: /home/SPECcpu2006

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 890G 3.7G 886G 1% /home
```

Additional information from dmidecode:

```
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.4.0 for D3343-B1x
```

Continued on next page
Fujitsu
PRIMERGY CX2550 M2, Intel Xeon E5-2697 v4, 2.30 GHz

| SPECint_rate2006 = 1570 |
| SPECint_rate_base2006 = 1510 |

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu  
Test date: May-2016  
Hardware Availability: Apr-2016  
Software Availability: Sep-2015

Platform Notes (Continued)

03/17/2016  
Memory:  
16x Hyundai Electronics (Hynix) HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz  
(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop_caches  
runcsp command invoked through numactl i.e.:  
umactl --interleave=all runspec <etc>  
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:  
```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:  
```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32  
401.bzip2: -D_FILE_OFFSET_BITS=64  
403.gcc: -D_FILE_OFFSET_BITS=64  
429.mcf: -D_FILE_OFFSET_BITS=64  
445.gobmk: -D_FILE_OFFSET_BITS=64  
456.hmmer: -D_FILE_OFFSET_BITS=64  
458.sjeng: -D_FILE_OFFSET_BITS=64  
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX  
464.h264ref: -D_FILE_OFFSET_BITS=64  
471.omnetpp: -D_FILE_OFFSET_BITS=64  
473.astar: -D_FILE_OFFSET_BITS=64  
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```
**Fujitsu**

**PRIMERGY CX2550 M2, Intel Xeon E5-2697 v4, 2.30 GHz**

**SPEC CINT2006 Result**

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<th>May-2016</th>
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<td>Hardware Availability</td>
<td>Apr-2016</td>
</tr>
</tbody>
</table>

**SPECint_rate2006 = 1570**

**SPECint_rate_base2006 = 1510**

**Base Optimization Flags**

C benchmarks:
- `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-opt-prefetch`
- `-opt-mem-layout-trans=3`

C++ benchmarks:
- `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-opt-prefetch`
- `-opt-mem-layout-trans=3` `-W1,-z,muldefs` `-L/sh` `-lsmartheap`

**Base Other Flags**

C benchmarks:
- `403.gcc`: `-Dalloca=_alloca`

**Peak Compiler Invocation**

C benchmarks (except as noted below):
- `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`
  - `400.perlbench`: `icc -m64`
  - `401.bzip2`: `icc -m64`
  - `456.hmmer`: `icc -m64`
  - `458.sjeng`: `icc -m64`

C++ benchmarks:
- `icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

**Peak Portability Flags**

400.perlbench: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64` `-DSPEC_CPU_LINUX_X64`
401.bzip2: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
403.gcc: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
429.mcf: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
445.gobmk: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
456.hmmer: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
458.sjeng: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
462.libquantum: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LP64`
464.h264ref: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LINUX`
471.omnetpp: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LINUX`
473.astar: `-D_FILE_OFFSET_BITS=64` `-DSPEC_CPU_LINUX`
Fujitsu
PRIMERGY CX2550 M2, Intel Xeon E5-2697 v4, 2.30 GHz

SPECint_rate2006 = 1570
SPECint_rate_base2006 = 1510

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2016
Hardware Availability: Apr-2016
Software Availability: Sep-2015

Peak Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
   -ipo(pass 2) -o3(pass 2) -no-prec-div(pass 2)
   -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
   -ipo(pass 2) -o3(pass 2) -no-prec-div(pass 2)
   -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
   -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -o3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
   -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
   -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -o3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
   -ipo(pass 2) -o3(pass 2) -no-prec-div(pass 2)
   -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
   -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
   -ipo(pass 2) -o3(pass 2) -no-prec-div(pass 2)
   -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
   -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
   -ipo(pass 2) -o3(pass 2) -no-prec-div(pass 2)
   -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
   -opt-ra-region-strategy=block -Wl,-z,muldefs
   -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page
Fujitsu
PRIMERGY CX2550 M2, Intel Xeon E5-2697 v4, 2.30 GHz

SPECint_rate2006 = 1570
SPECint_rate_base2006 = 1510

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2016
Hardware Availability: Apr-2016
Software Availability: Sep-2015

Peak Optimization Flags ( Continued )
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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