SPEC® CFP2006 Result

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NEC Corporation

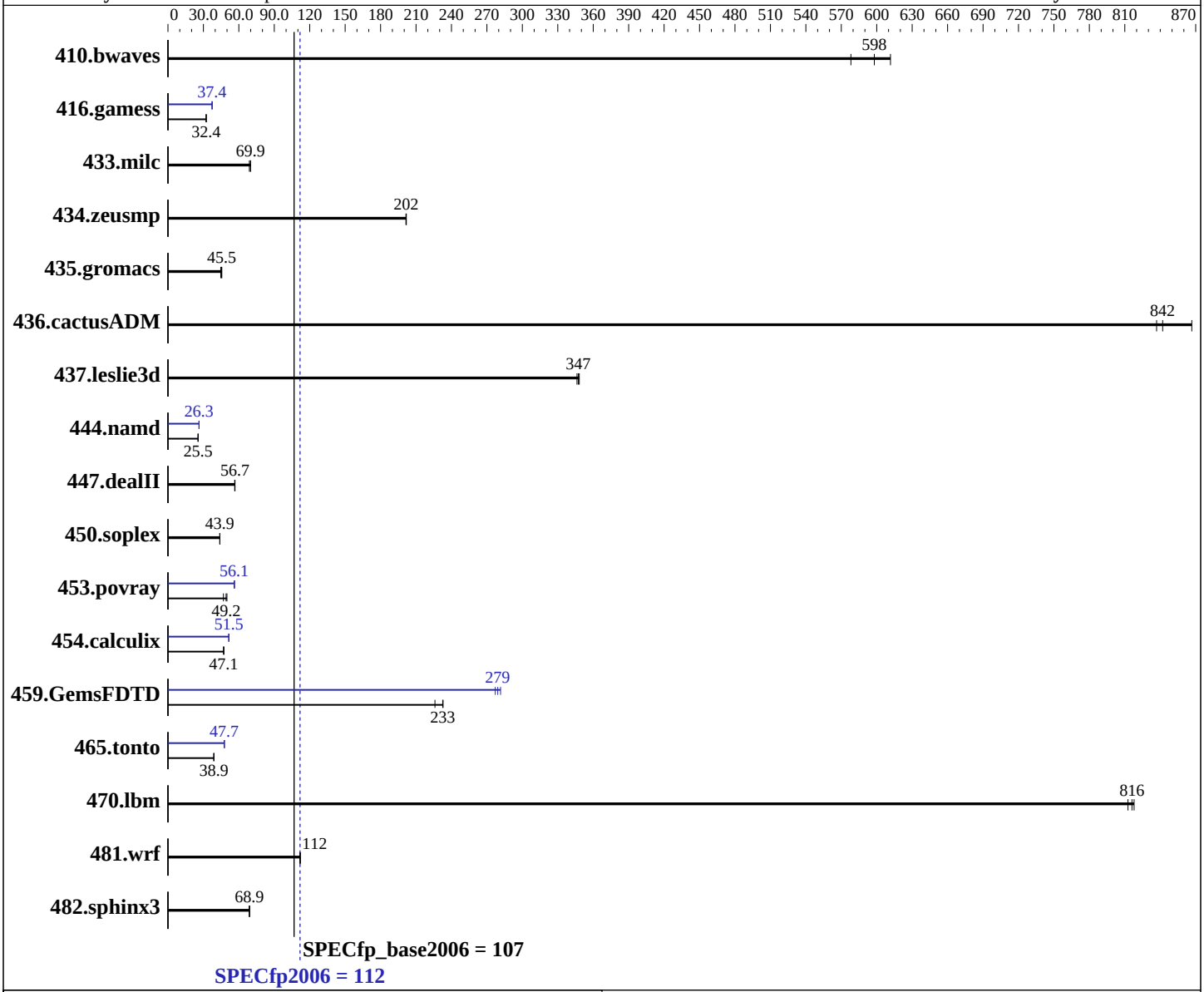
Express5800/R120g-1M (Intel Xeon E5-2650 v4)

SPECfp®2006 = 112

SPECfp_base2006 = 107

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: May-2016
Hardware Availability: Apr-2016
Software Availability: Jan-2016



Hardware

CPU Name:	Intel Xeon E5-2650 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) Kernel 3.10.0-327.4.5.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.2	612	23.5	578	**22.7**	**598**	22.2	612	23.5	578	**22.7**	**598**
416.gamess	606	32.3	**604**	**32.4**	604	32.4	**524**	**37.4**	524	37.3	523	37.4
433.milc	131	69.9	**131**	**69.9**	133	68.9	131	69.9	**131**	**69.9**	133	68.9
434.zeusmp	45.2	202	45.1	202	**45.1**	**202**	45.2	202	45.1	202	**45.1**	**202**
435.gromacs	160	44.7	**157**	**45.5**	157	45.5	160	44.7	**157**	**45.5**	157	45.5
436.cactusADM	14.3	837	13.8	867	**14.2**	**842**	14.3	837	13.8	867	**14.2**	**842**
437.leslie3d	27.1	346	**27.1**	**347**	27.0	348	27.1	346	**27.1**	**347**	27.0	348
444.namd	314	25.5	315	25.5	**314**	**25.5**	305	26.3	305	26.3	**305**	**26.3**
447.dealII	**202**	**56.7**	202	56.8	203	56.5	**202**	**56.7**	202	56.8	203	56.5
450.soplex	190	44.0	190	43.9	**190**	**43.9**	190	44.0	190	43.9	**190**	**43.9**
453.povray	107	49.9	**108**	**49.2**	113	46.9	**94.8**	**56.1**	95.0	56.0	94.3	56.4
454.calculix	175	47.1	174	47.3	**175**	**47.1**	160	51.6	160	51.5	**160**	**51.5**
459.GemsFDTD	45.6	233	46.9	226	**45.6**	**233**	38.3	277	**38.0**	**279**	37.7	282
465.tonto	252	39.0	254	38.8	**253**	**38.9**	206	47.7	206	47.7	**206**	**47.7**
470.lbm	16.8	818	16.9	813	**16.8**	**816**	16.8	818	16.9	813	**16.8**	**816**
481.wrf	**99.7**	**112**	99.7	112	100	112	**99.7**	**112**	99.7	112	100	112
482.sphinx3	283	68.9	282	69.1	**283**	**68.9**	283	68.9	282	69.1	**283**	**68.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Snoop Mode: Home Snoop with Directory
 Hyper-Threading: Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1

 The Express5800/R120g-1M (Intel Xeon E5-2650 v4) and
 the Express5800/R120g-2M (Intel Xeon E5-2650 v4) models are electronically equivalent.
 The results have been measured on the Express5800/R120g-2M (Intel Xeon E5-2650 v4) model.

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.xml`

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For other inquiries, please contact webmaster@spec.org.

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