# SPEC® CINT2006 Result



Hewlett Packard Enterprise

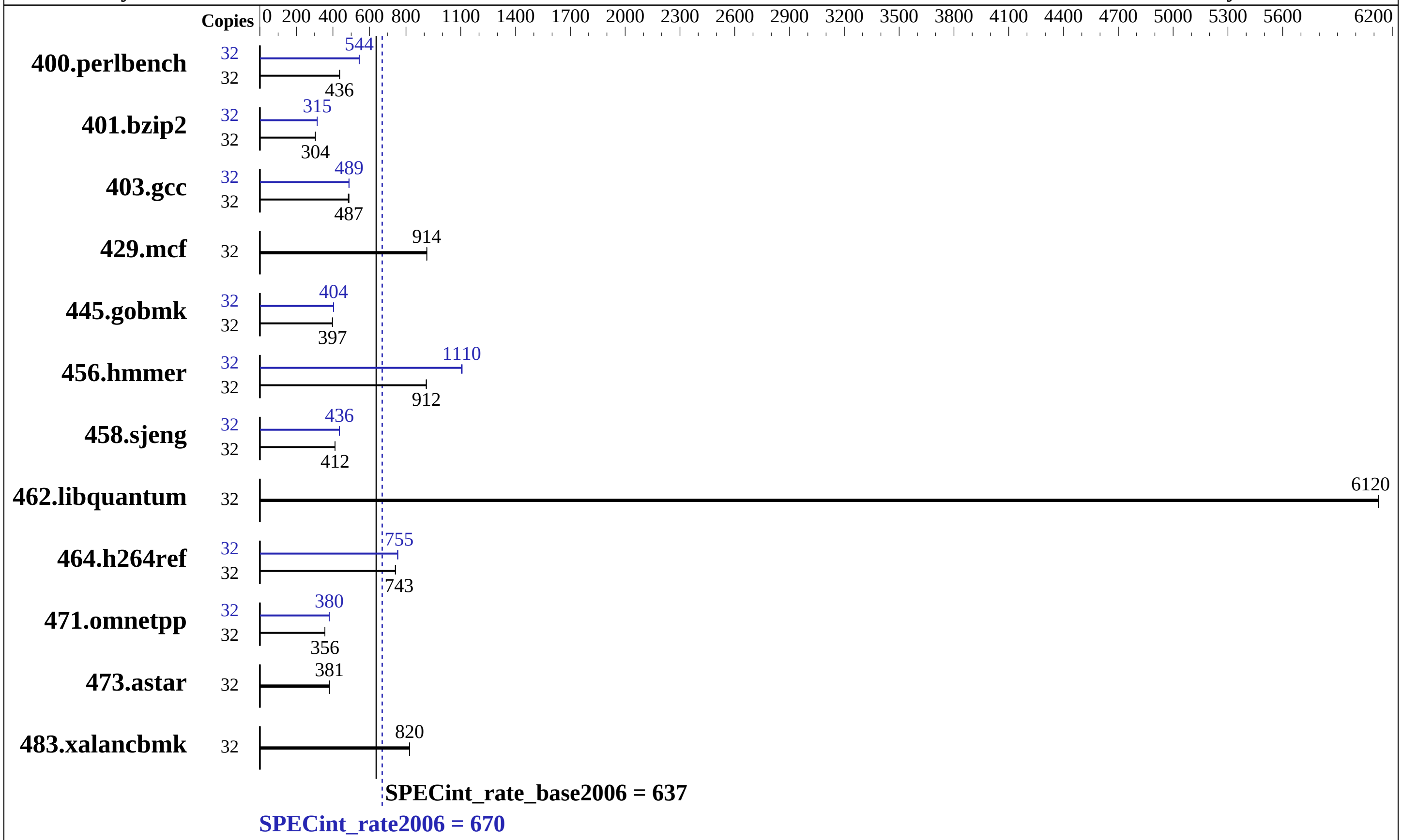
(Test Sponsor: HPE)

ProLiant XL450 Gen9
(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint®_rate2006 = 670

SPECint_rate_base2006 = 637

| | | | |
|---|---|---|---|
| **CPU2006 license:** 3 | | **Test date:** | Apr-2016 |
| **Test sponsor:** | HPE | **Hardware Availability:** | Mar-2016 |
| **Tested by:** | HPE | **Software Availability:** | Dec-2015 |




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2620 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.00 GHz |
| CPU MHz: | 2100 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (8 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz) |
| Disk Subsystem: | 2 x 480 GB SATA SSD, RAID 1 |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 (x86_64) SP1 Kernel 3.12.49-11-default |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | btrfs |
| System State: | Run level 5 (multi-user w/GUI) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.2 |

# SPEC CINT2006 Result



Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant XL450 Gen9
(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint_rate2006 = 670

SPECint_rate_base2006 = 637

**CPU2006 license:** 3
**Test sponsor:** HPE
**Tested by:** HPE

**Test date:** Apr-2016
**Hardware Availability:** Mar-2016
**Software Availability:** Dec-2015

## Results Table

| | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **Benchmark** | **Copies** | **Seconds** | **Ratio** | **Seconds** | **Ratio** | **Seconds** | **Ratio** | **Copies** | **Seconds** | **Ratio** | **Seconds** | **Ratio** | **Seconds** | **Ratio** |
| 400.perlbench | 32 | 717 | 436 | **717** | **436** | 714 | 438 | 32 | 576 | 543 | **575** | **544** | 574 | 545 |
| 401.bzip2 | 32 | **1015** | **304** | 1017 | 304 | 1014 | 304 | 32 | 983 | 314 | 982 | 315 | **982** | **315** |
| 403.gcc | 32 | 532 | 484 | 527 | 488 | **529** | **487** | 32 | 529 | 487 | **527** | **489** | 527 | 489 |
| 429.mcf | 32 | 319 | 914 | **319** | **914** | 319 | 913 | 32 | 319 | 914 | **319** | **914** | 319 | 913 |
| 445.gobmk | 32 | 844 | 398 | **845** | **397** | 846 | 397 | 32 | 831 | 404 | **832** | **404** | 832 | 403 |
| 456.hmmer | 32 | 328 | 910 | 327 | 912 | **327** | **912** | 32 | 271 | 1100 | **270** | **1110** | 269 | 1110 |
| 458.sjeng | 32 | 940 | 412 | 941 | 411 | **940** | **412** | 32 | 890 | 435 | 888 | 436 | **889** | **436** |
| 462.libquantum | 32 | 108 | 6130 | 108 | 6120 | **108** | **6120** | 32 | 108 | 6130 | 108 | 6120 | **108** | **6120** |
| 464.h264ref | 32 | 956 | 741 | **953** | **743** | 952 | 744 | 32 | 937 | 756 | **937** | **755** | 940 | 753 |
| 471.omnetpp | 32 | **561** | **356** | 561 | 357 | 561 | 356 | 32 | 526 | 380 | **526** | **380** | 526 | 380 |
| 473.astar | 32 | 590 | 381 | **590** | **381** | 590 | 381 | 32 | 590 | 381 | **590** | **381** | 590 | 381 |
| 483.xalancbmk | 32 | **269** | **820** | 270 | 819 | 269 | 821 | 32 | **269** | **820** | 270 | 819 | 269 | 821 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
   echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
   echo 1 >      /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
   numactl --interleave=all runspec <etc>
```

## Platform Notes

```
BIOS Configuration:
   Power Profile set to Maximum Performance
   QPI Snoop Configuration set to Cluster on Die
   Collaborative Power Control set to Disabled
   Thermal Configuration set to Maximum Cooling
   Processor Power and Utilization Monitoring set to Disabled
   Memory Double Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-yzm1 Wed Apr 27 04:56:33 2016
```

Continued on next page

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant XL450 Gen9
(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint_rate2006 = 670
SPECint_rate_base2006 = 637

**CPU2006 license:** 3
**Test sponsor:** HPE
**Tested by:** HPE

**Test date:** Apr-2016
**Hardware Availability:** Mar-2016
**Software Availability:** Dec-2015

## Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
      2 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       131836564 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-yzm1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Apr 27 04:46

SPEC is set to: /cpu2006
   Filesystem     Type   Size  Used Avail Use% Mounted on
   /dev/sda3      btrfs  439G   16G  423G   4% /
```

Continued on next page

## Platform Notes (Continued)

```
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP U21 03/10/2016
   Memory:
    8x UNKNOWN NOT AVAILABLE
    8x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 2133 MHz

 (End of data from sysinfo program)
 Regarding the sysinfo display about the memory installed, the correct amount of
 memory is 128 GB and the dmidecode description should have one line reading as:
 8x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 2133 MHz
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
```

## Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

## Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64

Continued on next page

## Base Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

## Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64

Continued on next page

## Peak Portability Flags (Continued)

456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page

# Peak Optimization Flags (Continued)

471.omnetpp (continued):
`-L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue May 17 16:50:53 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 May 2016.