**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)

ProLiant DL380 Gen9  
(2.60 GHz, Intel Xeon E5-2623 v4)

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Mar-2016</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>HPE</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2016</td>
</tr>
<tr>
<td>Tested by:</td>
<td>HPE</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Nov-2015</td>
</tr>
</tbody>
</table>

**SPECfp®2006 = 95.4**

**SPECfp_base2006 = 91.8**

### CPU2006 license: 3

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E5-2623 v4</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2600</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>8 cores, 2 chips, 4 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1,2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 7.2, (Maipo)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel:</td>
<td>Yes</td>
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<tr>
<td>File System:</td>
<td>xfs</td>
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### Software Availability:

<table>
<thead>
<tr>
<th>410.bwaves</th>
<th>42.6</th>
</tr>
</thead>
<tbody>
<tr>
<td>416.gamess</td>
<td>35.9</td>
</tr>
<tr>
<td>433.milc</td>
<td>70.7</td>
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<tr>
<td>434.zeusmp</td>
<td>158</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>50.8</td>
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<tr>
<td>436.cactusADM</td>
<td></td>
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<tr>
<td>437.leslie3d</td>
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<td>447.dealII</td>
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<tr>
<td>450.soplex</td>
<td>38.6</td>
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<td>453.povray</td>
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<td>454.calculix</td>
<td>57.4</td>
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<td>459.GemsFDTD</td>
<td>52.9</td>
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<tr>
<td>465.tonto</td>
<td>53.1</td>
</tr>
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<td>470.lbm</td>
<td>43.9</td>
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<tr>
<td>481.wrf</td>
<td>84.2</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>72.6</td>
</tr>
</tbody>
</table>

**Continued on next page**
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.60 GHz, Intel Xeon E5-2623 v4)

SPECfp2006 = 95.4
SPECfp_base2006 = 91.8

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<td>41.4</td>
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<td>459.GemsFDTD</td>
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<td>268</td>
<td>72.6</td>
<td>270</td>
<td>72.3</td>
<td>268</td>
<td>72.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
QPI Snoop Configuration set to Home Snoop
Collaborative Power Control set to Disabled

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SPEC CFP2006 Result

Hewlett Packard Enterprise
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SPECfp2006 = 95.4
SPECfp_base2006 = 91.8

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Nov-2015

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2623 v4@ 2.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
  cache size : 10240 KB

From /proc/meminfo
 MemTotal:       528068684 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
 os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.2 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.2"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
Linux DL380Gen9allbin 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT
2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 27 21:31

SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
Continued on next page
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Platform Notes (Continued)
/dev/sda5  xfs  318G  78G  241G  25% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 03/10/2016
Memory:
8x UNKNOWN NOT AVAILABLE
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
OMP_NUM_THREADS = "8"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort  -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

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CPU2006 license: 3
Test date: Mar-2016
Test sponsor: HPE
Hardware Availability: Mar-2016
Tested by: HPE
Software Availability: Nov-2015

Base Portability Flags (Continued)

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Continued on next page
Peak Compiler Invocation (Continued)

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

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SPECfp2006 = 95.4
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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
                           -ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2)
                           -par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll2
                           -inline-level=0 -opt-prefetch -parallel
465.tonto: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
                           -ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2)
                           -par-num-threads=1 (pass 1) -prof-use (pass 2) -inline-calloc
                           -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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