SPEC® CFP2006 Result

Huawei
Huawei CH121 V3 (Intel Xeon E5-2650L v4)

SPECfp®2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

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Non-Compliant
Huawei

Huawei CH121 V3 (Intel Xeon E5-2650L v4)

<table>
<thead>
<tr>
<th>SPECfp2006</th>
<th>NC</th>
</tr>
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<tbody>
<tr>
<td>SPECfp_base2006</td>
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**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Feb-2016  
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<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Xeon E5-2650L v4</td>
<td><strong>Operating System:</strong> Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
</tr>
<tr>
<td><strong>CPU Characteristics:</strong> Intel Turbo Boost Technology up to 2.50 GHz</td>
<td><strong>Compiler:</strong> C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td><strong>CPU MHz:</strong> 1700</td>
<td><strong>Auto Parallel:</strong> Yes</td>
</tr>
<tr>
<td><strong>FPU:</strong> Integrated</td>
<td><strong>File System:</strong> ext4</td>
</tr>
<tr>
<td><strong>CPU(s) enabled:</strong> 28 cores, 2 chips, 14 cores/chip</td>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>CPU(s) orderable:</strong> 1, 2 chip</td>
<td><strong>Bit Pointers:</strong> 64-bit</td>
</tr>
<tr>
<td><strong>Primary Cache:</strong> 32 KB I + 32 KB D on chip per core</td>
<td><strong>Ped. Pointers:</strong> 32/64-bit</td>
</tr>
<tr>
<td><strong>Secondary Cache:</strong> 256 KB I+D on chip per core</td>
<td><strong>Other Software:</strong> None</td>
</tr>
<tr>
<td><strong>L3 Cache:</strong> 35 MB I+D on chip per chip</td>
<td><strong>Other Hardware:</strong> None</td>
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<tr>
<td><strong>Other Cache:</strong> None</td>
<td><strong>Memory:</strong> 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
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<tr>
<td><strong>Memory:</strong> 256 GB</td>
<td><strong>Disk Subsystem:</strong> 1 x 1 T SATA, 7200 RPM</td>
</tr>
<tr>
<td><strong>Disk Subsystem:</strong> None</td>
<td><strong>Other Cache:</strong> None</td>
</tr>
</tbody>
</table>

Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
Huawei CH121 V3 (Intel Xeon E5-2650L v4)

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Results Table

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<td>NC</td>
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<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /speccpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Sat Feb 27 18:17:57 2016

Continued on next page

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Huawei

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v4@ 1.70GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 3 4 5 7 8 9 10 11 12 13 14
cache size : 35840 KB

From /proc/meminfo
MemTotal: 263569784 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTL NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
Redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Feb 27 12:04
SPEC is set to: /speccpu/spec16

Continued on next page
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General Notes
Environment variable set at runtime before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

General Notes (Continued)

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### Base Compiler Invocation (Continued)

**Fortran benchmarks:**

```
ifort -m64
```

**Benchmarks using both Fortran and C:**

```
icc  -m64 ifort -m64
```

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>416.gamess</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>433.milc</td>
<td>-DSPEC_CPU_LP64</td>
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<td>453.povray</td>
<td>-DSPEC_CPU_LP64</td>
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<td>454.calculix</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
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<tr>
<td>459.GemsFDTD</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>465.tonto</td>
<td>-DSPEC_CPU_LP64</td>
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<td>470.lbm</td>
<td>-DSPEC_CPU_LP64</td>
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<td>481.wrf</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
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<tr>
<td>487.sphinx</td>
<td>-DSPEC_CPU_LP64</td>
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</table>

### Base Optimization Flags

**C benchmarks:**

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

**C++ benchmarks:**

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

**Fortran benchmarks:**

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```
## SPEC CFP2006 Result

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<th>Benchmark</th>
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<tr>
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### Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -parallel -opt-prefetch`  
- `-ansi-alias`

### Peak Compiler Invocation

- **C benchmarks:** `icc -m64`
- **C++ benchmarks:** `icpc -m64`
- **Fortran benchmarks:** `ifort -m64`

Benchmarks using both Fortran and C:
- `icc -m64 ifort -m64`

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

- **C benchmarks:**
  - `433.milc: basepeak = yes`
  - `701.lbm: basepeak = yes`
  - `482.sphinx3: basepeak = yes`

- **C++ benchmarks:**
  - `444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-llp32`

Non-compliant
Huawei

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2 (pass 2) -prof-gen: threadsafe (pass 1)
 -ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
 -par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll4
 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2 (pass 2) -prof-gen: threadsafe (pass 1)
 -ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
 -par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll2
 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2 (pass 2) -prof-gen: threadsafe (pass 1)
 -ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
 -par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll2
 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2 (pass 2) -prof-gen: threadsafe (pass 1)
 -ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
 -par-num-threads=1 (pass 1) -prof-use (pass 2) -inline-calloc
 -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -03 -no-prec-div -auto-ilp32 -ansi-alias

Non-compliant
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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags source by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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