SPEC® CFP2006 Result

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NEC Corporation

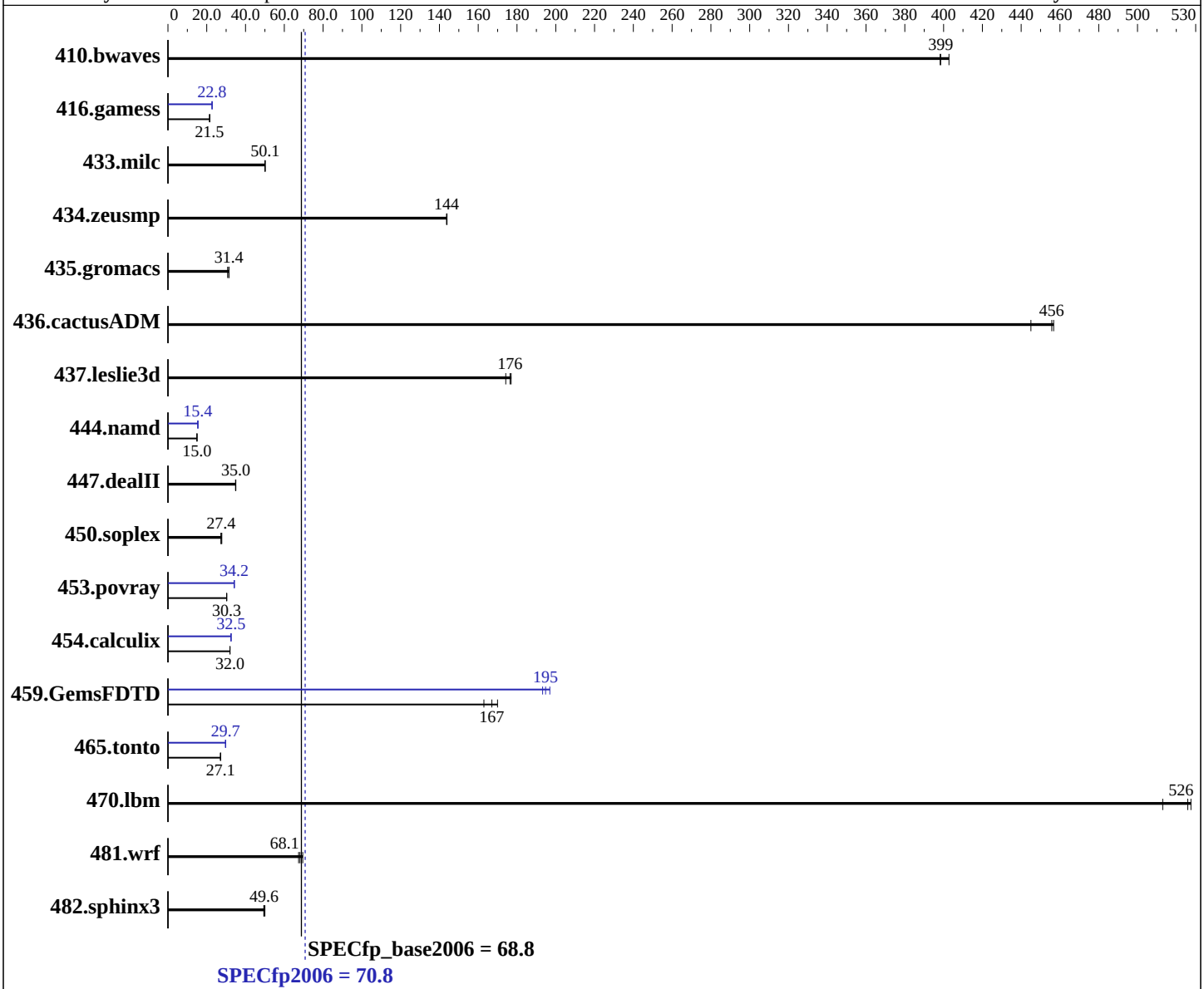
Express5800/R120g-2E (Intel Xeon E5-2603 v4)

SPECfp®2006 = 70.8

SPECfp_base2006 = 68.8

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Mar-2016
Hardware Availability: Apr-2016
Software Availability: Jan-2016



Hardware

CPU Name:	Intel Xeon E5-2603 v4
CPU Characteristics:	
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) Kernel 3.10.0-327.4.5.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 1866 MHz)
Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.7	403	**34.1**	**399**	34.1	398	33.7	403	**34.1**	**399**	34.1	398
416.gamess	911	21.5	911	21.5	**911**	**21.5**	860	22.8	861	22.8	**860**	**22.8**
433.milc	183	50.1	**183**	**50.1**	183	50.0	183	50.1	**183**	**50.1**	183	50.0
434.zeusmp	**63.3**	**144**	63.3	144	63.2	144	**63.3**	**144**	63.3	144	63.2	144
435.gromacs	232	30.8	227	31.4	**227**	**31.4**	232	30.8	227	31.4	**227**	**31.4**
436.cactusADM	26.2	457	**26.2**	**456**	26.9	445	26.2	457	**26.2**	**456**	26.9	445
437.leslie3d	54.0	174	**53.3**	**176**	53.1	177	54.0	174	**53.3**	**176**	53.1	177
444.namd	536	15.0	**536**	**15.0**	536	15.0	520	15.4	**520**	**15.4**	520	15.4
447.dealII	**327**	**35.0**	328	34.8	327	35.0	**327**	**35.0**	328	34.8	327	35.0
450.soplex	**304**	**27.4**	300	27.8	305	27.4	**304**	**27.4**	300	27.8	305	27.4
453.povray	176	30.2	175	30.4	**176**	**30.3**	**156**	**34.2**	156	34.1	155	34.2
454.calculix	**258**	**32.0**	258	32.0	258	32.0	254	32.5	254	32.5	**254**	**32.5**
459.GemsFDTD	62.4	170	65.1	163	**63.5**	**167**	53.9	197	**54.5**	**195**	54.9	193
465.tonto	362	27.2	363	27.1	**363**	**27.1**	**331**	**29.7**	332	29.7	331	29.8
470.lbm	**26.1**	**526**	26.0	528	26.8	513	**26.1**	**526**	26.0	528	26.8	513
481.wrf	**164**	**68.1**	161	69.5	166	67.4	**164**	**68.1**	161	69.5	166	67.4
482.sphinx3	**393**	**49.6**	390	50.0	394	49.4	**393**	**49.6**	390	50.0	394	49.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Snoop Mode: Home Snoop with Directory
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "12"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevB.xml

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For other inquiries, please contact webmaster@spec.org.

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