Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

<table>
<thead>
<tr>
<th>Copy</th>
<th>410.bwaves</th>
<th>416.gamess</th>
<th>433.milc</th>
<th>434.zeusmp</th>
<th>435.gromacs</th>
<th>436.cactusADM</th>
<th>437.leslie3d</th>
<th>444.namd</th>
<th>447.dealII</th>
<th>450.soplex</th>
<th>453.povray</th>
<th>454.calculix</th>
<th>459.GemsFDTD</th>
<th>465.tonto</th>
<th>470.lbm</th>
<th>481.wrf</th>
<th>482.sphinx3</th>
</tr>
</thead>
</table>

### SPEC Frequently Asked Questions (FAQ)

**SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules.** Specifically, the memory was not available as required by the SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

---

**Copies Non-Compliant**
Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon E5-2667 v4</td>
<td>Operating System: Red Hat Enterprise Linux Server release 7.0 (el7)</td>
</tr>
<tr>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz</td>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>CPU MHz: 3200</td>
<td>Auto Parallel: No</td>
</tr>
<tr>
<td>FPU: Integrated</td>
<td>File System: ext4</td>
</tr>
<tr>
<td>CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>CPU(s) orderable: 1.2 chip</td>
<td>Base Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
<td>Peak Pointers: 32/64-bit</td>
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<tr>
<td>Secondary Cache: 256 KB I+D on chip per core</td>
<td>Other Software: None</td>
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<td>L3 Cache: 25 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache: None</td>
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<tr>
<td>Memory: 256 GB (16 x 16 GB 2Rx4 P2-2400T)</td>
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</tr>
<tr>
<td>Disk Subsystem: 1 x 1000 GB SATA, 7200rpm</td>
<td></td>
</tr>
<tr>
<td>Other Hardware: None</td>
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</tbody>
</table>

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability. This result is non-compliant.
SPEC CFP2006 Result

Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.<a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and<a href="https://www.spec.org/osg/policy.html#AppendixC">SPEC CPU rule 1.3.2</a>.

Results Table

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<tr>
<th>Benchmark</th>
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<tr>
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<td>32</td>
<td>NC</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Non-Compliant
Huawei
Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b1385532ceab812e28219e1

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2667 v4@ 3.20GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 2 3 4 8 10 11 12
physical 1: cores 0 2 3 4 8 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal:       263569324 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Continued on next page
Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

** SPECfp_rate2006 = NC **
** SPECfp_rate_base2006 = NC **

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>3175</th>
</tr>
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<tbody>
<tr>
<td>Test date:</td>
<td>Mar-2016</td>
</tr>
<tr>
<td>Test sponsor:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2016</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Mar-2016</td>
</tr>
</tbody>
</table>

** SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability. **

** Platform Notes (Continued) **

```
uname -a:
    Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
    EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Mar 25 05:47
```

** SPEC is set to: /spec16**

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>ext4</td>
<td>913G</td>
<td>108G</td>
<td>759G</td>
<td>13%</td>
<td>/</td>
</tr>
</tbody>
</table>

** Additional information from dmidecode: **

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

** BIOS Insyde Corp. 3.09 02/22/2016**

** Memory: **
- 8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
- 8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

** General Notes **

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = /spec16/libs/32:/spec16/libs/64:/spec16/sh
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB RedHat EL 7.1

Transparent Huge Pages enabled with:
```
echo always > /sys/kernel/mm/transient_hugepage/enabled
```

Filesystem page cache cleared with:
```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:
```
numactl --interleave=all runspec <etc>
```
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td><code>-DSPEC_CPU_LP64 -nofor_main</code></td>
</tr>
<tr>
<td>416.gamess</td>
<td><code>-DSPEC_CPU_LP64 -nofor_main</code></td>
</tr>
<tr>
<td>433.milc</td>
<td><code>-DSPEC_CPU_LP64</code></td>
</tr>
<tr>
<td>434.zeusmp</td>
<td><code>-DSPEC_CPU_LP64</code></td>
</tr>
<tr>
<td>435.gromacs</td>
<td><code>-DSPEC_CPU_LP64 -nofor_main</code></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td><code>-DSPEC_CPU_LP64 -nofor_main</code></td>
</tr>
<tr>
<td>437.leslie3d</td>
<td><code>-DSPEC_CPU_LP64</code></td>
</tr>
<tr>
<td>444.namd</td>
<td><code>-DSPEC_CPU_LP64 -nofor_main</code></td>
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<td><code>-DSPEC_CPU_LP64</code></td>
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<tr>
<td>459.GemsFDTD</td>
<td><code>-DSPEC_CPU_LP64 -nofor_main</code></td>
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<tr>
<td>465.tonto</td>
<td><code>-DSPEC_CPU_LP64</code></td>
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<tr>
<td>470.lbm</td>
<td><code>-DSPEC_CPU_LP64</code></td>
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<td>481.wrf</td>
<td><code>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</code></td>
</tr>
<tr>
<td>482.sphinx3</td>
<td><code>-DSPEC_CPU_LP64</code></td>
</tr>
</tbody>
</table>

### Base Optimization Flags

- **C benchmarks:**
  - `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`
Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

**SPECfp_rate2006 = NC**

**SPECfp_rate_base2006 = NC**

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

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**Base Optimization Flags (Continued)**

C++ benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ipo -O3 -no-prec-div -opt-prefetch
- -ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
- -ansi-alias -opt-mem-layout-trans=3

**Peak Compiler Invocation**

C benchmarks:
icc   -m64

C++ benchmarks (except as noted below):
icpc   -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort  -m64

**Peak Portability Flags**

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

Continued on next page

Non-Compliant
Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Test date: Mar-2016
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Peak Portability Flags (Continued)

450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

Non-Compliant
SPEC CFP2006 Result

Huawei
Huawei RH1288 V3 (Intel Xeon E5-2667 v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
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<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>NC</td>
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</table>

CPU2006 license: 3175  
Test sponsor: Huawei  
Test date: Mar-2016  
Hardware Availability: Mar-2016  
Software Availability: Mar-2016  
Tested by: Huawei  
Test sponsor: Huawei  
Software Availability: Mar-2016

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Peak Optimization Flags (Continued)

| Benchmark | Flags
|-----------|--------------------------------------------------|
| 410.bwaves | basepeak = yes
| 416.gamess | -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-
| 434.zeusmp | basepeak = yes
| 437.leslie3d | basepeak = yes
| 459.GemsFDTD | basepeak = yes
| 465.tonto | -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3
| 435.gromacs | -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32
| 436.cactusADM | basepeak = yes
| 437.calculix | basepeak = yes
| 481.wrf | basepeak = yes

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
Non-Compliant

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.