Huawei CH222 V3 (Intel Xeon E5-2650 v4)

SPECfp®_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Copies

410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
465.tonto
470.lbm
481.wrf
482.sphinx3

Non-Compliant
Huawei

**Huawei CH222 V3 (Intel Xeon E5-2650 v4)**

| SPECfp_rate2006 | NC |
| SPECfp_rate_base2006 | NC |

**CPU2006 license:** 3175  
**Test date:** Feb-2016  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Hardware Availability:** Mar-2016  
**Software Availability:** Mar-2016

**SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).**

**Hardware**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5-2650 v4</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 2.90 GHz</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2200</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>24 cores, 2 chips, 12 cores/chip, 2 threads/core</td>
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<tr>
<td>CPU(s) orderable</td>
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<tr>
<td>Primary Cache</td>
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<tr>
<td>Secondary Cache</td>
<td>256 KB I+D on chip per core</td>
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<tr>
<td>L3 Cache</td>
<td>30 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache</td>
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<tr>
<td>Memory</td>
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<td>Disk Subsystem</td>
<td>1 x 1 T SATA, 7200 RPM</td>
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<tr>
<td>Other Hardware</td>
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**Software**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
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</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel</td>
<td>No</td>
</tr>
<tr>
<td>File System</td>
<td>ext4</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td>Base Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software</td>
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Huawei CH222 V3 (Intel Xeon E5-2650 v4)

SPEC fp_rate2006 = NC
SPEC fp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>Base</td>
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<tr>
<td>410.bwaves</td>
<td>48</td>
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<td>NC</td>
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<td>433.milc</td>
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<td>434.zeusmp</td>
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<td>459.GemsFDTD</td>
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<td>465.tonto</td>
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<td>NC</td>
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<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"
Huawei

Huawei CH222 V3 (Intel Xeon E5-2650 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Sysinfo program /speccpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb866 d9a2859b2ceab81e28219e1
running on localhost.localdomain Fri Feb 19 01:34:26 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblions (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblions : 12
physical 0: cores 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 2 3 4 5 8 9 10 11 12 13
cached size : 15360 KB

From /proc/meminfo
MemTotal:       263566960 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Continued on next page
Huawei

Huawei CH222 V3 (Intel Xeon E5-2650 v4)

SPECCFp_rate2006 = NC
SPECCf_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

CPU2006 license: 3175
Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPECFP2006 Result

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes (Continued)

uname -a:
    Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Wed May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 17 06:42

SPEC is set to: /speccpu/spec16
    Filesystem Type Size  Used Avail Use% Mounted on
    /dev/sda2 ext4  865G   94G  728G  12% /

Additional information from dmidecode:
    Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Insyde Corp. 7.11 02/11/2016
    Memory:
    8x NO DIMM NO DIMM 3 rank
    8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
    8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
File system page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model

Non-Compliant
## SPEC CFP2006 Result

### Huawei

**Huawei CH222 V3 (Intel Xeon E5-2650 v4)**

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
<th>NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>NC</td>
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</tbody>
</table>

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Feb-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Mar-2016

**Tested by:** Huawei  
**Software Availability:** Mar-2016  
**Base Compiler Invocation**

- C benchmarks:
  - `icc -m64`
- C++ benchmarks:
  - `icpc -m64`
- Fortran benchmarks:
  - `ifort -m64`
- Benchmarks using both Fortran and C:
  - `icc -m64 ifort -m64`

**Base Portability Flags**

- `410.bwaves`: `-DSPEC_CPU_LP64`
- `416.gamess`: `-DSPEC_CPU_LP64`
- `433.milc`: `-DSPEC_CPU_LP64`
- `434.zeusmp`: `-DSPEC_CPU_LP64`
- `435.gromacs`: `-DSPEC_CPU_LP64 -nofor_main`
- `436.cactusADM`: `-DSPEC_CPU_LP64 -nofor_main`
- `437.leslie3d`: `-DSPEC_CPU_LP64`
- `444.namd`: `-DSPEC_CPU_LP64 -nofor_main`
- `447.dealII`: `-DSPEC_CPU_LP64`
- `450.soplex`: `-DSPEC_CPU_LP64`
- `453.povray`: `-DSPEC_CPU_LP64`
- `454.calculix`: `-DSPEC_CPU_LP64 -nofor_main`
- `459.GemsFTD`: `-DSPEC_CPU_LP64`
- `465.tonto`: `-DSPEC_CPU_LP64`
- `470.lbm`: `-DSPEC_CPU_LP64`
- `481.wrf`: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- `482.sphinx3`: `-DSPEC_CPU_LP64`

**Base Optimization Flags**

- C benchmarks:
  - `-xCORE-AVX2`
  - `-ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
  - `-ansi-alias -opt-mem-layout-trans=3`

---

**Non-Compliant**

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SPEC CFP2006 Result

Huawei

Huawei CH222 V3 (Intel Xeon E5-2650 v4)

SPECFp_rate2006 = NC
SPECFp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ipo-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks (except as noted below):
icpc  -m64

450.soplex: icpc -o 450.soplex -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

Non-Compliant
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**Peak Portability Flags (Continued)**

- 450.soplex: -D_FILE_OFFSET_BITS=64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

**Peak Optimization Flags**

**C benchmarks:**

- 433.milc: basepeak = yes
- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

**C++ benchmarks:**

- 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
  -prof-use(pass 2) -fno-alias -auto-ilp32
- 447.dealII: basepeak = yes
- 450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
  -prof-use(pass 2) -opt-malloc-options=3
- 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
  -prof-use(pass 2) -unroll4 -ansi-alias

**Fortran benchmarks:**

Continued on next page
**SPEC CFP2006 Result**

**Huawei**

Huawei CH222 V3 (Intel Xeon E5-2650 v4)

- **SPECfp_rate2006 = NC**
- **SPECfp_rate_base2006 = NC**

**CPU2006 license:** 3175  
**Test date:** Feb-2016

**Test sponsor:** Huawei  
**Hardware Availability:** Mar-2016

**Tested by:** Huawei  
**Software Availability:** Mar-2016

**Peak Optimization Flags (Continued)**

410.bwaves: basepeak = yes

416.gamess:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

**Benchmarks using both Fortran and C:**

435.gromacs:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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